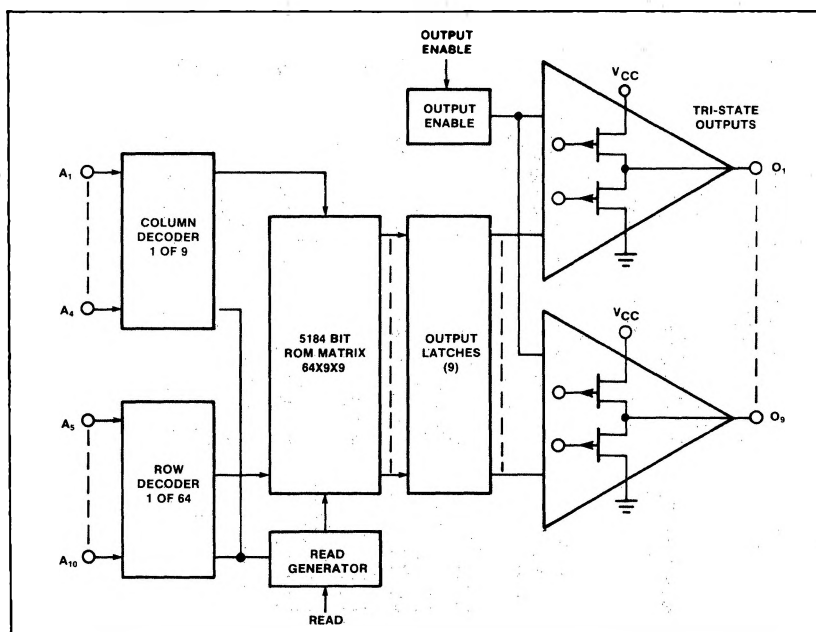
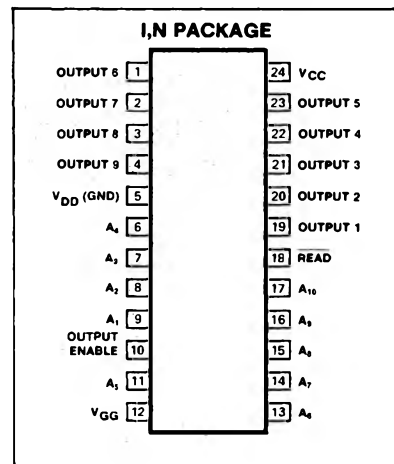


DESCRIPTION

The 2526 high speed ROM may be organized as 64X9X9 for use as a character generator, or as a 512X9 ROM for general purpose use. A read input controls the entry of data from the ROM into output latches. Three-state outputs allow OR-tying for implementing large memories. Output enable controls the 9 output devices without affecting address circuitry.

BLOCK DIAGRAM**PIN CONFIGURATION****ABSOLUTE MAXIMUM RATINGS¹**

PARAMETER		RATING	UNIT
TA	Temperature range		°C
	Operating	0 to 70	
TSTG	Storage	-65 to 150	
	Power dissipation at 70°C ²	730	mW
Input and supply voltages with respect to VCC ³		0.3 to -20	V

DC ELECTRICAL CHARACTERISTICS $T_A = 0^\circ\text{C to } 70^\circ\text{C}$, $V_{CC} = 5\text{V} \pm 5\%$, $V_{GG} = -12\text{V} \pm 5\%$, unless otherwise specified^{4,5,6,7}

PARAMETER	TEST CONDITIONS	LIMITS			UNIT
		Min	Typ	Max	
V_{IL} V_{IH}	Input voltage ⁸ Low High	-5 3.4		0.6 5.3	V
V_{OL} V_{OH}	Output voltage Low High			0.5	V
		$I_{OL} = 1.6\text{mA}$ $I_{OH} = 100\mu\text{A}$			
		3.8			
I_{LI}	Input load current	$V_{IN} = -5.5\text{V}$, $T_A = 25^\circ\text{C}$			nA
I_{LO}	Output leakage current	$V_{OUT} = \text{OV}$, $T_A = 25^\circ\text{C}$, $V_{CE} = V_{CC}$			nA
		10 10			
		500 1000			
I_{CC} I_{GG}	Supply current ⁹ V_{CC} V_{GG}		30 30	55 55	mA
C_{IN}	Address input capacitance	$f = 1\text{MHz}$, $V_{AC} = 25\text{mV p-p}$, $V_{IN} = V_{CC}$			pF
		10			

AC ELECTRICAL CHARACTERISTICS $T_A = 0^\circ\text{C to } 70^\circ\text{C}$, $V_{CC} = 5\text{V} \pm 5\%$, $V_{GG} = -12\text{V} \pm 5\%$ unless otherwise specified.

PARAMETER	TO	FROM	LIMITS			UNIT
			Min	Typ	Max	
t_{RPW} t_{RPW}	Pulse width Read ¹⁰ Read ¹¹		250 500	200 400		ns
t_{AD} t_{AG}	Address time ¹² Delay Pulse gap	Address Read low Read high Address			50 50	ns
t_{A1} ¹³ t_{A2} ¹³ t_{OE}	Delay time Output Output Output	Address End of read pulse Output enable		625 200 100	700 250 250	ns

NOTES

- Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.
- For operating at elevated temperatures the device must be derated based on a $+150^\circ\text{C}$ maximum junction temperature and a thermal resistance of 110°C/W junction to ambient.
- All inputs are protected against static charge.
- Parameters are valid over operating temperature range unless specified.
- All voltage measurements are referenced to ground.
- Manufacturer reserves the right to make design and process changes and improvements.
- Typical values are at $+25^\circ\text{C}$ and typical supply voltages.
- Guaranteed input levels are stated for worst case conditions including a $\pm 5\%$ variation in V_{CC} and a temperature variation of $0^\circ\text{C to } 70^\circ\text{C}$. Actual input requirements with respect to V_{CC} are $V_{IH} = V_{CC} - 1.85\text{V}$ and $V_{IL} = V_{CC} - 4.15\text{V}$.
- Outputs open, $t_{RPW} = 250\text{ns}$, $t_{RPW} = 500\text{ns}$.
- During t_{RPW1} addresses are decoded and sent to the memory matrix and the stored memory data is moved to the data inputs of the output RS latches. This data is clocked into the output latches at the end (rising edge) of the read pulse. After t_{A2} , data appears at the output terminals.
- During t_{RPW1} data is clocked into the output latches and the address decoders are precharged in preparation for the next cycle.
- Addresses must be stable within 50ns after the read line falls and must remain stable until at least 50ns before the read line goes high.
- $T_A = 0^\circ\text{C to } +70^\circ\text{C}$.

[illegible][illegible][illegible]

10061101 C1111111 00000001 00000001 11010001 00010001 00000100 T 63

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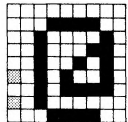
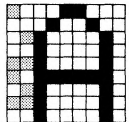
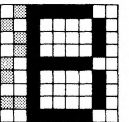
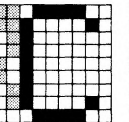
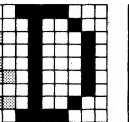
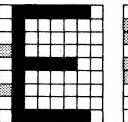
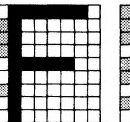
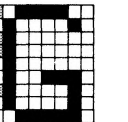
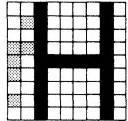
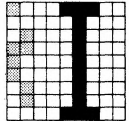
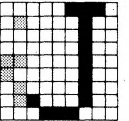
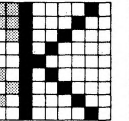
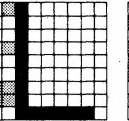
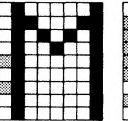
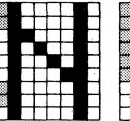
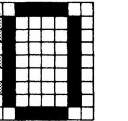
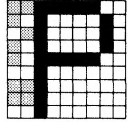
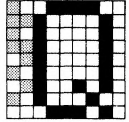
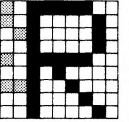
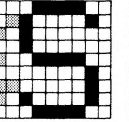
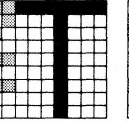
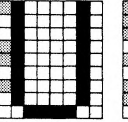
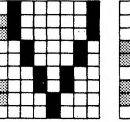
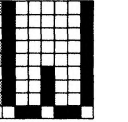
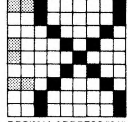
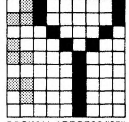
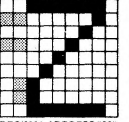
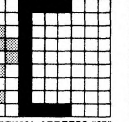
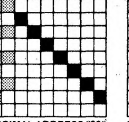
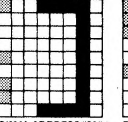
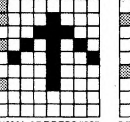
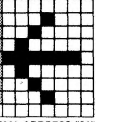
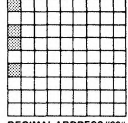
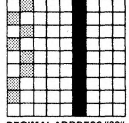
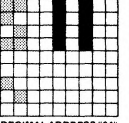
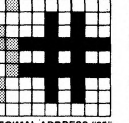
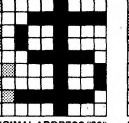
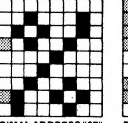
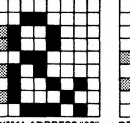
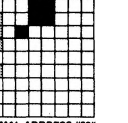
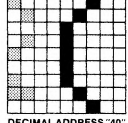
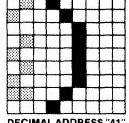
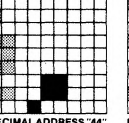
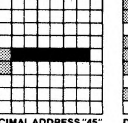
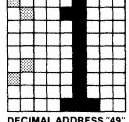
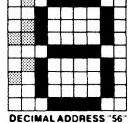
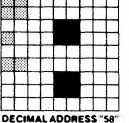
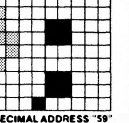
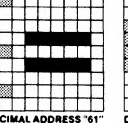
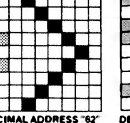
[illegible]

STANDARD CHARACTER FONTS

CM 3400
ASCII SET, VERTICAL SCAN 7X9 WITH CODE CONVERSION

COLUMN ADDRESSES

A₁ 0 1 0 1 0 1 0 1 0
 A₂ 0 0 1 1 0 0 1 1 0
 A₃ 0 0 0 0 1 1 1 1 0
 A₄ 0 0 0 0 0 0 0 0 1

							
DECIMAL ADDRESS "0" (A ₁ -A ₄)	DECIMAL ADDRESS "1"	DECIMAL ADDRESS "2"	DECIMAL ADDRESS "3"	DECIMAL ADDRESS "4"	DECIMAL ADDRESS "5"	DECIMAL ADDRESS "6"	DECIMAL ADDRESS "7"
							
DECIMAL ADDRESS "8"	DECIMAL ADDRESS "9"	DECIMAL ADDRESS "10"	DECIMAL ADDRESS "11"	DECIMAL ADDRESS "12"	DECIMAL ADDRESS "13"	DECIMAL ADDRESS "14"	DECIMAL ADDRESS "15"
							
DECIMAL ADDRESS "16"	DECIMAL ADDRESS "17"	DECIMAL ADDRESS "18"	DECIMAL ADDRESS "19"	DECIMAL ADDRESS "20"	DECIMAL ADDRESS "21"	DECIMAL ADDRESS "22"	DECIMAL ADDRESS "23"
							
DECIMAL ADDRESS "24"	DECIMAL ADDRESS "25"	DECIMAL ADDRESS "26"	DECIMAL ADDRESS "27"	DECIMAL ADDRESS "28"	DECIMAL ADDRESS "29"	DECIMAL ADDRESS "30"	DECIMAL ADDRESS "31"
							
DECIMAL ADDRESS "32"	DECIMAL ADDRESS "33"	DECIMAL ADDRESS "34"	DECIMAL ADDRESS "35"	DECIMAL ADDRESS "36"	DECIMAL ADDRESS "37"	DECIMAL ADDRESS "38"	DECIMAL ADDRESS "39"
							
DECIMAL ADDRESS "40"	DECIMAL ADDRESS "41"	DECIMAL ADDRESS "42"	DECIMAL ADDRESS "43"	DECIMAL ADDRESS "44"	DECIMAL ADDRESS "45"	DECIMAL ADDRESS "46"	DECIMAL ADDRESS "47"
							
DECIMAL ADDRESS "48"	DECIMAL ADDRESS "49"	DECIMAL ADDRESS "50"	DECIMAL ADDRESS "51"	DECIMAL ADDRESS "52"	DECIMAL ADDRESS "53"	DECIMAL ADDRESS "54"	DECIMAL ADDRESS "55"
							
DECIMAL ADDRESS "56"	DECIMAL ADDRESS "57"	DECIMAL ADDRESS "58"	DECIMAL ADDRESS "59"	DECIMAL ADDRESS "60"	DECIMAL ADDRESS "61"	DECIMAL ADDRESS "62"	DECIMAL ADDRESS "63"

NOTES

- A. BCDIC to ASCII in leftmost column, Baudot to ASCII in next column to right.
- B. Undefined addresses result in all outputs going low (TTL "0").
- C. Black squares in character font are high (TTL "1").

STANDARD CHARACTER FONTS (Cont'd)

CM 3941
ASCII SET, RASTER SCAN 7X9 WITH CODE CONVERSION

ROW ADDRESS				OUTPUTS							
A ₄	A ₃	A ₂	A ₁	O ₉	O ₈	O ₇	O ₆	O ₅	O ₄	O ₃	O ₂ O ₁

0 0 0 0								
0 0 0 1								
0 0 1 0								
0 1 1 1								
0 1 0 0								
0 1 0 1								
0 1 1 0								
0 1 1 1								
1 0 0 0								
	DECIMAL ADDRESS "8" (A ₁ -A ₄)	DECIMAL ADDRESS "1"	DECIMAL ADDRESS "2"	DECIMAL ADDRESS "3"	DECIMAL ADDRESS "4"	DECIMAL ADDRESS "5"	DECIMAL ADDRESS "6"	DECIMAL ADDRESS "7"
	DECIMAL ADDRESS "8"	DECIMAL ADDRESS "9"	DECIMAL ADDRESS "10"	DECIMAL ADDRESS "11"	DECIMAL ADDRESS "12"	DECIMAL ADDRESS "13"	DECIMAL ADDRESS "14"	DECIMAL ADDRESS "15"
	DECIMAL ADDRESS "16"	DECIMAL ADDRESS "17"	DECIMAL ADDRESS "18"	DECIMAL ADDRESS "19"	DECIMAL ADDRESS "20"	DECIMAL ADDRESS "21"	DECIMAL ADDRESS "22"	DECIMAL ADDRESS "23"
	DECIMAL ADDRESS "24"	DECIMAL ADDRESS "25"	DECIMAL ADDRESS "26"	DECIMAL ADDRESS "27"	DECIMAL ADDRESS "28"	DECIMAL ADDRESS "29"	DECIMAL ADDRESS "30"	DECIMAL ADDRESS "31"
	DECIMAL ADDRESS "32"	DECIMAL ADDRESS "33"	DECIMAL ADDRESS "34"	DECIMAL ADDRESS "35"	DECIMAL ADDRESS "36"	DECIMAL ADDRESS "37"	DECIMAL ADDRESS "38"	DECIMAL ADDRESS "39"
	DECIMAL ADDRESS "40"	DECIMAL ADDRESS "41"	DECIMAL ADDRESS "42"	DECIMAL ADDRESS "43"	DECIMAL ADDRESS "44"	DECIMAL ADDRESS "45"	DECIMAL ADDRESS "46"	DECIMAL ADDRESS "47"
	DECIMAL ADDRESS "48"	DECIMAL ADDRESS "49"	DECIMAL ADDRESS "50"	DECIMAL ADDRESS "51"	DECIMAL ADDRESS "52"	DECIMAL ADDRESS "53"	DECIMAL ADDRESS "54"	DECIMAL ADDRESS "55"
	DECIMAL ADDRESS "56"	DECIMAL ADDRESS "57"	DECIMAL ADDRESS "58"	DECIMAL ADDRESS "59"	DECIMAL ADDRESS "60"	DECIMAL ADDRESS "61"	DECIMAL ADDRESS "62"	DECIMAL ADDRESS "63"

NOTES

- A. BCDIC to ASCII in leftmost column, Baudot to ASCII in next column to right.
 B. Undefined addresses result in all outputs going low (TTL "0").
 C. Black squares in character font are high (TTL "1").