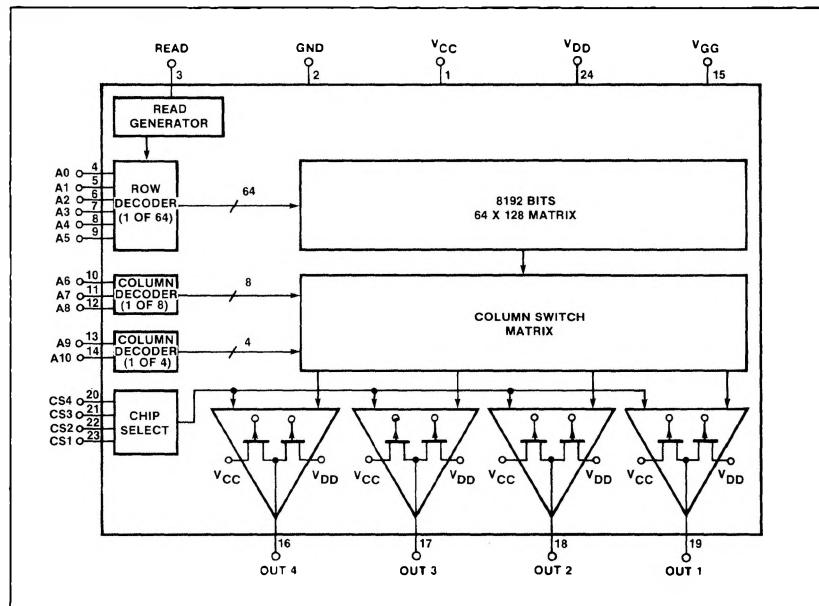
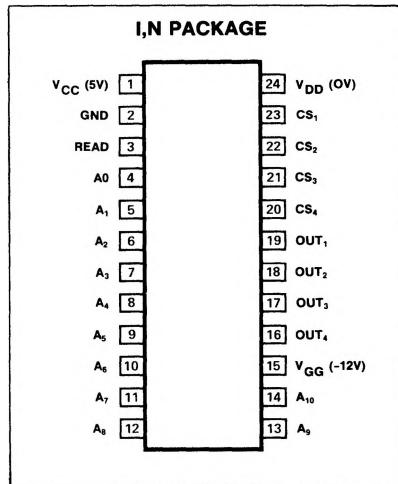


DESCRIPTION

The 2580 has a Read input which controls the entry of data from the ROM into output latches. Three-state outputs allow OR-tying for implementing larger memories. The outputs are enabled by a programmable 4-bit select code applied to 4 binary chip select terminals.

BLOCK DIAGRAM**PIN CONFIGURATION****ABSOLUTE MAXIMUM RATINGS¹**

| PARAMETER | RATING | UNIT |
|------------------------------------|---|------|
| T _A T _{TSG} | Temperature range Operating Storage | °C |
| P _D | Power dissipation at 70°C ² | mW |
| | Input and supply voltages with respect to V _{CC} ³ | V |

DC ELECTRICAL CHARACTERISTICS $T_A = 0^\circ C$ to $70^\circ C$, $V_{CC} = 5V \pm 5\%$, $V_{DD} = OV$, $V_{GG} = -12V \pm 5\%$
unless otherwise noted.^{4,5,6,7}

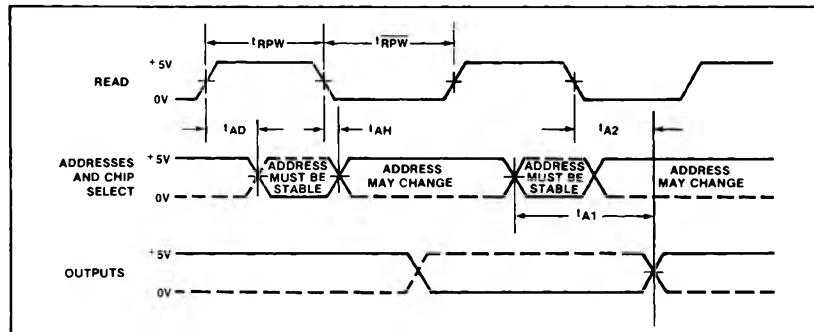
| PARAMETER | TEST CONDITIONS | LIMITS | | | UNIT |
|----------------------|---|--|-----|-------------------------|----------|
| | | Min | Typ | Max | |
| V_{IL} V_{IH} | Input voltage ⁸ Low High | | | 0.6 5.3 | V |
| V_{OL} V_{OH} | Output voltage Low High | $I_{OL} = 1.6mA$ $I_{OH} = 100\mu A$ | 3.8 | | V |
| I_{IL} I_{LO} | Input load current Output leakage current | $V_{IN} = -5.5V$, $T_A = 25^\circ C$ $V_{OUT} = OV$, $T_A = 25^\circ C$ | | 10 10 500 1000 | nA nA |
| I_{CC} I_{GG} | Supply current ⁹ V_{CC} V_{GG} | | | 23 23 35 35 | mA |
| C_{IN} | Input capacitance | $f = 1MHz$, $V_{AC} = 25m p-p$, $V_{IN} = V_{CC}$ | | 10 | pF |

AC ELECTRICAL CHARACTERISTICS $T_A = 25^\circ C$, $V_{CC} = 5V \pm 5\%$, $V_{DD} = OV$, $V_{GG} = -12V \pm 5\%$
unless otherwise specified.

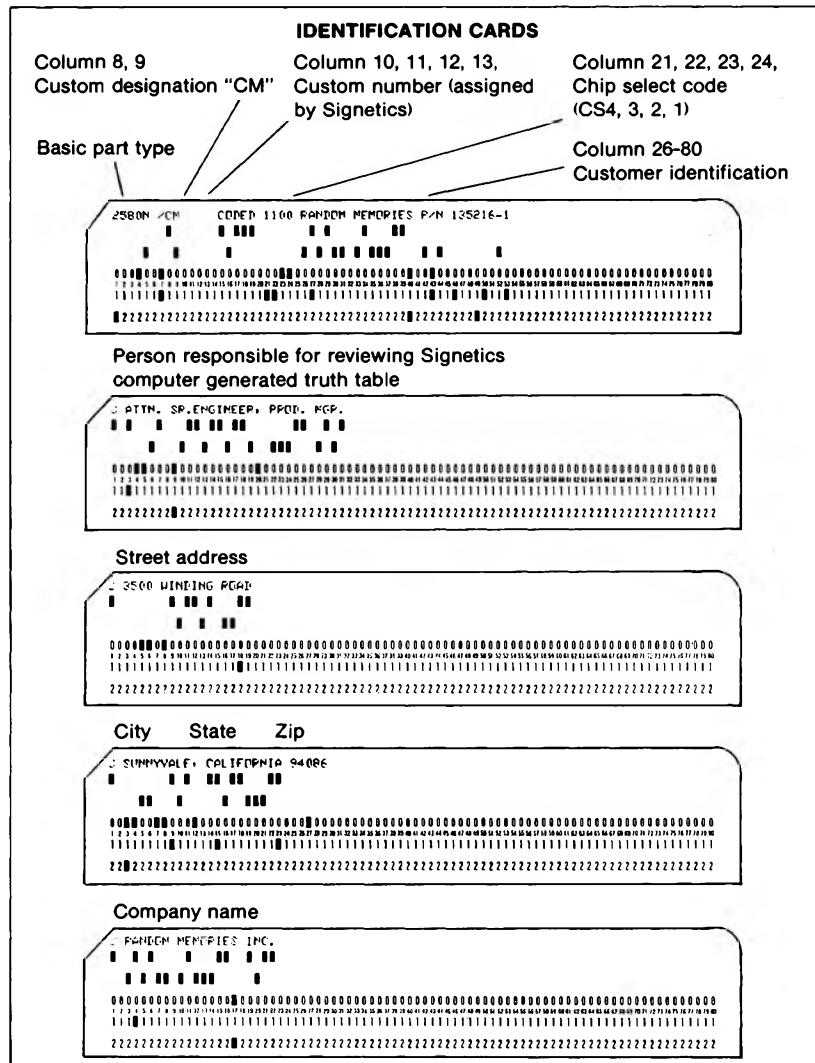
| PARAMETER | TO | FROM | LIMITS | | | UNIT | |
|------------------------------|---|------------------|------------------------------|------------|------------|------------|----|
| | | | Min | Typ | Max | | |
| t_{RPW} $t_{\bar{R}PW}$ | Pulse width Read ¹⁰ Read ¹¹ | | | 650 500 | 500 400 | ns | |
| t_{AD} t_{AH} | Address time Delay ¹² Hold | | | 0 | | 50 ns | |
| t_{A1} t_{A2} | Delay time | Output Output | Address End of read pulse | | 625 250 | 950 350 | ns |

NOTES

- Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.
- For operating at elevated temperatures the device must be derated based on a +150°C maximum junction temperature and a thermal resistance of 110°C/W junction to ambient.
- All inputs are protected against static charge.
- Parameters are valid over operating temperature range unless specified.
- All voltage measurements are referenced to ground.
- Manufacturer reserves the right to make design and process changes and improvements.
- Typical values are at $+25^\circ C$ and typical supply voltages.
- Guaranteed input levels are stated for worst case conditions including a $\pm 5\%$ variation in V_{CC} and a temperature variation of $0^\circ C$ to $70^\circ C$. Actual input requirements with respect to V_{CC} are $V_{IH} = V_{CC} - 1.85$ and $V_{IL} = V_{CC} - 4.15V$.
- Outputs open, $t_{RPW} = 500ns$, $t_{\bar{R}PW} = 500ns$.
- During t_{RPW} addresses are decoded and sent to the memory matrix, and the stored memory data is moved to the data inputs of the output RS latches. This data is clocked into the output latches at the end (falling edge) of the read pulse. After t_{A2} , data appears at the output terminals.
- During $t_{\bar{R}PW}$ data is clocked into the output latches and the address decoders are precharged in preparation for the next cycle.
- Addresses must be stable within 50ns after the read line rises and must remain stable until the read line falls.

TIMING DIAGRAM

CARD FORMAT



CODING FORMAT

Coding data for the 2580 may be sent to Signetics via punched cards or via a written truth table. Cards are preferred since errors are essentially eliminated.

On receipt of a card deck, Signetics will translate the card deck to a truth table using the Signetics Computer Aided Design (CAD) facility. The truth table will then be sent to the customer requesting engineer for final approval. On receipt of final approval, Signetics will cut the rubylith mask and proceed with manufacture.

DATA CARD FORMAT

| Columns | |
|--------------|---|
| 1-4 | Decimal equivalent of first data word location Example: 0124 Note: Leading zeros must be used for addresses from 0000 to 0999 |
| 5 | Dash (—) to separate numbers |
| 6-9 | Address of last word on card |
| 10 | Blank |
| 11-14 | First data word (O ₄ , O ₃ , O ₂ , O ₁) |
| 15 | Blank |
| 16-19 | Second data word |
| Etc. thru 71 | |
| 72-80 | Reserved for comments (these columns are ignored by the computer) |

Up to twelve (12) data words can be coded on one card. Less than 12 may be used as long as the first and last addresses are given in columns 1-9.

EXAMPLE

