National Semiconductor

5408/DM5408/DM7408 Quad 2-Input AND Gates

General Description

This device contains four independent gates each of which performs the logic AND function.

Features

Alternate Military/Aerospace device (5408) is available. Contact a National Semiconductor Sales Office/Distributor for specifications.

TL/F/6498-1

Connection Diagram



Order Number 5408DMQB, 5408FMQB, DM5408J, DM5408W or DM7408N See NS Package Number J14A, N14A or W14B

Function Table

Y =	AB
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Inputs		Output				
Α	в	Y				
L	L	L				
L	н	L				
н	L	L				
Н	Н	Н				

H = High Logic Level

L = Low Logic Level

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	5.5V
Operating Free Air Temperature Range	
DM54 and 54	-55°C to +125°C
DM74	0°C to +70°C
Storage Temperature Range	-65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM5408			DM7408			Units
		Min	Nom	Max	Min	Nom	Max	onito
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
VIH	High Level Input Voltage	2			2			ν
VIL	Low Level Input Voltage			0.8			0.8	V
ЮН	High Level Output Current			-0.8			-0.8	mA
IOL	Low Level Output Current			16			16	mA
T _A	Free Air Operating Temperature	-55		125	0		70	°C

Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions		Min	Typ (Note 1)	Max	Units
Vi	Input Clamp Voltage	V _{CC} = Min, I _I =	=12 mA			-1.5	V
V _{OH}	High Level Output Voltage	$V_{CC} = Min, I_{OI}$ $V_{IL} = Max$	_H = Max	2.4	3.4		v
V _{OL}	Low Level Output Voltage	$V_{CC} = Min, I_{OI}$ $V_{IH} = Min$	_ = Max		0.2	0.4	v
կ	Input Current @ Max Input Voltage	$V_{\rm CC} = Max, V_{\rm I}$	= 5.5V			1	mA
łн	High Level Input Current	$V_{CC} = Max, V_I = 2.4V$				40	μΑ
կլ	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$				-1.6	mA
los	Short Circuit	V _{CC} = Max (Note 2)	DM54	-20		-55	mA
	Output Current		(Note 2) DM74	-18		-55	
Іссн	Supply Current with Outputs High	V _{CC} = Max			11	21	mA
ICCL	Supply Current with Outputs Low	V _{CC} = Max			20	33	mA

Switching Characteristics at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$ (See Section 1 for Test Waveforms and Output Load)

Symbol	Parameter	Conditions	Min	Max	Units
^t PLH	Propagation Delay Time Low to High Level Output	$C_L = 15 pF$ $R_L = 400 \Omega$		27	ns
tPHL	Propagation Delay Time High to Low Level Output			19	ns

Note 1: All typicals are at V_{CC} = 5V, T_A = 25°C.

Note 2: Not more than one output should be shorted at a time.