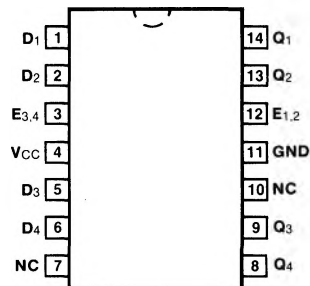


54/7477

QUAD D-TYPE LATCH

CONNECTION DIAGRAM PINOUT A

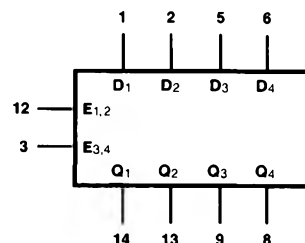


DESCRIPTION — The '77 contains four D-type latches used for temporary storage. Each latch shares an Enable input with one other latch. When the Enable input is HIGH, a latch is transparent, i.e., the Q output follows the D input each time it changes. When the Enable goes LOW, the information (that was present at the D input when the transition occurred) is retained at the Q output. For Truth Table, specifications, and logic diagrams, refer to the '75 data sheet, but note that \bar{Q} is not available on the '77.

ORDERING CODE: See Section 9

PKGS	PIN OUT	COMMERCIAL GRADE	MILITARY GRADE	PKG TYPE
		$V_{CC} = +5.0\text{ V} \pm 5\%$, $T_A = 0^\circ\text{C to } +70^\circ\text{C}$	$V_{CC} = +5.0\text{ V} \pm 10\%$, $T_A = -55^\circ\text{C to } +125^\circ\text{C}$	
Plastic DIP (P)	A	7477PC		9A
Ceramic DIP (D)	A	7477DC	5477DM	6A
Flatpak (F)	A	7477FC	5477FM	3I

LOGIC SYMBOL



V_{CC} = Pin 4
GND = Pin 11
NC = Pin 7, 10

INPUT LOADING/FAN-OUT: See Section 3 for U.L. definitions

PIN NAMES	DESCRIPTION	54/74 (U.L.) HIGH/LOW
$D_1 - D_4$	Data Inputs	2.0/2.0
$E_{1,2}$	Enable Input, Latches 1, 2	4.0/4.0
$E_{3,4}$	Enable Input, Latches 3, 4	4.0/4.0
$Q_1 - Q_4$	Latch Outputs	10/10