54ACQ240,54ACTQ240

54ACQ240 54ACTQ240 Quiet Series Octal Buffer/Line Driver with TRI-STATE Outputs



Literature Number: SNOS058



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54ACQ240 • 54ACTQ240 Quiet Series Octal Buffer/Line Driver with TRI-STATE® Outputs

General Description

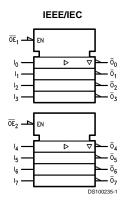
The 'ACQ/'ACTQ240 is an inverting octal buffer and line driver designed to be employed as a memory address driver, clock driver and bus oriented transmitter or receiver which provides improved PC board density. The 'ACQ/'ACTQ utilizes NSC Quiet Series technology to guarantee quiet output switching and improve dynamic threshold performance. FACT Quiet Series™ features GTO™ output control and undershoot corrector in addition to a split ground bus for superior performance.

- Guaranteed simultaneous switching noise level and dynamic threshold performance
- Improved latch-up immunity
- Inverting TRI-STATE outputs drive bus lines or buffer memory address registers
- Outputs source/sink 24 mA
- Faster prop delays than the standard 'ACT240
- 4 kV minimum ESD immunity
- Standard Microcircuit Drawing (SMD) 'ACTQ240: 5962-92184

Features

 \blacksquare I_{CC} and I_{OZ} reduced by 50%

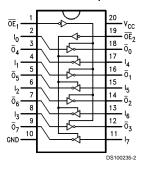
Logic Symbol



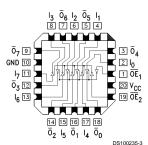
Pin Names	Description
\overline{OE}_1 , \overline{OE}_2	TRI-STATE Output Enable Inputs
I ₀ -I ₇	Inputs
$\overline{O}_0 - \overline{O}_7$	Outputs

Connection Diagrams

Pin Assignment for DIP and Flatpak



Pin Assignment for LCC



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FACT® is a registered trademark of Fairchild Semiconductor Corporation.

FACT Quiet Series™ is a trademark of Fairchild Semiconductor Corporation.

Truth Tables

Inputs		Outputs
ŌE₁	l _n	(Pins 12, 14, 16, 18)
L	L	Н
L	Н	L
Н	Х	Z

Inputs		Outputs
OE ₂	l _n	(Pins 3, 5, 7, 9)
L	L	Н
L	Н	L
Н	X	Z

H = HIGH Voltage Level
L = LOW Voltage Level
X = Immaterial
Z = High Impedance

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Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

-0.5V to +7.0V

±50 mA

 $\begin{array}{c} \text{V}_{\text{O}} = -0.5 \text{V} & -20 \text{ mA} \\ \text{V}_{\text{O}} = \text{V}_{\text{CC}} + 0.5 \text{V} & +20 \text{ mA} \\ \text{DC Output Voltage (V}_{\text{O}}) & -0.5 \text{V to V}_{\text{CC}} + 0.5 \text{V} \\ \end{array}$

DC Output Source or Sink Current (I_O)

Supply Voltage (V_{CC})

DC V_{CC} or Ground Current per Output Pin (I_{CC} or I_{GND}) ± 50 mA Storage Temperature (T_{STG}) -65° C to $+150^{\circ}$ C

Storage Temperature (T_{STG}) DC Latch-Up Source or

Sink Current ±300 mA

Junction Temperature (T_J)

CDIP 175°C

Recommended Operating Conditions

Supply Voltage (V_{CC})

 $\begin{tabular}{lll} 'ACQ & 2.0V to 6.0V \\ 'ACTQ & 4.5V to 5.5V \\ Input Voltage (V_I) & 0V to V_{CC} \\ Output Voltage (V_O) & 0V to V_{CC} \\ \end{tabular}$

Operating Temperature (T_A)

54ACQ/ACTQ -55°C to +125°C

Minimum Input Edge Rate $\Delta V/\Delta t$

'ACQ Devices

 $\rm V_{IN}$ from 30% to 70% of $\rm V_{CC}$

 V_{CC} @ 3.0V, 4.5V, 5.5V 125 mV/ns

Minimum Input Edge Rate $\Delta V/\Delta t$

'ACTQ Devices

 V_{IN} from 0.8V to 2.0V

 $\rm V_{CC} @ 4.5V, \, 5.5V \hspace{1.5cm} 125 \, mV/ns$

Note 1: Absolute maximum ratings are those values beyond which damage to the device may occur. The databook specifications should be met, without exception, to ensure that the system design is reliable over its power supply, temperature, and output/input loading variables. National does not recommend operation of FACT® circuits outside databook specifications.

Note 2: All commercial packaging is not recommended for applications requiring greater than 2000 temperature cycles from -40°C to $+125^{\circ}\text{C}$.

DC Characteristics for 'ACQ Family Devices

			54ACQ		
Symbol	Parameter	V _{cc}	T _A =	Units	Conditions
		(V)	-55°C to +125°C		
			Guaranteed Limits		
V _{IH}	Minimum High Level	3.0	2.1		V _{OUT} = 0.1V
	Input Voltage	4.5	3.15	V	or V _{CC} – 0.1V
		5.5	3.85		
V _{IL}	Maximum Low Level	3.0	0.9		V _{OUT} = 0.1V
	Input Voltage	4.5	1.35	V	or V _{CC} – 0.1V
		5.5	1.65		
V _{OH}	Minimum High Level	3.0	2.9		I _{OUT} = -50 μA
	Output Voltage	4.5	4.4	V	
		5.5	5.4		
					(Note 3)
					$V_{IN} = V_{IL} \text{ or } V_{IH}$
		3.0	2.4		$I_{OH} = -12 \text{ mA}$
		4.5	3.7	V	$I_{OH} = -24 \text{ mA}$
		5.5	4.7		$I_{OH} = -24 \text{ mA}$
V _{OL}	Maximum Low Level	3.0	0.1		I _{OUT} = 50 μA
	Output Voltage	4.5	0.1	V	
		5.5	0.1		
					(Note 3)
					$V_{IN} = V_{IL} \text{ or } V_{IH}$
		3.0	0.50		I _{OL} = 12 mA
		4.5	0.50	V	I _{OL} = 24 mA
		5.5	0.50		I _{OL} = 24 mA
I _{IN}	Maximum Input	5.5	±1.0	μΑ	$V_I = V_{CC}$, GND
	Leakage Current				(Note 5)

DC Characteristics for 'ACQ Family Devices (Continued)

			54ACQ		
Symbol	Parameter	V _{cc}	T _A =	Units	Conditions
		(V)	-55°C to +125°C		
			Guaranteed Limits		
I _{OLD}	Minimum Dynamic (Note 4)	5.5	50	mA	V _{OLD} = 1.65V Max
I _{OHD}	Output Current	5.5	-50	mA	V _{OHD} = 3.85V Min
I _{cc}	Maximum Quiescent	5.5	80.0	μΑ	V _{IN} = V _{CC}
	Supply Current				or GND (Note 5)
I _{oz}	Maximum TRI-STATE				V _I (OE) = V _{IL} , V _{IH}
	Leakage Current	5.5	±5.0	μA	$V_I = V_{CC}$, GND
					$V_O = V_{CC}$, GND
V _{OLP}	Quiet Output	5.0	1.5V	V	(Notes 6, 7)
	Maximum Dynamic V _{OL}	5.0		V	
V _{OLV}	Quiet Output Minimum Dynamic V _{OL}	5.0	-1.2V	V	(Notes 6, 7)

Note 3: All outputs loaded; thresholds on input associated with output under test.

Note 4: Maximum test duration 2.0 ms, one output loaded at a time.

Note 5: I_{IN} and I_{CC} @ 3.0V are guaranteed to be less than or equal to the respective limit @ 5.5V V_{CC}.

 $\rm I_{CC}$ for 54ACQ @ 25°C is identical to 74ACQ @ 25°C.

Note 6: Plastic DIP package.

Note 7: Max number of outputs defined as (n). Data inputs are driven 0V to 5V. One output @ GND.

Note 8: Max number of data inputs (n) switching. (n -1) inputs switching 0V to 5V ('ACQ). Input-under-test switching: 5V to threshold (V_{ILD}), 0V to threshold (V_{IHD}), f = 1 MHz

DC Characteristics for 'ACTQ Family Devices

			54ACTQ		
Symbol	Parameter	V _{cc}	T _A =	Units	Conditions
		(V)	-55°C to +125°C		
			Guaranteed	1	
			Limits		
V_{IH}	Minimum High Level	4.5	2.0	V	V _{OUT} = 0.1V
	Input Voltage	5.5	2.0		or V _{CC} – 0.1V
V_{IL}	Maximum Low Level	4.5	0.8	V	V _{OUT} = 0.1V
	Input Voltage	5.5	0.8		or V _{CC} – 0.1V
V _{OH}	Minimum High Level	4.5	4.4	V	I _{OUT} = -50 μA
	Output Voltage	5.5	5.4		
					(Note 9)
					$V_{IN} = V_{IL} \text{ or } V_{IH}$
		4.5	3.70	V	$I_{OH} = -24 \text{ mA}$
		5.5	4.70		I _{OH} = -24 mA
V_{OL}	Maximum Low Level	4.5	0.1	V	I _{OUT} = 50 μA
	Output Voltage	5.5	0.1		
					(Note 9)
					$V_{IN} = V_{IL} \text{ or } V_{IH}$
		4.5	0.50	V	I _{OL} = 24 mA
		5.5	0.50		I _{OL} = 24 mA
I _{IN}	Maximum Input	5.5	±1.0	μA	$V_I = V_{CC}$, GND
	Leakage Current				
I_{OZ}	Maximum TRI-STATE	5.5	±5.0	μA	$V_{I} = V_{IL}, V_{IH}$
	Leakage Current				$V_O = V_{CC}$, GND

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DC Characteristics for 'ACTQ Family Devices (Continued)

			54ACTQ		
Symbol	Parameter	V _{cc}	T _A =	Units	Conditions
		(V)	-55°C to +125°C		
			Guaranteed		
			Limits		
I _{CCT}	Maximum	5.5	1.6	mA	$V_{I} = V_{CC} - 2.1V$
	I _{CC} /Input				
I _{OLD}	Minimum Dynamic	5.5	50	mA	V _{OLD} = 1.65V Max
	(Note 10)				
I _{OHD}	Output Current	5.5	-50	mA	V _{OHD} = 3.85V Min
I _{cc}	Maximum Quiescent	5.5	80.0	μΑ	$V_{IN} = V_{CC}$
	Supply Current				or GND (Note 11)
V _{OLP}	Quiet Output Maximum	5.0	1.5V	V	(Notes 12, 13)
	Dynamic V _{OL}	3.0		V	
V _{OLV}	Quiet Output Minimum	5.0	-1.2V	V	(Notes 12, 13)
	Dynamic V _{OL}	3.0			

Note 9: All outputs loaded; thresholds on input associated with output under test.

Note 10: Maximum test duration 2.0 ms, one output loaded at a time.

Note 11: I_{CC} for 54ACTQ @ 25°C is identical to 74ACTQ @ 25°C.

Note 12: Plastic DIP package.

Note 13: Max number of Data Inputs defined as (n). n-1 Data Inputs are driven 0V to 3V. One Data Input @ V_{IN} = GND.

Note 14: Max number of Data Inputs (n) switching. (n-1) Inputs switching 0V to 3V ('ACTQ). Input-under-test switching: 3V to threshold (V_{ILD}), 0V to threshold (V_{IHD}), f = 1 MHz.

AC Electrical Characteristics

Symbol	Parameter	V _{CC} (V) (Note 15)	54ACQ T _A = -55°C to +125°C C _L = 50 pF		Units	Fig. No.
			Min	Max		
t _{PHL} , t _{PLH}	Propagation Delay	3.3	1.0	12.5	ns	
	Data to Output	5.0	1.0	9.0		
t _{PZL} , t _{PZH}	Output Enable Time	3.3	1.0	13.5	ns	
		5.0	1.0	10.0		
t _{PHZ} , t _{PLZ}	Output Disable Time	3.3	1.0	11.0	ns	
		5.0	1.0	9.0		

Note 15: Voltage Range 5.0 is 5.0V ± 0.5 V

Voltage Range 3.3 is 3.3 ±0.3V.

Note 16: Skew is defined as the absolute value of the difference between the actual propagation delay for any two separate outputs of the same device. The specification applies to any outputs switching in the same direction, either HIGH to LOW (t_{OSHL}) or LOW to HIGH (t_{OSLH}). Parameter guaranteed by design.

AC Electrical Characteristics

Symbol	Parameter	V _{cc} (V) (Note 17)	54ACTQ T _A = -55°C to +125°C C _L = 50 pF		Units	Fig. No.
			Min	Max		
t _{PHL} , t _{PLH}	Propagation Delay	5.0	1.5	9.0	ns	
	Data to Output					
t _{PZL} , t _{PZH}	Output Enable Time	5.0	1.5	11.0	ns	
t _{PHZ} , t _{PLZ}	Output Disable Time	5.0	1.5	10.0	ns	

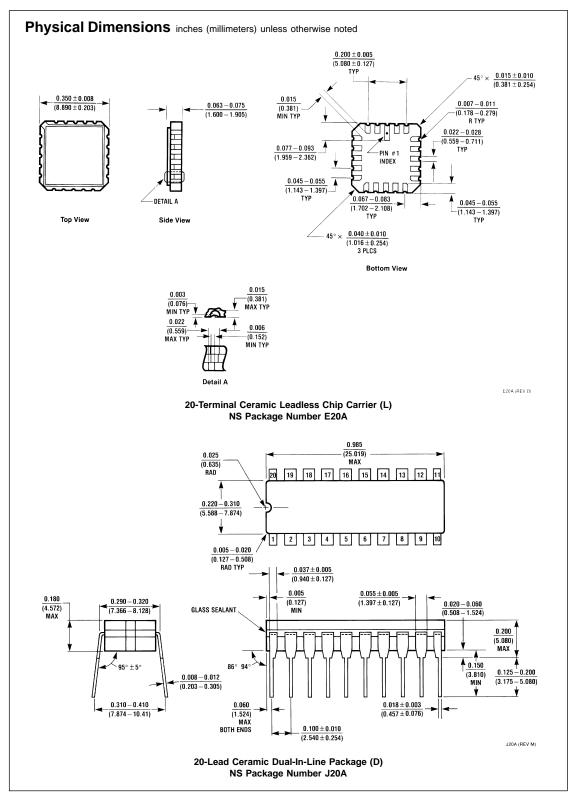
Note 17: Voltage Range 5.0 is 5.0V ±0.5V

Note 18: Skew is defined as the absolute value of the difference between the actual propagation delay for any two separate outputs of the same device. The specification applies to any outputs switching in the same direction, either HIGH to LOW (t_{OSHL}) or LOW to HIGH (t_{OSLH}). Parameter guaranteed by design.

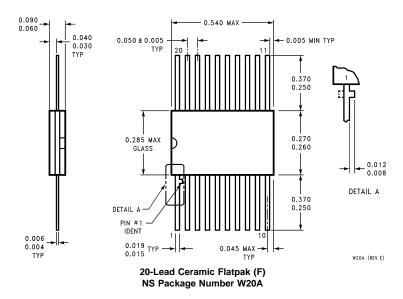
Capacitance

Symbol	Parameter	Тур	Units	Conditions
C _{IN}	Input Capacitance	4.5	pF	V _{CC} = OPEN
C _{PD}	Power Dissipation	70	pF	V _{CC} = 5.0V
	Capacitance			

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Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



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