

# 54AC/74AC20

## Dual 4-Input NAND Gate

### General Description

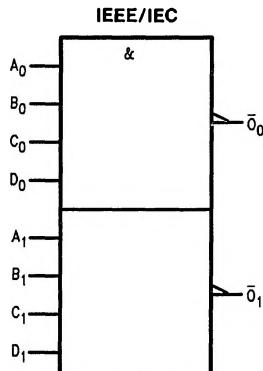
The 'AC20 contains four 4-input NAND gates.

### Features

- Outputs source/sink 24 mA
- Standard Military Drawing (SMD)
- 'AC20: 5962-87613

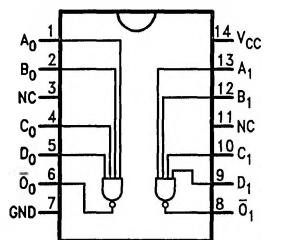
**Ordering Code:** See Section 8

### Logic Symbol



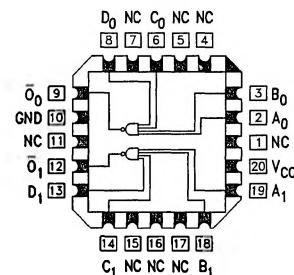
TL/F/9918-1

**Pin Assignment  
for DIP, Flatpak and SOIC**



TL/F/9918-3

**Pin Assignment  
for LCC**



TL/F/9918-2

### Connection Diagrams

Pin Names	Description
A <sub>m</sub> , B <sub>n</sub> , C <sub>p</sub> , D <sub>q</sub> O <sub>r</sub>	Inputs Outputs

## Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage ( $V_{CC}$ )	$-0.5V$ to $+7.0V$
DC Input Diode Current ( $I_{IIK}$ ) $V_I = -0.5V$	$-20\text{ mA}$
$V_I = V_{CC} + 0.5V$	$+20\text{ mA}$
DC Input Voltage ( $V_I$ )	$-0.5V$ to $V_{CC} + 0.5V$
DC Output Diode Current ( $I_{OK}$ ) $V_O = -0.5V$	$-20\text{ mA}$
$V_O = V_{CC} + 0.5V$	$+20\text{ mA}$
DC Output Voltage ( $V_O$ )	$-0.5V$ to $V_{CC} + 0.5V$
DC Output Source or Sink Current ( $I_O$ )	$\pm 50\text{ mA}$
DC $V_{CC}$ or Ground Current per Output Pin ( $I_{CC}$ or $I_{GND}$ )	$\pm 50\text{ mA}$
Storage Temperature ( $T_{STG}$ )	$-65^{\circ}\text{C}$ to $+150^{\circ}\text{C}$
Junction Temperature ( $T_J$ ) CDIP PDIP	$175^{\circ}\text{C}$ $140^{\circ}\text{C}$

Note 1: Absolute maximum ratings are those values beyond which damage to the device may occur. The databook specifications should be met, without exception, to ensure that the system design is reliable over its power supply, temperature, and output/input loading variables. National does not recommend operation of FACT™ circuits outside databook specifications.

## Recommended Operating Conditions

Supply Voltage ( $V_{CC}$ ) 'AC 'ACT	2.0V to 6.0V 4.5V to 5.5V
Input Voltage ( $V_I$ )	0V to $V_{CC}$
Output Voltage ( $V_O$ )	0V to $V_{CC}$
Operating Temperature ( $T_A$ ) 74AC/ACT 54AC/ACT	$-40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$ $-55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$
Minimum Input Edge Rate ( $\Delta V/\Delta t$ ) 'AC Devices $V_{IN}$ from 30% to 70% of $V_{CC}$ $V_{CC}$ @ 3.3V, 4.5V, 5.5V	125 mV/ns
Minimum Input Edge Rate ( $\Delta V/\Delta t$ ) 'ACT Devices $V_{IN}$ from 0.8V to 2.0V $V_{CC}$ @ 4.5V, 5.5V	125 mV/ns

## DC Characteristics for 'AC Family Devices

Symbol	Parameter	$V_{CC}$ (V)	74AC		54AC	74AC	Units	Conditions
			$T_A = +25^{\circ}\text{C}$		$T_A = -55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$	$T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$		
			Typ	Guaranteed Limits				
$V_{IH}$	Minimum High Level Input Voltage	3.0 4.5 5.5	1.5 2.25 2.75	2.1 3.15 3.85	2.1 3.15 3.85	2.1 3.15 3.85	V	$V_{OUT} = 0.1\text{V}$ or $V_{CC} - 0.1\text{V}$
$V_{IL}$	Maximum Low Level Input Voltage	3.0 4.5 5.5	1.5 2.25 2.75	0.9 1.35 1.65	0.9 1.35 1.65	0.9 1.35 1.65	V	$V_{OUT} = 0.1\text{V}$ or $V_{CC} - 0.1\text{V}$
$V_{OH}$	Minimum High Level Output Voltage	3.0 4.5 5.5	2.99 4.49 5.49	2.9 4.4 5.4	2.9 4.4 5.4	2.9 4.4 5.4	V	$I_{OUT} = -50\text{ }\mu\text{A}$
		3.0 4.5 5.5		2.56 3.86 4.86	2.4 3.7 4.7	2.46 3.76 4.76	V	* $V_{IN} = V_{IL}$ or $V_{IH}$ $-12\text{ mA}$ $I_{OH} = 24\text{ mA}$ $-24\text{ mA}$
$V_{OL}$	Maximum Low Level Output Voltage	3.0 4.5 5.5	0.002 0.001 0.001	0.1 0.1 0.1	0.1 0.1 0.1	0.1 0.1 0.1	V	$I_{OUT} = 50\text{ }\mu\text{A}$
		3.0 4.5 5.5		0.36 0.36 0.36	0.5 0.5 0.5	0.44 0.44 0.44	V	* $V_{IN} = V_{IL}$ or $V_{IH}$ $12\text{ mA}$ $I_{OL} = 24\text{ mA}$ $24\text{ mA}$
$I_{IN}$	Maximum Input Leakage Current	5.5		$\pm 0.1$	$\pm 1.0$	$\pm 1.0$	$\mu\text{A}$	$V_I = V_{CC}, \text{ GND}$

\*All outputs loaded; thresholds on input associated with output under test.

†Maximum test duration 2.0 ms, one output loaded at a time.

## DC Characteristics for 'AC Family Devices (Continued)

Symbol	Parameter	V <sub>CC</sub> (V)	74AC		54AC		74AC		Units	Conditions		
			T <sub>A</sub> = + 25°C		T <sub>A</sub> = - 55°C to + 125°C	T <sub>A</sub> = - 40°C to + 85°C	Guaranteed Limits					
			Typ									
I <sub>OLD</sub>	†Minimum Dynamic Output Current	5.5			50	75	mA	V <sub>OLD</sub> = 1.65V Max				
I <sub>OHD</sub>		5.5			-50	-75	mA	V <sub>OHD</sub> = 3.85V Min				
I <sub>CC</sub>	Maximum Quiescent Supply Current	5.5		4.0	80.0	40.0	µA	V <sub>IN</sub> = V <sub>CC</sub> or GND				

\*All outputs loaded; thresholds on input associated with output under test.

†Maximum test duration 2.0 ms, one output loaded at a time.

Note :I<sub>IN</sub> and I<sub>CC</sub> @ 3.0V are guaranteed to be less than or equal to the respective limit @ 5.5V V<sub>CC</sub>.

I<sub>CC</sub> for 54AC @ 25°C is identical to 74AC @ 25°C.

## AC Electrical Characteristics: See Section 2 for waveforms

Symbol	Parameter	V <sub>CC</sub> * (V)	74AC			54AC		74AC		Units	Fig. No.
			T <sub>A</sub> = + 25°C C <sub>L</sub> = 50 pF			T <sub>A</sub> = - 55°C to + 125°C C <sub>L</sub> = 50 pF	T <sub>A</sub> = - 40°C to + 85°C C <sub>L</sub> = 50 pF				
			Min	Typ	Max	Min	Max	Min	Max		
t <sub>PLH</sub>	Propagation Delay	3.3	2.0	6.0	8.5	1.0	11.0	1.5	10.0	ns	2-3,4
		5.0	1.5	5.0	7.0	1.0	8.5	1.0	8.0		
t <sub>PHL</sub>	Propagation Delay	3.3	1.5	5.0	7.0	1.0	10.5	1.0	9.0	ns	2-3,4
		5.0	1.5	4.0	6.0	1.0	7.0	1.0	7.0		

\*Voltage Range 3.3 is 3.3V ± 0.3V

Voltage Range 5.0 is 5.0V ± 0.5V

## Capacitance

Symbol	Parameter	Typ	Units	Conditions
C <sub>IN</sub>	Input Capacitance	4.5	pF	V <sub>CC</sub> = 5.0V
C <sub>PD</sub>	Power Dissipation Capacitance	40.0	pF	V <sub>CC</sub> = 5.0V