DGG PACKAGE

(TOP VIEW)

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- Member of the Texas Instruments Widebus™ Family
- Latch-Up Performance Exceeds 500 mA Per JEDEC Standard JESD-17
- Bus-Hold Inputs Eliminate the Need for External Pullup Resistors
- Typical V_{OLP} (Output Ground Bounce)
 < 0.8 V at V_{CC} = 5 V, T_A = 25°C
- Distributed V_{CC} and GND Pin Configuration Minimizes High-Speed Switching Noise
- Packaged in Plastic Thin Shrink Small-Outline (DGG) Package

description

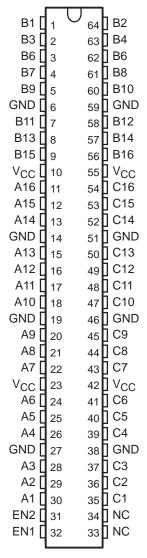
The 74ACT16254 is a dual 16-bit, noninverting bus-interface device. The A and C ports perform a transceiver function, like that of the 74ACT245. The B and C ports perform the buffer/driver function of the 74ACT244. The A and C port outputs are designed to sink up to 12 mA.

The 74ACT16254 is designed for asynchronous communication between data buses. The control function implementation minimizes external timing requirements.

Data transmission from the A port to the C port, C port to A port, or B port to C port is accomplished by setting the appropriate logic levels on the bus enable (EN1 and EN2) inputs.

All outputs are disabled when logic highs are placed on both EN1 and EN2; the buses are effectively isolated.

The 74ACT16254 is packaged in TI's thin shrink small-outline package (DGG), which provides twice the I/O pin count and functionality of standard small-outline packages in the same printed-circuit-board area.



NC - No internal connection

Active bus-hold circuitry is provided to hold unused or floating data and I/O pins at a valid logic level.

The 74ACT16254 is characterized for operation from -40°C to 85°C.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

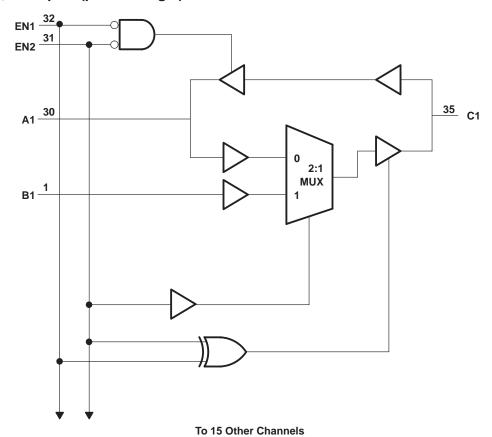
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FUNCTION TABLE

INPUTS		ODERATION			
EN2	EN1	OPERATION			
Н	Н	Isolation			
Н	L	B data to C bus			
L	Н	A data to C bus			
L	L	C data to A bus			

logic diagram, each port (positive logic)



74ACT16254 16-BIT ADDRESS/DATA MULTIPLEXER WITH 3-STATE OUTPUTS

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage range, V _{CC}	0.5 V to 7 V
Input voltage range, V _I (see Note 1)	\dots -0.5 V to V _{CC} + 0.5 V
Output voltage range, VO (see Note 1)	$-0.5 \text{ V to V}_{CC} + 0.5 \text{ V}$
Current into any output in the low state, IO	50 mA
Input clamp current, I _{IK} (V _I < 0)	–50 mA
Output clamp current, I _{OK} (V _O < 0)	–50mA
Continuous current through V _{CC} or GND	±100 mA
Maximum power dissipation at $T_A = 55^{\circ}C$ (in still air) (see Note 2)	1 W
Storage temperature range, T _{stg}	65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions (see Note 3)

		MIN	MAX	UNIT
VCC	Supply voltage	4.5	5.5	V
VIH	High-level input voltage	2		V
VIL	Low-level input voltage		0.8	V
VI	Input voltage	0	Vcc	V
loh	High-level output current		-12	mA
lOL	Low-level output current		12	mA
Δt/Δν	Input transition rise or fall rate		10	ns/V
TA	Operating free-air temperature	-40	85	°C

NOTE 3: Unused inputs must be held high or low to prevent them from floating.

NOTES: 1. The input and output negative-voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

^{2.} The maximum package power dissipation is calculated using a junction temperature of 150°C and a board trace length of 750 mils.

74ACT16254 16-BIT ADDRESS/DATA MULTIPLEXER WITH 3-STATE OUTPUTS

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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS		MIN	TYP	MAX	UNIT
VIK	V _{CC} = 4.5 V,	I _I = -18 mA				-1.2	V
	$V_{CC} = 4.5 \text{ V},$	I _{OH} = -100 μA		3			
∨он	$V_{CC} = 5.5 \text{ V},$	I _{OH} = -100 μA		4.2			V
	$V_{CC} = 4.5 \text{ V},$	$I_{OH} = -12 \text{ mA}$		3			
	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	I _{OL} = 100 μA				0.1	V
V _{OL}	V _{CC} = 4.5 V,	I _{OL} = 12 mA				0.4 V	
ΙĮ	V _{CC} = 5.5 V,	$V_I = V_{CC}$ or GND	Inputs only			±10	μΑ
1	V _{CC} = 4.5 V,	V _I = 2 V	A D -= 0 ====	-100			
^I hold	V _{CC} = 4.5 V,	V _I = 0.8 V	A, B, or C port	100			μΑ
I _{OZ} ‡	V _{CC} = 5.5 V,	$V_O = V_{CC}$ or GND				±20	μΑ
ICC	$V_{CC} = 5.5 \text{ V}, \qquad I_{O} = 0,$	$V_I = V_{CC}$ or GND				50	μΑ
ΔI _{CC} §	V _{CC} = 5.5 V, Other inputs at V _{CC} or GND	One input at 3.4 V,				500	μΑ
C _i	V _{CC} = 5 V,	$V_I = V_{CC}$ or GND			3.5		pF
C _{io}	V _{CC} = 5 V,	$V_O = V_{CC}$ or GND			5		pF

 $[\]overline{\dagger}$ All typical values are at $T_A = 25^{\circ}$ C.

switching characteristics over recommended ranges of supply voltage and operating free-air temperature, $C_L = 50$ pF (unless otherwise noted) (see Figure 1)

DADAMETED	FROM	TO (OUTPUT)	T _A = 25°C			LINUT
PARAMETER	(INPUT)		MIN	TYP	MAX	UNIT
^t pd	A or B	С	1.5	3.7	6.2	ns
t _{pd}	С	А	1.5	3.3	5.5	ns
^t en	EN1 or EN2	С	1.5	5.3	9.5	ns
^t dis	EN1 or EN2	С	1.5	4.4	8	ns
^t en	EN2	A	1.5	6.2	10.5	ns
^t dis	EN2	A	1.5	4.8	8	ns

operating characteristics, T_A = 25°C

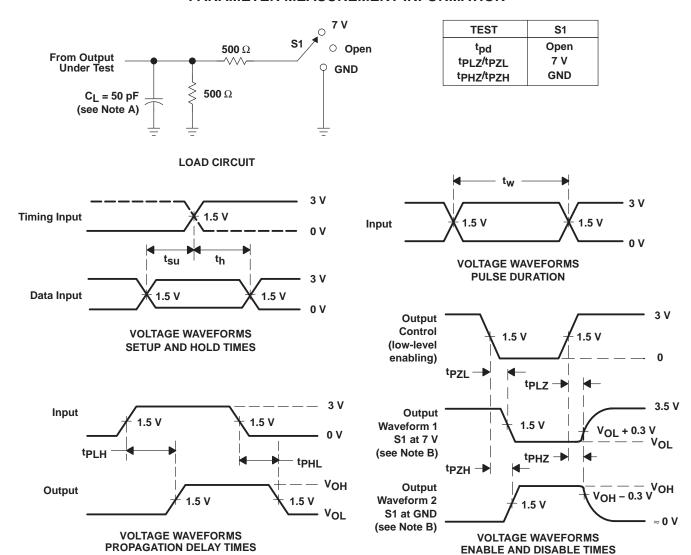
PARAMETER			TEST CONDITIONS	TYP	UNIT
C _{pd}	Power dissipation capacitance	Outputs enabled	- C _L = 50 pF, f = 10 MHz -	16	pF
		Outputs disabled		2	pF



[‡] The parameter IOZ includes the input-leakage current.

[§] This is the increase in supply current for each input that is at the specified TTL voltage level rather than VCC or GND.

PARAMETER MEASUREMENT INFORMATION



- NOTES: A. C_L includes probe and jig capacitance.
 - B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
 - C. All input pulses are supplied by generators having the following characteristics: PRR \leq 1 MHz, $Z_O = 50 \ \Omega$, $t_f \leq 2.5 \ ns$, $t_f \leq 2.5 \ ns$.
 - D. The outputs are measured one at a time with one input transition per measurement.
 - E. tpLz and tpHz are the same as tdis.
 - F. t_{PZL} and t_{PZH} are the same as t_{en} .
 - G. tpHL and tpLH are the same as tpd.

Figure 1. Load Circuit and Voltage Waveforms

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