LCX11

National Semiconductor

74LCX11 **Triple 3-Input AND Gate with 5V Tolerant Inputs**

General Description

The 74LCX11 is a triple 3-input AND gate with buffered outputs. LCX devices are designed for low voltage (3.3V) operation with the added capability of interfacing to a 5V signal environment.

The 74LCX11 is fabricated with advanced CMOS technology to achieve high speed operation while maintaining CMOS low power dissipation.

Features

- 5V tolerant inputs
- Power down high impedance inputs and outputs
- 2.0V-3.6V V_{CC} supply operation
- ±24 mA output drive
- Implements patented Quiet Series™ noise/EMI reduction circuitry

ADVANCE INFORMATION

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- Functionally compatible with 74 series 11
- Latch-up performance exceeds 500 mA
- ESD performance:
 - Human body model > 2000V Machine model > 200V

Connection and Logic Diagrams







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