Product Overview

74LVC541: Low Voltage CMOS Octal Buffer Flow Through Pinout

For complete documentation, see the data sheet.

The 74LVC541A is a high performance, non-inverting octal buffer operating from a 1.2 to 3.6V supply. This device is similar in function to the MC74LCX244, while providing flow through architecture. High impedance TTL compatible inputs significantly reduce current loading to input drivers while TTL compatible outputs offer improved switching noise performance. A V_I specification of 5.5V allows 74LVC541A inputs to be safely driven from 5V devices. The 74LVC541A is suitable for memory address driving and all TTL level bus oriented transceiver applications.

Current drive capability is 24mA at the outputs. The Output Enable(OE1bar, OE2bar) inputs, when HIGH, disables the output by placing them in a HIGH Z condition.

Features

- Designed for 1.2 to 3.6 V V_{CC} Operation
- 5 V Tolerant - Interface Capability With 5 V TTL Logic
- Supports Live Insertion and Withdrawal
- I_{OFF} Specification Guarantees High Impedance When VCC = 0 V
- 24 mA Output Sink and Source Capability
- Near Zero Static Supply Current in All Three Logic States (10 μA) Substantially Reduces System Power Requirements
- Latchup Performance Exceeds 250 mA
- ESD Performance: Human Body Model > 2000 V; Machine Model > 200 V
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Part Electrical Specifications

Product	Compliance	Status	Channels	Output	V_{CC} Min (V)	V_{CC} Max (V)	t_{pd} Max (ns)	I_O Max (mA)	Package Type
74LVC541ADTR2G	Pb-free Halide free	Active	8	3-State	1.2	3.6	6.5	24	TSSOP 20 LEAD

For more information please contact your local sales support at www.onsemi.com.

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