

DIGITAL 8000 SERIES TTL/MSI

REFER TO PAGE 18 FOR B AND E PACKAGE PIN CONFIGURATIONS.

DESCRIPTION

The 8T01 Nixie^{*} Decoder/Driver is a one-out-of-ten decoder which has been designed to provide the necessary high voltage characteristics required for driving gas-filled coldcathode indicator tubes.

It may also be utilized in driving relays or other high voltage interface circuitry. The element is designed using

LOGIC DIAGRAM



TTL techniques and is therefore completely compatible with DTL and TTL elements.

The specially designed output drivers provide the necessary stable output state. There are no input codes where all outputs are "off" or where more than one output can be turned "on."

TRUTH TABLE

<u> </u>	INPUT			
D				
0	0	0	0	0 1 2 3 4 5 6 7 8 9 8 9 8 9 8 9 8 9 8 9 8 9 8 9
0 0	0		1	
0	ň	11	0 1 0 1 0 1 0	
ŏ	1	i	l o	4
ŏ	1	ŏ	1	5
0	1	1	0	6
0 0	1	1	1	7
1	0	0	0	8
1	0	0	1	9
1	0	1	0	8
1	0		1	9
1 1	0 1 1 1 0 0 0 1	0	0	8
1	1			9
1	1	0 1 0 1 1 0 1 1 0 1 1 0 1	1 0 1 0	9

ELECTRICAL CHARACTERISTICS (Over Recommended Operating Temperature And Voltage)

	LIMITS				TEST CONDITIONS	
CHARACTERISTICS	MIN.	TYP.	MAX.	UNITS	INPUTS	OUTPUTS
"1" Output Voltage	68			v	0.8V	1.0mA
"0" Output Voltage			2.75	v	2.3V	5.0mA
"1" Input Current			40	μΑ	4.5V	
"0" Input Current (Aand D)			-0.9	mA	0.4V	
"0" Input Current (Band C)			-1.8	mA	0.4V	
Power Consumption		60		mW		

NOTES:

1. All voltage and capacitance measurements are referenced to the ground terminal. Terminals not specifically referenced are left electrically open.

2. All measurements are taken with Pin 8 tied to zero volts.

3. Positive current flow is defined as into the terminal referenced.

4. Positive NAND Logic definition:

"UP" Level = "1", "DOWN" Level = "0".

 Precautionary measures should be taken to ensure current limiting in accordance with Absolute Maximum Ratings should the isolation diodes become forward biased.

6. Manufacturer reserves the right to make design and process changes and improvements.

7. S8T01B operating temperature range is -20°C to +85°C.

SIGNETICS DIGITAL 8000 SERIES TTL/MSI - 8T01

SCHEMATIC DIAGRAM

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TYPICAL APPLICATIONS

