

January 1996

ACS521MS

Radiation Hardened 8-Bit Magnitude Comparator

Features Pinouts Devices QML Qualified in Accordance with MIL-PRF-38535 20 PIN CERAMIC DUAL-IN-LINE MIL-STD-1835 DESIGNATOR CDIP2-T20, · Detailed Electrical and Screening Requirements are Contained in LEAD FINISH C SMD# 5962-96709 and Intersil' QM Plan TOP VIEW 1.25 Micron Radiation Hardened SOS CMOS GB 1 20 VCC A0 2 19 YB Single Event Upset (SEU) Immunity: <1 x 10⁻¹⁰ Errors/Bit/Day B0 3 18 B7 (Typ) A1 4 17 A7 B1 5 16 B6 15 A6 A2 6 14 B5 B2 7 13 A5 A3 8 • Latch-Up Free Under Any Conditions 12 B4 B3 9 GND 10 11 A4 Military Temperature Range-55°C to +125°C Significant Power Reduction Compared to ALSTTL Logic DC Operating Voltage Range 4.5V to 5.5V 20 PIN CERAMIC FLATPACK MIL-STD-1835 DESIGNATOR CDFP4-F20, Input Logic Levels LEAD FINISH C - VIL = 30% of VCC Max TOP VIEW - VIH = 70% of VCC Min GB r 1 20 Input Current ≤ 1µA at VOL, VOH η ΥΒ A0 r 2 19 B0 r 3 18 B7 🖥

A1 r

B1 r

А2 г

B2 r

A3 🗖

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GND

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• Fast Propagation Delay..... 15ns (Max), 10ns (Typ)

Description

The Intersil ACS521MS is a Radiation Hardened 8 bit magnitude comparator device. It provides a low output YB when Word A equals word B and input GB is low. All other input states cause a high output.

The ACS521MS utilizes advanced CMOS/SOS technology to achieve high-speed operation. This device is a member of radiation hardened, high-speed, CMOS/SOS Logic Family.

The ACS521MS is supplied in a 20 lead Ceramic Flatpack (K suffix) or a Ceramic Dual-In-Line Package (D suffix).

Ordering Information

PART NUMBER	TEMPERATURE RANGE	SCREENING LEVEL	PACKAGE
5962F9670901VRC	-55°C to +125°C	MIL-PRF-38535 Class V	20 Lead SBDIP
5962F9670901VXC	-55°C to +125°C	MIL-PRF-38535 Class V	20 Lead Ceramic Flatpack
ACS521D/Sample	25°C	Sample	20 Lead SBDIP
ACS521K/Sample	25°C	Sample	20 Lead Ceramic Flatpack
ACS521HMSR	25°C	Die	Die

CAUTION: These devices are sensitive to electrostatic discharge; follow proper IC Handling Procedures. 1-888-INTERSIL or 321-724-7143 | Copyright © Intersil Corporation 1999

- A7

В6

٦ A6

<u>д В5</u>

_ A5

B4

- A4

Functional Diagram



TRUTH TABLE

	OUTPUT		
GB	Α	В	YB
0	A =	L	
0	A ≠ B		Н
1	Х	Х	Н

NOTE: L = Low, H = High, X = Don't Care

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Die Characteristics

DIE DIMENSIONS:

102mils x 102mils 2,600mm x 2,600mm

METALLIZATION:

Type: AlSi Metal 1 Thickness: 7.125kÅ ±1.125kÅ Metal 2 Thickness: 9kÅ ±1kÅ

GLASSIVATION:

Type: SiO₂ Thickness: 8kÅ ±1kÅ

WORST CASE CURRENT DENSITY:

<2.0 x 10⁵ A/cm²

BOND PAD SIZE:

> 4.3mils x 4.3mils

> 110µm x 110µm

Metallization Mask Layout

