CR timer BA225/BA225F/BA235/BA226/BA226F/BA236

The BA225, BA225F, BA235, BA226, BA226F, and BA236 are monolithic ICs having independent mono and multi circuits which consume very low current (0.75mA typ.). Using external resistors and capacitors, the timing control time can be set within a range from 0.01ms to 100ms.

As the BA225, BA225F, and BA235 are triggered at the rising edge of the signal, they have no trigger input differentiating circuit and can be used in measuring instruments, control devices, digital data systems and other equipment as sub-compact attachments. The BA226, BA226F, and BA236 are available as falling edge trigger types.

Applications

Delay timers Mono / multi timing controllers (ideal for VTR system controllers) Pulse generators

Features

- 1) As these are edge trigger types (BA225/BA235: rising edge trigger types, BA226/BA236: falling edge trigger types), there is no need for an input differentiating circuit.
- 2) The dual type design enables delay timer configuration.
- 3) Fewer attachments are required.
- 4) Current consumption is as low as 0.75mA for each circuit.
- 5) Uniformity in the circuit current between high and low output states simplifies the design of the power supply section.
- 6) Wide operating power supply voltage range of 4.0V to 16V.
- 7) The BA235 and BA236 pin layouts are symmetrical, allowing reverse insertion.
- 8) Hysteresis in the input results in a high level of noise withstand resistance.

Block diagram



ROHM

Standard ICs

BA225/BA225F/BA235/BA226/BA226F/BA236

●Absolute maximum ratings (Ta=25℃)

Parameter Power supply voltage		Symbol	Limits	Unit V
		Vcc	16	
Power dissipation	BA225 / BA226		800*1	mW
	BA235 / BA236	Pd	950 * ²	mW
	BA225F / BA226F	1	450 * ³	mW
Operating temperature		Topr	-20~75	ΰ
Storage temperature		Tstg	-55~125	r

*1 Reduced by 8 mW for each increase in Ta of 1°C lover 25°C .

*2 Reduced by 9.5 mW for each increase in Ta of 1°C $\,$ over 25°C .

*3 Reduced by 4.5 mW for each increase in Ta of 1°C $\,$ over 25°C .

●Electrical characteristics (unless otherwise noted, Ta=25℃, Vcc=5V)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	Measuremen Circuit
Power supply voltage	Vcc	4.0	5.0	16.0	V	_	Fig.7
Quiescent current	la	_	1.5	3.0	mA	_	Fig.7
Timing error	TERR		1	10	%	$R=100k\Omega$, $C=0.1 \mu F^{*1}$	Fig.7
Timing change with power supply	Tos	_	0.5	3.0	%	5V→16V	Fig.7
Timing change with temperature	Тот	_	200		ppm/°C	_	Fig.7
	6 V _T -	1.0	2.0	3.0	- V *2		
Trigger voltage BA236		0.7	1.6	2.5		** E	Fig.7
Trigger current	h	_	70	200	μA	VT = 3.0V (BA225 / BA235) VT = 2.5V (BA226 / BA226F / BA236)	Fig.7
"L" output voltage	Vol	_	0.5	1.0	V	Isink=5mA	Fig.7
"H" output voltage	Vон	3.0	4.0		V	Isource==5mA	Fig.7

*1 One-shot cycle T= 0.5 CR

*2 With input hysteresis (hysteresis width≑200 to 600 mV)



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Standard ICs

BA225/BA225F/BA235/BA226/BA226F/BA236



S; BA225 BA225F OUT BA235 GND CR ۶G 100k Q Counter 0.1 μ F

Fig.7

Description of attachments

(1) Timing capacitors (Pins 2 and 6)

б.V

Connected between these pins and the ground (GND), this capacitor determines the one-shot period. We recommend using a capacitor with a value greater than 1000pF. The one-shot cycle is determined by T = 1/2C·R.

(2) Timing resistor (Pins 2 and 6)

Connected to the power supply line, these resistors determine the one-shot period. The timing capacitor is charged through this resistor. The recommended range for the timing resistor is $10k\Omega$ to $1M\Omega$.

(3) Power supply capacitor (Pin 8)

Connect a 0.02 µ F capacitor between the power supply pin and the ground (GND) so as to prevent malfunction resulting from a rise in the impedance of the power supply line.

(4) Load (Pins 1 and 7)

A load of up to 5mA can be accommodated for both the inflow current and the outflow current.

CR timers

Operation notes

(1) Usage range for voltage

The recommended voltage range is 4.0V to 16V. Use of these ICs at 3.0V or lower voltage may cause the same mode as the input trigger signal to be output at around 2.6V.

(2) Input trigger

The input trigger level is 2.0V. The LOW level is 1.0V or lower and the HIGH level is 3.0V or higher. Trigger signals of 10ms / V or lower should be used for both the rising and falling edges.

(3) Time constant determining C·R

The recommended value for the timing capacitor is 1000pF or more and that for the timing resistor is from $10k\Omega$ to $1M\Omega$.

Application example

Figure 8 shows an example of the circuit including these ICs, while Figure 9 shows the basic operation timing chart. Figure 10 shows the basic operation timing chart of the falling edge trigger type IC. When no trigger signal is applied, the output is in the LOW state and the timing capacitor is in the discharged state. When a trigger signal is applied, its rising edge causes the output to become HIGH and the timing capacitor to be charged.

The charging time of this timing capacitor is determined by the time constants of the external timing resistor and the timing capacitor. When the charged voltage reaches $0.4 \times Vcc$, the flip-flop in the IC is reset and the output state changes from HIGH to LOW. At the same time, the timing capacitor is discharged to be ready for the next operation. The voltage, which is determined by comparison of discharge resistance and timing resistance in the IC, remains on the C and R pins. If timing resistance is set at 5k Ω or less, this voltage will reach threshold level (0.4 × Vcc), and operation will come to a halt. Also, when timing resistance is set to a low value, voltage remains on the C and R pins, which shortens the time constant. If timing resistance is set to 2M Ω or higher, the IC's internal comparator cannot be driven, which may bring operation to a halt.

(4) Note that pin connections and pin arrangements on the BA225 / BA226 (BA235F / BA226F) differ from those on the BA235 / BA236.



Fig.8

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BA225/BA225F/BA235/BA226/BA226F/BA236

Application examples



(Falling edge trigger)

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Standard ICs

BA225/BA225F/BA235/BA226/BA226F/BA236



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