# Audio ICs

# 3V dual pre/power amplifier BA3513AFS

The BA3513AFS is a dual, pre/power amplifier designed for headphone stereo applications. It has all of the basic signal circuits required for tape players, and operates off a 3V supply.

The auto-reverse-compatible preamplifier block and fixed-gain power amplifier blocks are independent to facilitate noise reduction.

The preamplifier block can be direct-coupled, and the power amplifiers do not require bootstrap capacitors, and use a fixed-gain negative feedback circuit to reduce the number of external components required and allow compact and reliable set designs.

### Applications

3V headphone stereos and 3V radio cassette players.

## Features

- 1) Dual preamplifiers and power amplifiers on one chip.
- 2) Preamplifier suitable for auto-reverse use.
- 3) Transistor switch provided for metal-tape muting.
- Power amplifier gain is optimized for noise reduction.
- 5) Radiation prevention pin provided.

# Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits	Unit	
Supply voltage	, Vcc	4.5	V mW	
Power dissipation	Pd	800*		
Operating temperature Topr		-25~75	°C	
Storage temperature Tstg		-55~125	°	

\* When mounted on a 90mm x 50mm x 1.6mm glass-epoxy PCB, reduced by 8.0mW for each increase in Ta of 1°C over 25°C

## Recommended operating conditions (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	Vcc	1.8	2.4	3.6	v

ROHM

323

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Low-frequency amplifiers

BA3513AFS

# Block diagram



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# •Electrical characteristics (unless otherwise specified $Ta = 25^{\circ}C$ , $V_{cc} = 2.4V$ and f = 1kHz)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	Measuremen Circuit	
Quiescent circuit current	la	-	8	14	mA	V <sub>IN</sub> =0V <sub>rms</sub> , 14, 18pin Open	Fig.7	
$\langle Preamplifier \rangle R_L = 10 k \Omega$						· · · ·	•••••	
Open-circuit voltage gain	Gvo	72	78	-	dB	V <sub>0</sub> =-10dBm		
Maximum output voltage	Vom	200	300	-	mVrms	THD=1%		
Total harmonic distortion	THD <sub>1</sub>	-	0.03	0.15	%	Vo=0.2Vms, NAB33dB		
Input conversion-noise voltage	V <sub>NIN</sub>	_	1.0	1.8	μ Vrms	Rg=2.2kΩ, BPF20~20kHz		
Ripple rejection	RR <sub>1</sub>	40	47		dB	V <sub>RR</sub> =-20dBm, f=100Hz NAB33dB, Rg <del>=</del> 2.2kΩ	Fig.7	
Forward-reverse crosstalk	CT <sub>F-B</sub>	65	75.5	_	dB	Single channel Vo = -10dBm $R_g$ =2.2k $\Omega$ , BPF20~20kHz	_	
Input bias current	I <sub>B1</sub>	-	60	300	nA	VIN=0Vrms		
$\langle Power amplifier \rangle R = 16 k \Omega$							•	
Rated output	Ролт	30	40	_	mW	THD=10%		
Closed-circuit voltage gain	Gvc	24.7	26.7	28.7	dB	V <sub>IN</sub> =-40dBm	1	
Total harmonic distortion	THD₂	-	0.2	1.0	%	P₀=1mW	·	
Output noise voltage	VNO	i	30	39	μVrms	R₀=0Ω、BPF20~20kHz	Fig.7	
Ripple rejection	RR <sub>2</sub>	45	58	_	dB	$V_{RR}$ =-20dBm, f=100Hz, Rg=0 $\Omega$		
Input resistance	Rin	21.4	30	38.6	kΩ			
Input bias current	B2	-	22	80	nA	V <sub>IN</sub> =0V <sub>rms</sub> , R <sub>g</sub> =10kΩ <sup>*1</sup>		
Channel balance	СВ	+	0	0.7	dB	Vo=-10dBm		
Switching transistor ON resistance	Rта	_	6.0	18	Ω	14pin GND, 2pin, 23pin		
Preamplifier + power amplifier (connec	tion as per a	application	n example	circuit)				
Channel separation	CS	37	47	_	dB	$P_{re}-R_g=2.2k\Omega$ , VR Max.* <sup>2</sup> Single channel Power-Vo = -5dBm BPF20~20kHz		
Leakage from preamp to power amp for signal leak VR Min.	SL	-	-63	-57	dBm	Pre-Vo=-12dBm VR Min.*3, When both channels are operating	Fig.7	

\*1 
$$I_{B2} = \frac{V_{B2}}{10k\Omega} \times \frac{4}{3}$$

 $V_{B2}$ : Voltage at each end of Ag (10  $\Omega$  ).

\* 2 0dB attenuation from the preamplifier output to power amplifier input.

\*3 Power amplifier signal source impedance is  $0 \Omega$ .

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325

Pre/power an

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326

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Measurement circuit







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327





1



BA3513AFS

External dimensions (Unit: mm)





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