# IF detector for pagers BA4118FV

The BH4118FV is an IF detector IC developed for use with pagers.

## Applications

**Pagers** 

#### Features

- 1) Internal detector circuit and IF amplifier circuit which accommodates 455kHz.
- 2) Low current consumption of 550  $\mu$  A.
- 3) Built-in RSSI function.

- 4) Built-in FSK waveform rectification circuit.
- 5) Handles transmission at 1200bps.
- 6) Equipped with internal battery alarm.
- 7) Built-in constant-voltage source.

## ● Absolute maximum ratings (Ta = 25°C, for measurement circuit)

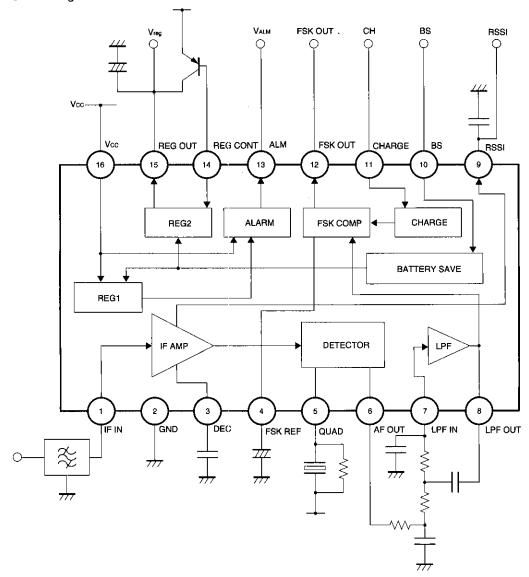
Parameter	Symbol	Limits	Unit
Power supply voltage	Vcc	5.0	٧
Power dissipation	Pd	350 *1	mW
Operating temperature	Topr	<b>−30~</b> +85	°C
Storage temperature	Tstg	<b>−</b> 55∼ <b>+</b> 125	°C

<sup>\*1</sup> Reduced by 3.5mW for each Increase in Ta of 1°C over 25°C.

## Operating range

Parameter	Symbol	Limits	Unit
Operating voltage	Vcc	1.1~3.5	V

# Block diagram



• Electrical characteristics (Unless otherwise noted, Ta=25°C, Vcc=1.4V,  $f_{\text{IN (IF)}}$ =455kHz,  $\Delta f$ = $\pm 4$ kHzdev, fmod = 600Hz)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Quiescent current	lo .	_	550	800	μΑ	No input
Battery save quiescent current	lo (es)		0	5	μA	No input
IF amplifier input resistance	Rif		2	_	kΩ	
Signal-to-noise ratio 1	S/N1	50	62	_	dB	V <sub>IN (IF)</sub> =60dB $\mu$ V EMF
Signal-to-noise ratio 2	S / N2	28	33	_	dB	Vin (iF) =25dB $\mu$ V EMF
-3 dB limiting sensitivity	L.S	19	24	29	dB μV EMF	. <b>–</b>
Demodulation output level	Voo	20	30	44	mVrms	Vin (if) =60dB µV EMF
AM spression ratio	AMR	35	45	_	dB	Vin (iF) =60dB μ V EMF AM=30%
Waveform rectification output duty ratio	DR	40	50	60	%	Vin (if) =60dB μV EMF
Alarm detection voltage	VALM	1.05	1.10	1.15	٧	13pin "H "
Regulator output voltage	VREG	0.95	1	1.05	V	RL=430 Ω
"L" output voltage (ALM)	VALM L	_	0	0.4	V	IL=100 μA
"H" leakage current (ALM)	lalm H	_	0	2	μA	
"L" output voltage (FSK)	VFSKL	_	0	0.4	V	IL=100 μA
"H" leakage current (FSK)	lesk H		0	2	μΑ	
"L" output voltage (REG CONT)	VREGL	_	_	0.6	v	L=100 μA
RSSI output voltage	Vassi	0.48	0.62	0.76	ν	V <sub>IN</sub> =80dB μ V EMF
RSSI output resistance	Ressi	_	82	_	kΩ	_
Rapid charging/discharging current	Існ	35	65	110	μΑ	V4=GMD,V8=0.18V

# Measurement circuit IALM=V/100k Irsk=V/100k # § 10003 SW11 SW10 SW8 SW6 SW5 SW4 12 11 10 ALARM REG2 FSK COMP CHARGE BATTERY SAVE REG1 DETECTOR GND 2 SW3 SW1 (V) S/N, LS, Voo, AMR V1 0V , 0.36V Unit Resistance : $\Omega$ Capacitor : $\mu$ F

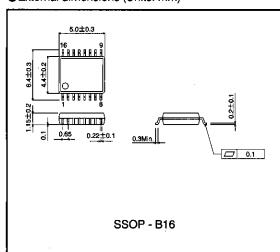
Fig. 1

66

# Measurement circuit switches

Parameter	Meter	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8	SW9	SW10	SW11	V1
Quiescent current	la	1	2	3	1	1	1	1	1	1	2	2	0
Battery save quiescent current	la	1	1	1	2	1	2	1	2	1	2	2	0
Signal-to-noise ratio 1	S/N	1	1	1	1	1	1	1	1	1	1	1	0
Signal-to-noise ratio 2	S/N	1	1	1	1	1	1	1	1	1	1	1	0
-3 dB limiting sensitivity	L.S	1	1	1	1	1	1	1	1	1	1	1	0
Demodulation output level	Von	1	1	1	1	1	1	1	1	1	1	1	0
AM suppression ratio	AMR	1	1	1	1	1	1	1	1	1	1	1	0
Waveform rectification output duty ratio	DR	1	1	1	1	1	1	1	1	1	1	1	0
Alarm detection voltage	VALM	1	1	1	1	1	1	1	1	1	1	1	0
Regulator output voltage	VREG	1	1	1	1	1	1	1	1	1	1	1	0
"L" output voltage (ALM)	VALM L	1	1	1	1	1	1	1	3	1	1	1	0
"H" leakage current (ALM)	falm H	1	1	1	1	1	1	1	1	2	1	1	0
"L" output voltage (FSK)	VFSKL	2	3	2	1	1	3	1	1	1	1	1	0
"H" leakage current (FSK)	lfsk h	2	3	2	1	1	1	2	1	1	1	1	0.36
"L" output voltage (REG CONT)	VREG L	1	1	1	1	1	1	1	1	1	3	2	0
RSSI output voltage	Vassi	1	1	1	1	1	1	1	1	1	1	1	0
Rapid charging/discharging current	Існ	2	3	2	1	2	1	1	1	1	1	1	0

# External dimensions (Units: mm)



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