Optical disc ICs

4-channel BTL driver for CD players BA6997FP/BA6997FM

The BA6997FP and BA6997FM, both designed for CD players, have an internal 4-channel BTL driver and 5V regulator (which requires attached PNP transistor), as well as switches for the 5V regulator and temperature monitor pins.

Applications

CD players and other optical disc devices

Features

- 1) 4-channel BTL driver for CD players.
- 2) Wide dynamic range.
- 3) Internal thermal shutdown circuit.

- 4) Internal level shift circuit, for a minimum of attached components.
- 5) Internal 5V regular with switch.

●Absolute maximum ratings (Ta=25℃)

	v	• •		
Parameter Power supply voltage		Symbol	Limits	Unit
		Vcc	18	v
Power dissipation	BA6997FP		1800*1 *2	
	BA6997FM	Pd –	2200*1 *3	- mW
Operating temperature range		Topr	-40~85	°C
Storage tem	perature range	Tstg		ĉ

*1 When mounted to a 70 × 70 × 1.6 mm glass epoxy board with less than 3% copper foil.

*2 Reduced by 14.4 mW for each increase in Ta of 1°C over 25°C.

*3 Reduced by 17.6 mW for each increase in Ta of 1°C over 25°C.

Recommended operating conditions (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	
Power supply voltage	Vcc	4.5	—	12	V	
	Vcc*4	6.0	_	12	Y	

*4 When using the regulator

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Block diagram



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Pin No.	Pin name	Function	
1	OUT1-A	Channel 1 driver output	
2	OUT2-B	Channel 1 driver output	
З	IN1	Channel 1 input	L
4	IN1'	Adjusting channel 1 gain	
5	REG-B	Base connection for regulator power transistor	
6	REG · OUT	Base connection for regulator output power transistor	
7	REG · GND	Regulator ground/common circuit ground*1	
8	BIAS	Bias input	
9	MUTE	Mute control	
10	REG · SW	Regulator switch	
11	TEMP · MON	Temperature monitor *2	
12	IN2	Channel 2 input	
13	OUT2-B	Channel 2 driver output	;
14	OUT2-A	Channel 2 driver output	
15	GND	Substrate ground	
16	OUT3-A	Channel 3 driver output	\
17	OUT3-B	Channel 3 driver output	0
18	IN3"	Adjusting channel 3 gain	ö
19	IN3'	Adjusting channel 3 gain	
20	IN3	Channel 3 input	<u> </u>
21	Vcc	Vcc	
22	Vcc	Vcc	č
23	IN4	Channel 4 input	0
24	IN4'	Adjusting channel 4 gain	
25	IN4"	Adjusting channel 4 gain	
26	OUT4-B	Channel 4 driver output	
27	OUT4-A	Channel 4 driver output	
28	GND	Substrate ground	

*2 See "Precautions for use."

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•Electrical characteristics (Unless otherwise noted, Ta=25°C, Vcc=8V, f=1kHz, RL=8 Ω)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions			Measurement Circui
Quiescent current 1	IQ1	5.5	11.0	14.5	mA	No load, mute off, regulator on			Fig. 1
Quiescent current 2	IQ2	-	-	6.0	mA	No load, mute on, regulator off			Fig. 1
(Drivers)									
Output offset voltage	V00	-100	-	100	mV	Preamplifier buffer configured		Fig. 1	
Max. output amplitude	VOM	5.0	5.4	_	۷	· · · · · · · · · · · · · · · · · · ·			Fig. 1
Closed loop voltage gain 1	GVC1	14.1	15.6	17,1	dB	Vin = 0.1 Vrms, channels	s 1 and 2		Fig. 1
Closed loop voltage gain 2	GVC2	13.6	15.6	17.6	dB	Vin = 0.1 Vrms, channels	s 3 and 4		Fig. 1
MUTE-OFF voltage	VMOFF	2.0	-	—	۷				Fig. 1
MUTE-ON voltage	VMON	-	-	0.5	v				Fig. 1
Ripple rejection ratio	RR	-	60	-	dB	Vin=0.1Vrms, 100Hz	Preamplifier buffer configured		Fig. 1
Interchannel crosstalk	СТ	-	60	-	dB	Vin=0.1Vrms, 1kHz			Fig. 1
(Pre-stage operationalamplifier)							•		
Input bias current	ŀВ	-	-	300	nA				Fig. 1
Common mode input voltage	VICM	0	-	6.8	٧			Fig. 1	
Output voltage, H level	VOHOP	6.6	7.1	-	v				Fig. 1
Output voltage, L level	VOLOP	_	0.8	1.1	٧	Ground at 100Ωoutput CH2		Fig. 1	
Output current, H level	IOHOP	2	_		mA			CH2	Fig. 1
Output current, L level	IOLOP	5	-	-	mA	V_{CC} at 100 Ω output		Excluding	Fig. 1
Slew rate	SROP	-	2	-	V/µS			channel 2	Fig. 1
(5 V regulator)									
Output voltage	VREG	4.75	5.00	5.25	V	I∟=100mA			Fig. 1
Output load	∆VRL	-50	0	10	mV	l∟=0~200mA			Fig. 1
Supply voltage	∆VVcc	-10	· 0	25	m۷	(Vcc=6~9V)IL=100mA		Fig. 1	
Ripple rejection ratio	RRREG	-	50	_	dB	V _{IN} =1Vp-p 1kHz			Fig, 1
Regulator switch-on voltage	VREGON	2.0	-	_	۷				Fig. 1
Regulator switch-off voltage	VREGOFF	ł		0.5	٧				Fig. 1

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Fig. 1

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Operation notes

(1) The BA6997FP and BA6997FM have an internal thermal shutdown circuit. Output current is muted when the chip temperature exceeds 175°C (typically).

(2) The output current can be muted by opening the mute pin (9 pin) voltage or lowering it below 0.5V. This pin should be pulled up above 2.0V during normal operation. When muting occurs, the output pins output the internal bias voltage, roughly Vcc/2.

(3) The regulator can be turned off by opening the regulator switch (10 pin) or lowering it below 0.5V. This pin should be pulled up above 2.0V during normal operation.

(4) Muting also occurs when the bias pin (8 pin) voltage drops below 1.4V (typically). This pin should stay above 1.6V during normal operation.

(5) Attach a bypass capacitor (roughly 0.1 μ F) to the power supply, at the base of the IC.

(6) Be sure to connect the radiating fin to an external ground.

(7) The capacitor between regulator output (6 pin) and REGGND (7 pin) also serves to prevent oscillation of the IC, so select one with good temperature characteristics.

(8) We recommend 2SB1132 as the PNP transistor to attach to the regulator.

(9) The internal circuitry of the temperature monitor pin is shown in the diagram below. Note that the internal reference voltage is also used for the 5V regulator, which will cease to operate normally when the temperature monitor pin emits a current exceeding the regulator's capacity. Set I to several dozen μ A.



Fig. 2



Fig. 3 Tj vs. Vmon

Electrical characteristic curves

rdhm

(variable load)







Fig. 7 Supply voltage vs. operational amplifier output drive current



External dimensions (Units: mm)

BA6997FP/BA6997FM

Optical Disc ICs

CD/CD-ROM Drivers (4 channels)

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