Speech network for telephones BA8216

The BA8216 is a speech network IC which possesses the basic functions required for handset communications. In addition to amplifying signals from a transmitter and sending them to a telephone line, it also amplifies only reception signals from a telephone line and drives the receiver.

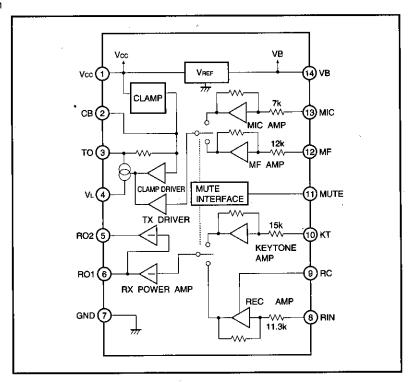
Applications

Telephones and telephone equipment

Features

- 1) Basic speech network functions built in.
 - · Handset transmit and receive circuits
 - · DTMF transmit circuit
 - · Key tone input circuit
 - Mute control and side tone masking circuits
- 2) Can be used with 1.3k Ω loop circuit resistance and 100 Ω telephone resistance.
- A BTL circuit is used for reception output, providing a wide dynamic range which enables use of a ceramic receiver.
- 4) Few external components are used.
- 5) 14-pin DIP package.

Block diagram



Speech network

●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Line voltage	VL	18		
Line current	IL I	135	mA	
Power dissipation	Pd	900*	mW	
Operating temperature	Topr	-25~75	°C	
Storage temperature	Tstg	-55~125	°C	

ullet Electrical characteristics (Unless otherwise noted, Ta=25°C , S1=1, S2=1, f=1kHz, BPF=400Hz to 30kHz)

Parameter		Symbol	Min.	Тур.	Max.	Unit	I∟ (mA)	Conditions	Measurement Circuit		
Line voltage (20)			VL20	3.8	4.15	4.5	٧	20			
Line voltage (120)			V _{L120}	4.1	4.6	5.5	٧	120		Fig.11	
"H" input voltage			Vін	0.8	_	1	٧	40	S ₂ =2		
"L" input voltage			VIL	. –	_	0.5	٧	40	S ₂ =2		
"H" input current			Ін	100	200	300	μA	40	S2=2, VIH=4V		
stromagnetic		Gain	GRD	-13.8	-10.8	-7.8	dВ	40	V⊤=20dBV	Fig.12	
	Rece- iving	Maximum output	VRD	-19	—15	-	dBV	20	THD=5%		
		Input impedance	Zrin	8.3	11.3	14.3	kΩ	40			
		Gain	Скто	10.5	13.5	16.5	dB	40	S ₂ =3, V _{KT} =-40dBV	Flg.12	
	кт	Maximum output	Vкто	-19	—1 5	_	dBV	20	S ₂ =3, THD=5%		
		Input impedance	Zĸτ	11	15	19	kΩ	40			
ezoelectric		Gain	Gap	0.9	3.9	6.9	dB	40	S ₁ =2, V _T =-20dBV	- Fig.12	
	Rece	Maximum output	Vap	1	5	-	dBV	20	S1=2, THD=5%		
	iving	Input impedance	ZRIN	8.3	11.3	14.3	kΩ	40			
		Gain	Gктр	25.1	28.1	31.1	dB	40	S ₁ =2, S ₂ =3 V _{KT} =-40dBV	Fig.12	
	кт	Maximum output	Vктр	1	5	_	dBV	20	S ₁ =2, S ₂ =3 THD=5%		
		Input Impedance	Zĸτ	11	15	19	kΩ	40]	
		Gain	Сміс	19.6	22.6	25.6	dB	40	V _M =-40dBV	Fi- 40	
	МІС	Maximum output	Vміс	0	4	-	dBV	20	THD=5%	Fig.13	
Ē		Input impedance	Zмic	5	7	9	kΩ	40			
ransmit		Gain	. Смг	21.1	24.1	27.1	dB	40	S2=3, VD=-40dBV	Ele 49	
F	MF	Maximum output	Vмғ	0	4	-	dBV	15	S ₂ =3, THD=5%	Flg.13	
	ļ	Input impedance	ZMF	9	12	15	kΩ	40			
Mute ratio *			MRRD	30	35	-	dB	40	V⊤=-20dBV S₂=1→3	- Fig.12	
			MRRP	30	35	_	dB	40	V _T =-20dBV S ₂ =1→3, S ₁ =2		
			МЯміс	60	67	_	dB	40	V _M =-40dBV S ₂ =1→3	Fig. 10	
			МВмя	60	67	_	dB	40	V ₀ =-40dBV S ₂ =3→1	- Fig.13	
			ΔGRD	_	-6	_	dB		See Flg. 14	Fig.14	
Attenuation during branch		ΔGRP	-	-5		dB	_	See Fig. 14			
-			Δ С міс	-	— 15	-	dB	_	See Fig. 14		
Noise level			Nad	<u> </u>	-75	_	dBV	120	V⊤=0	Fig.12	
			NRP	_	-73	<u> </u>	dBV	120	S1=2, V1=0		
			NMIC		-74	-	dBV	120	V _M =0	F:- 40	
			NMF	 	—71	<u> </u>	dBV	120	S ₂ =3, V _D =0	Fig.13	

^{*} When using 1 kHz bandpass filter

●Electrical characteristic curves

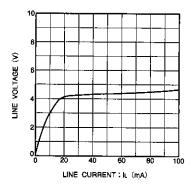


Fig. 1 Line voltage vs. line current characteristic

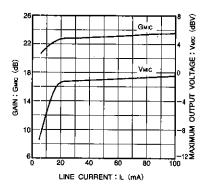


Fig. 2 MIC Line current characteristic

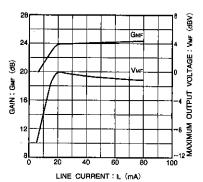


Fig. 3 MF Line current characteristic

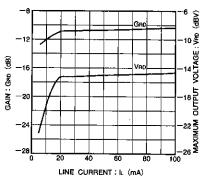


Fig. 4 Electromagnetic receiving Line current characteristic

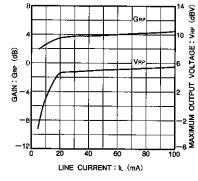


Fig. 5 Piezoelectric receiving Line current characteristic

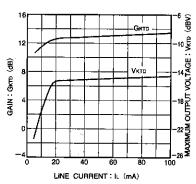


Fig. 6 Electromagnetic KT Line current characteristic

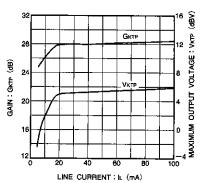


Fig. 7 Piezoelectric KT Line current characteristic

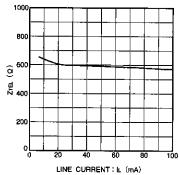


Fig. 8 AC impedance Line current characteristic

Measurement circuits

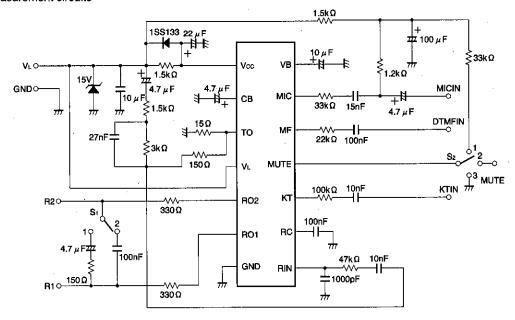


Fig. 9 Basic measurement circuit

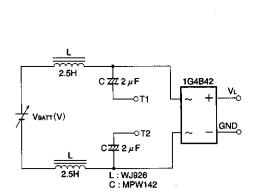


Fig. 10 Trunk circuit

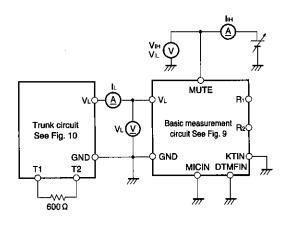


Fig. 11 DC characteristics measurement circuit

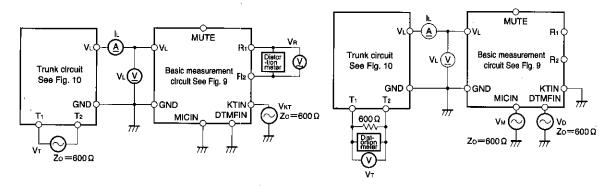


Fig. 12 Reception system measurement circuit

Fig. 13 Transmission system measurement circuit

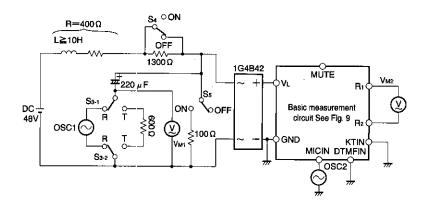


Fig. 14 Branch measurement circuit

Measurement method (MUTE = HIGH)

- (1) Reception
- Set S₃ to R and S₄ and S₅ to OFF, and input a 1kHz signal from OSC1. Adjust to -10dBV if V_{M₂} is piezoelectric, and to -30dBV if V_{M₂} is electromagnetic.
- With S₄ OFF and S₅ ON, record the output level of V_{M2} and note this value as V_{M2} (2).
- 3) With S4 ON and S5 ON, measure the output level of V_{M2} , and note this value as V_{M2} \Im .

 $\Delta G_R = 20 \log (V_{M2})/V_{M2}$

- (2) Transmission
- Set S₃ = T and S₄ and S₅ to OFF, input a 1kHz signal from OSC2, and adjust so that V_{M1} is −10dBV.
- With S₄ OFF and S₅ ON, record the output level of V_{M1} and note this value as V_{M1} ②.
- 3) With S₄ ON and S₅ ON, measure the output level of V_{M1}, and note this value as V_{M1} ③.

 $\Delta GMIC = 20log (V_{M1} ③/V_{M1} ②)$

- S₃: Send and receive switch (reception side)
- S_4 : ON/OFF switch for line resistance (1300 Ω) (OFF)
- S_5 : ON / OFF switch for parallel resistance (100 Ω) (OFF)

Circuit opperation

The BA8216 carries out the following basic operations.

(1) Handset talk and receive

The BA8216 receives a voice signal from a telephone line and outputs it to a handset speaker. It also takes a voice signal from the handset microphone and outputs it to the telephone line.

(2) DTMF send and key tone input

The BA8216 has a mute switch which can be switched between a "handset send and receive" mode and a "DTMF send and key tone input" mode by an external logic signal. In the "DTMF send and key tone input" mode, it transmits DTMF signals from the dial pad to the telephone line, and key tones to the handset receiver.

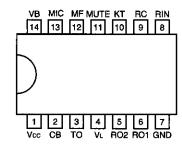
Operation notes

The maximum power dissipation for the BA8216 is 900mW. Since the maximum power dissipation varies with temperature, the product of the applied voltage V_L and the total current drawn by the IC, after factoring in the temperature, should not exceed the maximum dissipation.

Pin description

Pin No.	Symbol	Name	Function
1	Vcc	Internal power supply pin-	Internal power supply pin. Power is supplied from VL through resistor R ₁₀₁ , and is smoothed by capacitor C ₁₀₁ .
2	СВ	Bypass capacitor connector pin	This is used to connect an AC bypass capacitor to form a DC feedback loop for stabilizing the DC potential of the VL pin.
3	то	Transmit/power dissipation resistor connector pin	This is connected between the power dissipation resistor R _{10s} and the GND, to eliminate unnecessary power consumption. At the same time, R _{10s} determines the gain of the final output stage of the transmission driver. R ₁₀₄ and R ₁₀₅ form two legs of the side tone suppression bridge, which is also connected to this pin.
4	VL	V _L pin	This is the power supply pin. The transmit signal is output to the telephone line through this pin. It is connected to the (+) side of the diode bridge.
5	RO ₂	Receive output pin	When a piezoelectric receiver is used, connect a 330- protection resistor R_{191} to this pin. When a dynamic receiver is used, R_{191} may be 0Ω .
6	RO ₁	Receive output pin	When a piezoelectric receiver is used, connect a 330- protection resistor R_{106} to this pin. C_{191} is shorted. When a dynamic receiver is used, a 4.7 μ F DC blocking capacitor (C_{191}) is connected in series with the 680Ω resistor (R_{106}) to this pin.
7	GND	Ground pin	This pin has the lowest potential on the IC. It is connected to the () pin of the diode bridge.
8	RIN	Receive input pin	After passing through a side tone suppression circuit, the receive signal frothe telephone line is input to this pin.
9	RC	Receive amplifier bypass capacitor pin	This is connected to the AC bypass capacitor of the reception amplifier.
10	KT	Key tone input pin	When the MUTE pin is low, key tone signals input on this pin are transmitted to the handset speaker.
11	MUTE	Mute input pin	When this is high, hand-set transmission is normal. When this is low, DTMF signals applied at the MF input are output to the telephone line, and key tones applied to the KT pin are transmitted to the hand receiver.
12	MF	DTMF signal input pin	When the MUTE pin is low, DTMF signals input to this pin are output to the telephone line.
13	MIC	Microphone input pin	Used to input signals from the microphone.
14	VB	Bias pin	This is the IC internal bias pin. It is connected to the bypass capacitor C ₁₁₁ .

●Pin layout



Mute control input logic

MUTE	MIC AMP	MF AMP	REC AMP	KT AMP	
Н	ON	OFF	ON	OFF	
L	OFF	ON	OFF	ON	

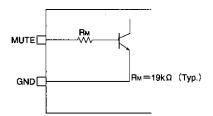


Fig. 15 Mute input equivalent circuit

Application example

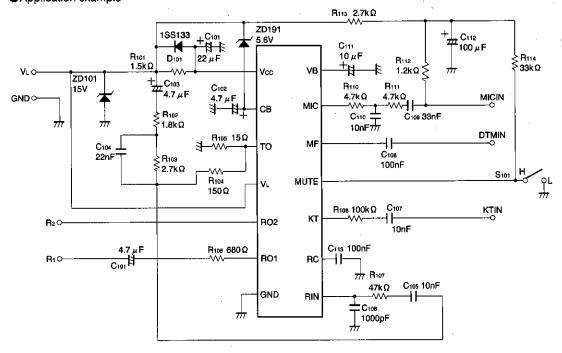
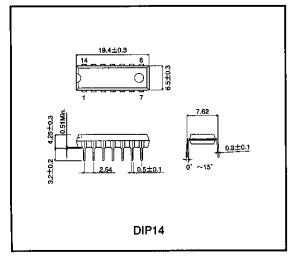


Fig. 16

External dimensions (Units: mm)



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