# Single-chip 4-bit Microcontroller for CD-DA BU34440

The BU34440 is a single-chip 4-bit microcontroller designed for CD-DA and contains parallel I/O, serial I/O, timer/counter and all other functions required for CD control in a single compact package.

### Applications

Portable CD-DA devices, portable CD stereos

### Features

- Low-voltage, high-speed operation (V<sub>00</sub>=2.3~5.5V at 4.4MHz).
- 2) 4kByte ROM, 256 nibble RAM.
- 3) Internal 8-bit SIO (LSB first).
- 4) 14 programmable pull-up input/outputs.
- 5) 5 programmable pull-up inputs.



## ●Absolute maximum ratings (Ta=25℃)

Parameter	Symbol	Limits	Unit	
Power supply voltage	VDD	-0.3~7.0	V	
Power dissipation	Pd	500*	mW	
Operating temperature	Topr	-25~75	Ĉ	
Storage temperature	Tstg	-55~125	ĉ	

\* Reduced by 5.0 mW for each increase in Ta of 1°C over 25°C.

Recommended operating conditions (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	
Power supply voltage	VDD	2.3	-	5.5	V	
Input voltage, HIGH (without hysteresis)	₩	0.7Vdd		VDD	V	
Input voltage, LOW (without hysteresis)	V⊫	0	_	0.3Vdd	٧	
Input voltage, HIGH (with hysteresis)	Vінs	0.75Vdd	_	VDD	v	
Input voltage, LOW (with hysteresis)	Vils	0	_	0.25Vpd	V	

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CPU02A

OSC03A

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RESET

RAM 256X4 BIT

PC INC

PC STACK 8

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INSTRUCTION DECODER

ROM 4096X8 BIT

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INTERRUPT CONTROL

L-BUS U-BUS

STK

SYSTEM CLK

Block diagram



TMR03B

STP04A

BIX03A

PI011A

PI011A

PЮ11A

P1012A

S1004B

W0~W3

BIN

→ P00~P03

→P10~P13

→ P20~P23

→P30~P31

→SCK →SIO —SIN

IRQ€

IRQ ← STP←

IRQ ← STP←

IRQ←



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\* No internal PROM \* The address bus and data bus do not output externally (addressing to external memory is not possible). \* 4-bit ALU

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Pin No.	Pin name	1/0	Function	Туре		
4~7 8~11 22~25	P00~P03 P10~P13 P20~P23 (block Pl011A)	1⁄0	<ul> <li>4-bit input and output.</li> <li>Each bit is programmable for input or output (open drain output N-channel).</li> <li>Pull-up resistor ON/OFF operation is programmable (each bit can be set separately).</li> <li>Resetting turns the pull-up resistors off via input.</li> </ul>			
26~27	P30~P31 (block Pl012)	I⁄0	<ul> <li>2-bit input and output.</li> <li>Each bit is programmable for input or output (open drain output N-channel).</li> <li>Pull-up resistor ON/OFF operation is programmable (each bit can be set separately).</li> <li>Resetting turns the pull-up resistors off via input. * 1</li> </ul>	D		
18~21	W0~W3 (block STP04A)	I	<ul> <li>Standard 4-bit input.</li> <li>Programmable for stop cancel input or interrupt request. signal output (each bit can be set separately).</li> <li>Pull-up resistor ON/OFF operation is programmable (each bit can be set separately).</li> <li>Resetting turns the pull-up resistors off.</li> </ul>	С		
17	BIN (block BIX03A)	1	Standard 1-bit input.     Programmable for stop cancel input or interrupt request signal output.     Pull-up resistor ON/OFF operation is programmable.     Resetting turns the pull-up resistors off.	С		
12	SIN	<u> </u>	• 8-bit serial data input.	Α		
14	SIO	١⁄٥	<ul> <li>8-bit serial data input/output.</li> <li>Programmable selection of input/output.</li> </ul>	E		
13	SCK (block SI004B)	1/0	<ul> <li>Clock input/output for sending and receiving serial data.</li> <li>Programmable selection from among 3 int. clocks and 1 ext. clock.</li> </ul>	E		
1	OSC1	1	Oscillator input.     External clock input.			
2	OSC2 (block OSC03A)	0	Oscillator output.			
15	TEST	I.	Test input (This is a chip test pin that contains an internal pull-down resistor and so should normally remain open.)			
16	RESET		Reset input (Setting this pin to LOW resets the CPU.)	A		
28	VDD		Power supply pin.			
3	GND	_	• Ground pin.			

Type: Refer to "Input and output equivalent circuits."

\*1 Because these pins reach high impedance immediately after resetting, some applications may require pin processing.

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●Electrical characteristics (unless otherwise noted, Ta=25℃, VDD=5V)

Parameter	Symbol	Pin	Min.	Тур.	Max.	Unit	Conditions
STOP circuit current	Іррэт		-	—	1	μA	STOP mode
HALT circuit current	Іорнт			1	_	mA	HALT mode     fosc=4.4MHz
Operational circuit current	IDDOP		_	4	-	mA	• fosc=4.4MHz
Clock frequency	fosc	OSC1, OSC2	2	_	4.4	MHz	
Input voltage 1, HIGH	VIH1	P00~P03, P10~P13, P20~P23, P30~P31	3.5	-	_	v	• Pxx = input
Input voltage 2, HIGH	VIH2	W0~W3, BIN, SIN, SIO, SCK, TEST, RESET	3.75	-	_	v	<ul> <li>Hysteresis input</li> <li>SIO, SCK = input</li> </ul>
Input voltage 3, HIGH	Vінэ	OSC1	3.9		_	v	External clock inpu
Input voltage 1, LOW	VIL1	P00~P03, P10~P13, P20~P23, P30~P31	-	_	1.5	v	• Pxx = input
Input voltage 2, LOW	Vil.2	W0~W3, BIN, SIN, SIO, SCK, TEST, RESET		_	1.25	v	<ul> <li>Hysteresis input</li> <li>SIO, SCK = input</li> </ul>
Input voltage 3, LOW	ViL3	OSC1	. –	-	1.1	V	External clock inpu
Input current 1, HIGH	Інт	P00~P03, P10~P13, P20~P23, P30~P31, W0~W <u>3, BIN</u> , SIN, SIO, SCK, RESET	-		1	μA	No pull-down resistor     Pxx, SIO, SCK≕input     Vin = Voo
Input current 2, HIGH	Ііна	TEST	35	70	140	μA	<ul> <li>Internal pull-down resistor</li> <li>V<sub>IN</sub>=V<sub>DD</sub></li> </ul>
Input current 1, LOW	lı∟ı	P00~P03, P10~P13, P20~P23, P30~P31 W0~W <u>3, BIN</u> , SIN, SIO, SCK, RESET, TEST			1	μA	<ul> <li>No pull-up resistor</li> <li>Pxx, SIO, SCK=inpu</li> <li>V<sub>IN</sub>=GND</li> </ul>
Input current 2, LOW	lil2	P00~P03, P10~P13, P20~P23, P30~P31 W0~W3, BIN	-90	-125	-160	μA	<ul> <li>Internal pull-up resistor</li> <li>V<sub>IN</sub>=GND</li> </ul>
Output voltage 1, HIGH	Vohi	SIO, SCK	4.5		_	v	• SIO, SCK = output • $l_{OH}$ = -500 $\mu$ A
Output voltage 1, LOW	Vol1	P00~P03, P10~P13, P20~P23, P30~P31 SIO, SCK	_	_	0.4	v	<ul> <li>Pxx, SIO, SCK = output</li> <li>I<sub>0L</sub>=1.6mA</li> </ul>
Output leak current	L.	P00~P03, P10~P13, P20~P23, P30~P31	-	_	1	μA	<ul> <li>Pxx = high- impedance output</li> </ul>
OSC feedback current	IFO	OSC1, OSC2	-4.0	-10	-14	μA	<ul> <li>Approx. 500 k Ω</li> </ul>

\* 1 machine cycle = 1/6 oscillation frequency

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Parameter	Symbol	Pin	Min.	Тур.	Max.	Unlt	Conditions
STOP circuit current	IDDST		-	-	1	μA	STOP mode
HALT circuit current	Іорнт		-	0.4	_	mA	HALT mode     fosc=4.4MHz
Operational circuit current	lodop		-	1.5	_	mA	• fosc=4.4MHz
Clock frequency	fosc	OSC1, OSC2	2	-	4.4	MHz	
Input voltage 1, HIGH	ViH1	P00~P03, P10~P13, P20~P23, P30~P31	2.1	-	-	V.	Pxx = input     ADC = digital input
Input voltage 2, HIGH	ViH2	W0~W3, BIN, SIN, SIO, SCK, TEST, RESET	2.25	<u> </u>	_	v	<ul> <li>Hysteresis input</li> <li>SIO, SCK = input</li> </ul>
Input voltage 3, HIGH	Vінз	OSC1	2.4			v	External clock input
Input voltage 1, LOW	VIL1	P00~P03, P10~P13, P20~P23, P30~P31	-	_	0.9	v	<ul> <li>Pxx = input</li> <li>ADC = digital input</li> </ul>
Input voltage 2, LOW	VIL2	W0~W3, BIN, SIN, SIO, SCK, TEST, RESET	_	_	0.75	v	<ul> <li>Hysteresis input</li> <li>SIO, SCK = input</li> </ul>
Input voltage 3, LOW	VIL3	OSC1	-	. —	0.65	v	External clock input
Input current 1, HIGH	Юнт	P00~P03, P10~P13, P20~P23, P30~P31, W0~W3, BIN, SIN, SIO, SCK, RESET	_		1	μA	No pull-down resistor     Pxx, SIO, SCK=input     V <sub>IN</sub> =V <sub>DD</sub>
Input current 2, HIGH	lih2	TEST	10	20	35	μA	<ul> <li>Internal pull-down</li> <li>resistor</li> <li>V<sub>IN</sub>==V<sub>DD</sub></li> </ul>
Input current 1, LOW	lııı	P00~P03, P10~P13, P20~P23, P30~P31, W0~W3, BIN, SIN, SIO, SCK, RESET, TEST	_	_	-1	μA	No pull-up resistor     Pxx, SIO, SCK=input     VIN=GND
Input current 2, LOW	1112	P00~P03, P10~P13, P20~P23, P30~P31, W0~W3, BIN	-20	-40	60	μĂ	Internal pull-up resistor V <sub>IN</sub> =GND
Output voltage 1, HIGH	Vон1	SIO, SCK	2.5	· _	— .	v	• SIO, SCK = output • $I_{OH}$ =-500 $\mu$ A
Output voltage 1, LOW	VoL1	P00~P03, P10~P13, P20~P23, P30~P31, SIO, SCK	-	_	0.6	v	• Pxx, SIO, SCK = output • I <sub>OL</sub> =1.6mA
Output leak current	h	P00~P03, P10~P13, P20~P23, P30~P31	-	-	1	μA	Pxx = high-impedance     output
OSC feedback current	leo	OSC1, OSC2	-1.5	-3	-5	μA	· Approx. 1 MΩ

\*1 machine cycle = 1/6 oscillation frequency

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## Hardware description

- Operates on a single power supply (V<sub>DD</sub>=2.3~ 5.5V)
- (2) Memory size ROM: 4096×8 bits RAM: 256×4 bits
- (3) Instruction execution time (1 cycle instruction)
   1.5 μ sec : (at 4MHz)
- (4) Subroutine nesting: 8 levels
- (5) Interrupts: 4 factors External: 2 factors Internal (timer/counter, serial I/O)
  - : 2 factors
- (6) ROM data table function (data table area: 4KB)

#### External dimensions (Units: mm)



- (7) Two energy-saving modes (STOP/HALT)
- (8) Internal 8-bit timer counter
- (9) Internal serial I / O, simplifying interface with attached LSI ICs (LSB first)
- (10) 14 programmable pull-up input/outputs
- (11) 5 programmable pull-up inputs

#### Notes

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