

CD4014BM/CD4014BC 8-Stage Static Shift Register

General Description

The CD4014BM/CD4014BC is an 8-stage parallel input/serial output shift register. A parallel/serial control input enables individual JAM inputs to each of 8 stages. Q outputs are available from the sixth, seventh and eighth stages. All outputs have equal source and sink current capabilities and conform to standard "B" series output drive.

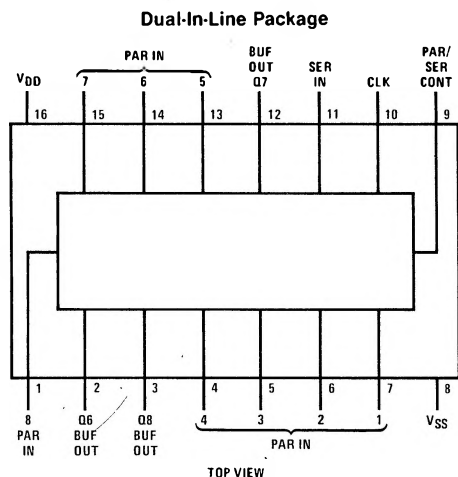
When the parallel/serial control input is in the logical "0" state, data is serially shifted into the register synchronously with the positive transition of the clock. When the parallel/serial control input is in the logical "1" state, data is jammed into each stage of the register synchronously with the positive transition of the clock.

All inputs are protected against static discharge with diodes to V_{DD} and V_{SS} .

Features

- Wide supply voltage range—3.0V to 15V
- High noise immunity— $0.45 V_{DD}$ (typ.)
- Low power TTL compatibility — fan out of 2 driving 74L or 1 driving 74LS
- 5V-10V-15V parametric ratings
- Symmetrical output characteristics
- Maximum input leakage $1 \mu A$ at 15V over full temperature range

Connection Diagram

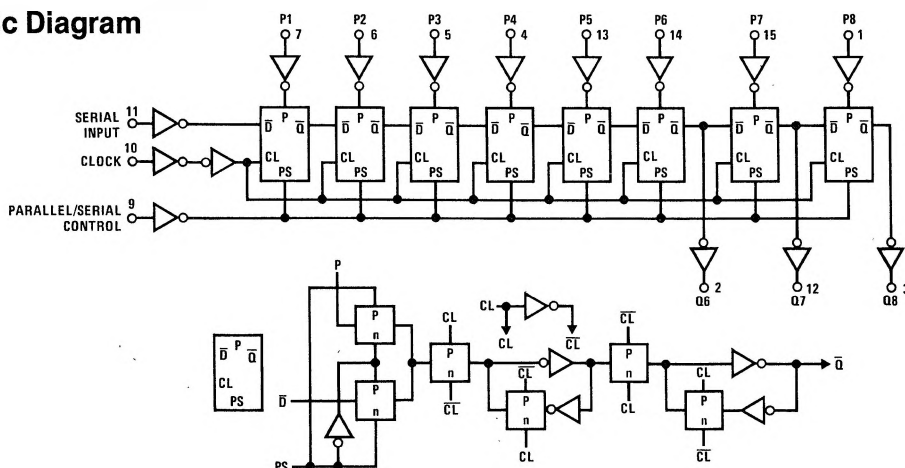


Truth Table

CL*	Serial Input	Parallel/Serial Control	PI 1	PI n	Q1 (Internal)	Q _n
Level change	X	1	0	0	0	0
	X	1	1	0	1	0
	X	1	0	1	0	1
	X	1	1	1	1	1
	0	0	X	X	0	Q _{n-1}
No Change	1	0	X	X	1	Q _{n-1}
	X	X	X	X	Q1	Q _n

* Level change
X = Don't care case

Logic Diagram



Absolute Maximum Ratings

(Notes 1 & 2)

V_{DD}	Supply Voltage	-0.5 to +18 V
V_{IN}	Input Voltage	-0.5 to V_{DD} + 0.5 V
T_S	Storage Temperature Range	-65°C to +150°C
P_D	Package Dissipation	500 mW
T_L	Lead Temperature (Soldering, 10 sec.)	300°C

Recommended Operating Conditions

(Note 2)

V_{DD}	Supply Voltage	3.0 to 15 V
V_{IN}	Input Voltage	0 to V_{DD}
T_A	Operating Temperature Range	-55°C to +125°C
	CD4014BM	-55°C to +125°C
	CD4014BC	40°C to 85°C

DC Electrical Characteristics (Note 2) — CD4014BM

Parameter		Conditions	- 55°C		25°C			125°C		Units
			Min	Max	Min	Typ	Max	Min	Max	
I _{DD}	Quiescent Device Current	V _{DD} = 5V V _{DD} = 10V V _{DD} = 15V		5 10 20		0.1 0.2 0.3	5 10 20	150 300 600	μA μA μA	
V _{OL}	Low Level Output Voltage	V _{DD} = 5V V _{DD} = 10V V _{DD} = 15V I _O < 1 μA		0.05 0.05 0.05		0 0 0	0.05 0.05 0.05	0.05 0.05 0.05	V V V	
V _{OH}	High Level Output Voltage	V _{DD} = 5V V _{DD} = 10V V _{DD} = 15V I _O < 1 μA	4.95 9.95 14.95		4.95 9.95 14.95	5 10 15		4.95 9.95 14.95	V V V	
V _{IL}	Low Level Input Voltage	V _{DD} = 5V, V _O = 0.5V or 4.5V V _{DD} = 10V, V _O = 1.0V or 9.0V V _{DD} = 15V, V _O = 1.5V or 13.5V		1.5 3.0 4.0		2 4 6	1.5 3.0 4.0	1.5 3.0 4.0	V V V	
V _{IH}	High Level Input Voltage	V _{DD} = 5V, V _O = 0.5V or 4.5V V _{DD} = 10V, V _O = 1.0V or 9.0V V _{DD} = 15V, V _O = 1.5V or 13.5V	3.5 7.0 11.0		3.5 7.0 11.0	3 6 9		3.5 7.0 11.0	V V V	
I _{OL}	Low Level Output Current	V _{DD} = 5V, V _O = 0.4V V _{DD} = 10V, V _O = 0.5V V _{DD} = 15V, V _O = 1.5V	0.64 1.6 4.2		0.51 1.3 3.4	0.88 2.2 8		0.36 0.90 2.4	mA mA mA	
I _{OH}	High Level Output Current	V _{DD} = 5V, V _O = 4.6V V _{DD} = 10V, V _O = 9.5V V _{DD} = 15V, V _O = 13.5V	- 0.64 - 1.6 - 4.2		- 0.51 - 1.3 - 3.4	- 0.88 - 2.2 - 8		- 0.36 - 0.90 - 2.4	mA mA mA	
I _{IN}	Input Current	V _{DD} = 15V, V _{IN} = 0V V _{DD} = 15V, V _{IN} = 15V		- 0.10 0.10		- 10 ⁻⁵ 10 ⁻⁵	- 0.10 0.10	- 1.0 1.0	μA μA	

DC Electrical Characteristics (Note 2) — CD4014BC

Parameter		Conditions	-40°C		25°C			85°C		Units
			Min	Max	Min	Typ	Max	Min	Max	
I _{DD}	Quiescent Device Current	V _{DD} = 5V		20		0.1	20		150	μA
		V _{DD} = 10V		40		0.2	40		300	μA
		V _{DD} = 15V		80		0.3	80		600	μA
V _{OL}	Low Level Output Voltage	V _{DD} = 5V	I _O < 1 μA	0.05	0	0.05		0.05	V	
		V _{DD} = 10V		0.05	0	0.05		0.05	V	
		V _{DD} = 15V		0.05	0	0.05		0.05	V	
V _{OH}	High Level Output Voltage	V _{DD} = 5V	I _O < 1 μA	4.95	4.95	5		4.95	V	
		V _{DD} = 10V		9.95	9.95	10		9.95	V	
		V _{DD} = 15V		14.95	14.95	15		14.95	V	
V _{IL}	Low Level Input Voltage	V _{DD} = 5V, V _O = 0.5V or 4.5V		1.5	2	1.5		1.5	V	
		V _{DD} = 10V, V _O = 1.0V or 9.0V		3.0	4	3.0		3.0	V	
		V _{DD} = 15V, V _O = 1.5V or 13.5V		4.0	6	4.0		4.0	V	
V _{IH}	High Level Input Voltage	V _{DD} = 5V, V _O = 0.5V or 4.5V	3.5	3.5	3		3.5	V		
		V _{DD} = 10V, V _O = 1.0V or 9.0V	7.0	7.0	6		7.0	V		
		V _{DD} = 15V, V _O = 1.5V or 13.5V	11.0	11.0	9		11.0	V		
I _{OL}	Low Level Output Current	V _{DD} = 5V, V _O = 0.4V	0.52	0.44	0.88		0.36	mA		
		V _{DD} = 10V, V _O = 0.5V	1.3	1.1	2.2		0.90	mA		
		V _{DD} = 15V, V _O = 1.5V	3.6	3.0	8		2.4	mA		
I _{OH}	High Level Output Current	V _{DD} = 5V, V _O = 4.6V	-0.52	-0.44	-0.88		-0.36	mA		
		V _{DD} = 10V, V _O = 9.5V	-1.3	-1.1	-2.2		-0.90	mA		
		V _{DD} = 15V, V _O = 13.5V	-3.6	-3.0	-8		-2.4	mA		
I _{IN}	Input Current	V _{DD} = 15V, V _{IN} = 0V		-0.3	-10 ⁻⁵	-0.3		-1.0	μA	
		V _{DD} = 15V, V _{IN} = 15V		0.3	10 ⁻⁵	0.3		1.0	μA	

AC Electrical Characteristics $T_A = 25^\circ\text{C}$, input $t_r, t_f = 20\text{ ns}$, $C_L = 50\text{ pF}$, $R_L = 200\text{ k}\Omega$

Parameter	Conditions	Min	Typ	Max	Units
t_{PLH}, t_{PHL}	Propagation Delay Time				
	$V_{DD} = 5\text{V}$		200	320	ns
	$V_{DD} = 10\text{V}$		80	160	ns
	$V_{DD} = 15\text{V}$		60	120	ns
t_{THL}, t_{TLH}	Transition Time				
	$V_{DD} = 5\text{V}$		100	200	ns
	$V_{DD} = 10\text{V}$		50	100	ns
	$V_{DD} = 15\text{V}$		40	80	ns
f_{CL}	Maximum Clock Input Frequency				
	$V_{DD} = 5\text{V}$	2.8	4		MHz
	$V_{DD} = 10\text{V}$	6	12		MHz
	$V_{DD} = 15\text{V}$	8	16		MHz
t_w	Minimum Clock Pulse Width				
	$V_{DD} = 5\text{V}$		90	180	ns
	$V_{DD} = 10\text{V}$		40	80	ns
	$V_{DD} = 15\text{V}$		25	50	ns
t_{rCL}, t_{fCL}	Clock Rise and Fall Time (Note 3)				
	$V_{DD} = 5\text{V}$			15	μs
	$V_{DD} = 10\text{V}$			15	μs
	$V_{DD} = 15\text{V}$			15	μs
t_s	Minimum Set-up Time (Note 5)				
	Serial Input				
	$t_H \geq 200\text{ ns}$				
	$V_{DD} = 5\text{V}$		60	120	ns
	$V_{DD} = 10\text{V}$		40	80	ns
	$V_{DD} = 15\text{V}$		30	60	ns
	Parallel Inputs				
	$t_H \geq 200\text{ ns}$				
	$V_{DD} = 5\text{V}$		80	160	ns
	$V_{DD} = 10\text{V}$		40	80	ns
	$V_{DD} = 15\text{V}$		30	60	ns
	Parallel/Serial Control				
	$t_H \geq 200\text{ ns}$				
	$V_{DD} = 5\text{V}$		100	200	ns
	$V_{DD} = 10\text{V}$		50	100	ns
	$V_{DD} = 15\text{V}$		40	80	ns
t_H	Minimum Hold Time				
	Serial In, Parallel In, $t_s \geq 400\text{ ns}$				
	$V_{DD} = 5\text{V}$			0	ns
	$V_{DD} = 10\text{V}$			10	ns
	$V_{DD} = 15\text{V}$			15	ns
C_I	Average Input Capacitance				
	Any Input		5	7.5	pF
C_{PD}	Power Dissipation Capacitance (Note 4)				
			110		pF

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. Except for "Operating Temperature Range" they are not meant to imply that the devices should be operated at these limits. The table of "Electrical Characteristics" provides conditions for actual device operation.

Note 2: $V_{SS} = 0\text{V}$ unless otherwise specified.

Note 3: If more than one unit is cascaded t_{rCL} should be made less than or equal to the fixed propagation delay of the output of the driving stage for the estimated capacitive load.

Note 4: C_{PD} determines the no load AC power consumption of any CMOS device. For complete explanation, see 54C/74C family characteristics application note AN-90.

Note 5: Set-up times are measured with reference to clock and a fixed hold time (t_H) as specified.

Typical Performance Characteristics

