National Semiconductor

DM74LS533
Octal Transparent Latch
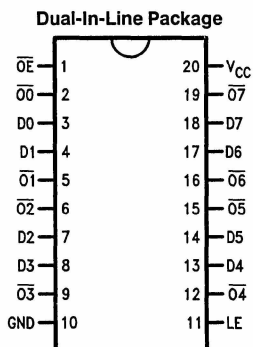# with TRI-STATE® Outputs

General Description

The 'LS533 consists of eight latches with TRI-STATE outputs for bus organized system applications. The flip-flops appear transparent to the data when Latch Enable (LE) is HIGH. When LE is LOW, the data that meets the setup times is latched. Data appears on the bus when the Output Enable ($\overline{OE}$) is LOW. When $\overline{OE}$ is HIGH the bus output is in the high impedance state. The 'LS533 is the same as the 'LS373, except that the outputs are inverted. For detailed specifications please see the 'LS373 data sheet, but note that the propagation delays from data to output are 5.0 ns longer for the 'LS533 than for the 'LS373.

Features

■ Eight latches in a single package
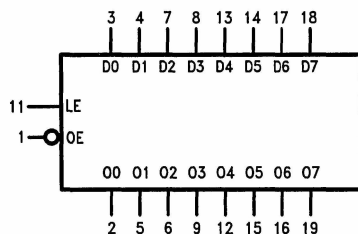■ TRI-STATE outputs for bus interfacing

Connection Diagram

Dual-In-Line Package

```
OE  ─1       20─ Vcc
O0  ─2       19─ O7
D0  ─3       18─ D7
D1  ─4       17─ D6
O1  ─5       16─ O6
O2  ─6       15─ O5
D2  ─7       14─ D5
D3  ─8       13─ D4
O3  ─9       12─ O4
GND ─10      11─ LE
```

TL/F/9811–1

Order Number DM74LS533WM or DM74LS533N
See NS Package Number M20B or N20A

Logic Symbol

```
        3   4   7   8  13  14  17  18
        D0  D1  D2  D3  D4  D5  D6  D7
11─ LE
1─○ OE
        O0  O1  O2  O3  O4  O5  O6  O7
        2   5   6   9  12  15  16  19
```

TL/F/9811–2

V_{CC} = Pin 20
GND = Pin 10

Pin Names	Description
D0, D7	Data Inputs
LE	Latch Enable Input (Active HIGH)
$\overline{OE}$	Output Enable Input (Active LOW)
$\overline{O0}$–$\overline{O7}$	Complementary TRI-STATE Outputs

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	7V
Operating Free Air Temperature Range	
DM74LS	0°C to +70°C
Storage Temperature Range	−65°C to +150°C

Note: *The "Absolute Maximum Ratings are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.*

Recommended Operating Conditions

Symbol	Parameter	DM74LS533			Units
		Min	**Nom**	**Max**	
V_{CC}	Supply Voltage	4.75	5	5.25	V
V_{IH}	High Level Input Voltage	2			V
V_{IL}	Low Level Input Voltage			0.8	V
I_{OH}	High Level Output Current			−0.4	mA
I_{OL}	Low Level Output Current			24	mA
T_A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

Over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions		Min	Typ (Note 1)	Max	Units
V_I	Input Clamp Voltage	V_{CC} = Min, I_I = −18 mA				−1.5	V
V_{OH}	High Level Output Voltage	V_{CC} = Min, I_{OH} = Max, V_{IL} = Max	DM74	2.7	3.4		V
V_{OL}	Low Level Output Voltage	V_{CC} = Min, I_{OL} = Max, V_{IH} = Min	DM74		0.35	0.5	V
		I_{OL} = 12 mA, V_{CC} = Min	DM74			0.4	
I_I	Input Current @ Max Input Voltage	V_{CC} = Max, V_I = 10V				0.1	mA
I_{IH}	High Level Input Current	V_{CC} = Max, V_I = 2.7V				20	μA
I_{IL}	Low Level Input Current	V_{CC} = Max, V_I = 0.4V				−0.4	mA
I_{OS}	Short Circuit Output Current	V_{CC} = Max (Note 2)	DM74	−20		−100	mA
I_{CCZ}	Supply Current	V_{CC} = Max				46	mA
I_{OZL}	TRI-STATE Output Off Current LOW	V_{CC} = V_{CCH} V_{OZL} = 0.4V				−20.0	μA
I_{OZH}	TRI-STATE Output Off Current HIGH	V_{CC} = V_{CCH} V_{OZH} = 2.7V				20.0	μA

Note 1: All typicals are at V_{CC} = 5V, T_A = 25°C.

Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.

Switching Characteristics

$V_{CC} = +5.0V$, $T_A = +25°C$ (See Section 1 for waveforms and load configurations)

Symbol	Parameter	$C_L = 15\,pF$ $R_L = 2\,k\Omega$		Units
		Min	Max	
t_{PLH} T_{PHL}	Propagation Delay Data to $\overline{Q}_x$		32 23	ns
t_{PLH} t_{PHL}	Propagation Delay LE to $\overline{Q}_x$		36 25	ns
t_{PZL} t_{PZH}	Output Enable Time $\overline{OE}$ to $\overline{Q}_x$		22 2	ns
t_{PHZ} t_{PLZ}	Output Enable Time $\overline{OE}$ to $\overline{Q}_x$		34 27	ns