

DM74LS564

Octal D-Type Flip-Flop (with TRI-STATE® Outputs)

General Description

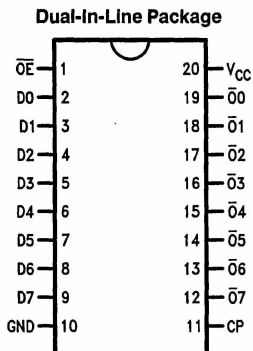
The 'LS564 is a high speed low power octal flip-flop with a buffered common Clock (CP) and a buffered common Output Enable ($\overline{OE}$). The information presented to the D inputs is stored in the flip-flops on the LOW-to-HIGH Clock (CP) transition

This device is functionally identical to the 'LS574, but has inverted outputs. For complete discussions of operations, truth tables, AC and DC electrical specifications, refer to the 'LS374 data sheet.

Features

- Inputs and outputs on opposite sides of package allowing easy interface with microprocessors
- Useful as input or output port for microprocessors
- Functionally identical to 'LS574
- Input clamp diodes limit high speed termination effects
- Fully TTL and CMOS compatible

Connection Diagram

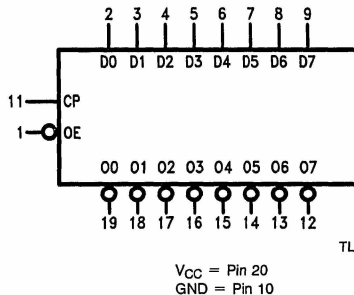


TL/F/10191-1

Order Number DM74LS564WM or DM74LS564N

See NS Package Number M20B or N20A

Logic Symbol



TL/F/10191-2

Pin Names	Description
D0-D7	Data Inputs
CP	Clock Pulse Input (Active Rising Edge)
$\overline{OE}$	TRI-STATE® Output Enable Input (Active LOW)
$\overline{Q0}-\overline{Q7}$	TRI-STATE Outputs

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V

Input Voltage 7V

Operating Free Air Temperature Range
DM74LS 0°C to +70°C

Storage Temperature Range -65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM74LS564			Units
		Min	Nom	Max	
V _{CC}	Supply Voltage	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			V
V _{IL}	Low Level Input Voltage			0.8	V
I _{OH}	High Level Output Current			-2.6	mA
I _{OL}	Low Level Output Current			24	mA
T _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 1)	Max	Units
V _I	Input Clamp Voltage	V _{CC} = Min, I _I = -18 mA			-1.5	V
V _{OH}	High Level Output Voltage	V _{CC} = Min, I _{OH} = Max, V _{IL} = Max	2.7	3.4		V
V _{OL}	Low Level Output Voltage	V _{CC} = Min, I _{OL} = Max, V _{IH} = Min		0.35	0.5	V
		I _{OL} = 12 mA, V _{CC} = Min		0.25	0.4	
I _I	Input Current @ Max Input Voltage	V _{CC} = Max, V _I = 10V			0.1	mA
I _{IH}	High Level Input Current	V _{CC} = Max, V _I = 2.7V			20	μA
I _{IL}	Low Level Input Current	V _{CC} = Max, V _I = 0.4V			-0.4	mA
I _{OS}	Short Circuit Output Current	V _{CC} = Max (Note 2)	-30		-130	mA
I _{CC}	Supply Current	V _{CC} = Max			60	mA
I _{OZH}	TRI-STATE Output OFF Current HIGH	V _{CC} = V _{CCH} , V _{OZH} = 2.7V			20	μA
I _{OZL}	TRI-STATE Output OFF Current LOW	V _{CC} = V _{CCH} , V _{OZL} = 0.4V			-20	μA

Note 1: All typicals are at V_{CC} = 5V, T_A = 25°C.

Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.

Switching Characteristics

$V_{CC} = +5.0V$, $T_A = +25^{\circ}C$ (See Section 1 for test waveforms and output load)

Symbol	Parameter	Min	Max	Units
t_{PLH} t_{PHL}	Propagation Delay CP to $\overline{O}n$		28 28	ns
t_{PZH} t_{PZL}	Enable Time $\overline{OE}$ to $\overline{O}n$		28 28	ns
t_{PHZ} t_{PLZ}	Enable Time $\overline{OE}$ to $\overline{O}n$		20 25	ns
t_s	Setup Time Dn to CP	5		ns
t_h	Hold Time Dn to CP	5		ns
$t_w (H)$ $t_w (L)$	Pulse Width (HIGH/LOW) CP	20 10		ns