

DS1488 Quad Line Driver

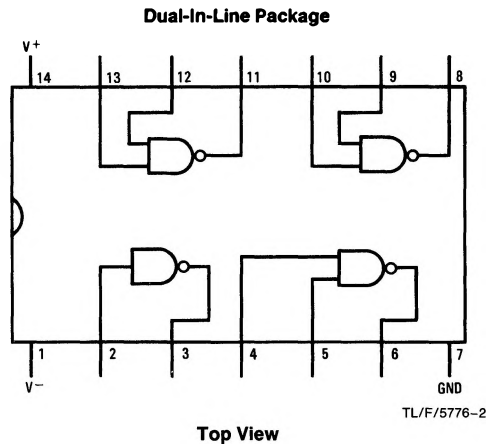
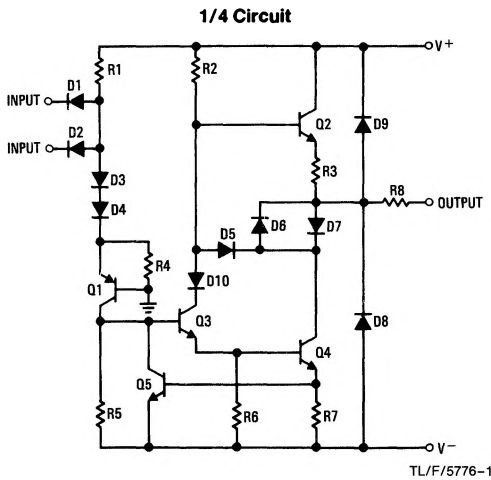
General Description

The DS1488 is a quad line driver which converts standard TTL input logic levels through one stage of inversion to output levels which meet EIA Standard RS-232D and CCITT Recommendation V.24.

Features

- Current limited output ± 10 mA typ
- Power-off source impedance 300Ω min
- Simple slew rate control with external capacitor
- Flexible operating supply range
- Inputs are TTL/LS compatible

Schematic and Connection Diagrams

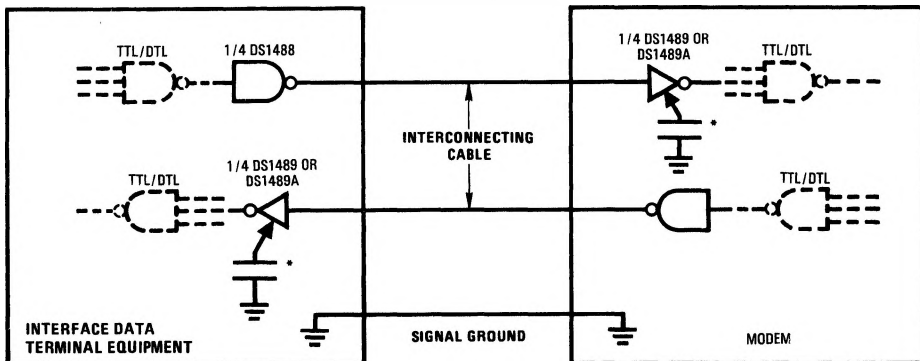


Top View

Order Number DS1488J, DS1488M or DS1488N
See NS Package Number J14A, M14A or N14A

Typical Applications

RS-232C Data Transmission



TL/F/5776-3

*Optional for noise filtering

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage

V^+ +15V
 V^- -15V

Input Voltage (V_{IN}) $-15V \leq V_{IN} \leq 7.0V$ Output Voltage $\pm 15V$ Operating Temperature Range 0°C to $+75^\circ\text{C}$ Storage Temperature Range -65°C to $+150^\circ\text{C}$ Maximum Power Dissipation* at 25°C

Cavity Package 1364 mW
 Molded DIP Package 1280 mW
 SO Package 974 mW

Lead Temperature (Soldering, 4 sec.) 260°C

*Derate cavity package 9.1 mW/ $^\circ\text{C}$ above 25°C ; derate molded DIP package 10.2 mW/ $^\circ\text{C}$ above 25°C ; derate SO package 7.8 mW/ $^\circ\text{C}$ above 25°C .

Electrical Characteristics (Notes 2 and 3) $V_{CC}^+ = 9V$, $V_{CC}^- = -9V$ unless otherwise specified

Symbol	Parameter	Conditions	Min	Typ	Max	Units
I_{IL}	Logical "0" Input Current	$V_{IN} = 0V$		-1.0	-1.3	mA
I_{IH}	Logical "1" Input Current	$V_{IN} = +5.0V$		0.005	10.0	μA
V_{OH}	High Level Output Voltage	$R_L = 3.0\text{ k}\Omega$, $V_{IN} = 0.8V$ $V^+ = 9.0V$, $V^- = -9.0V$	6.0	7.0		V
		$V^+ = 13.2V$, $V^- = -13.2V$	9.0	10.5		V
V_{OL}	Low Level Output Voltage	$R_L = 3.0\text{ k}\Omega$, $V_{IN} = 1.9V$ $V^+ = 9.0V$, $V^- = -9.0V$	-6.0	-6.8		V
		$V^+ = 13.2V$, $V^- = -13.2V$	-9.0	-10.5		V
I_{OS}^+	High Level Output Short-Circuit Current	$V_{OUT} = 0V$, $V_{IN} = 0.8V$	-6.0	-10.0	-12.0	mA
I_{OS}^-	Low Level Output Short-Circuit Current	$V_{OUT} = 0V$, $V_{IN} = 1.9V$	6.0	10.0	12.0	mA
R_{OUT}	Output Resistance	$V^+ = V^- = 0V$, $V_{OUT} = \pm 2V$	300			Ω
I_{CC}^+	Positive Supply Current (Output Open)	$V_{IN} = 1.9V$ $V^+ = 9.0V$, $V^- = -9.0V$		15.0	20.0	mA
				19.0	25.0	mA
				25.0	34.0	mA
		$V_{IN} = 0.8V$ $V^+ = 9.0V$, $V^- = -9.0V$		4.5	6.0	mA
				5.5	7.0	mA
				8.0	12.0	mA
I_{CC}^-	Negative Supply Current (Output Open)	$V_{IN} = 1.9V$ $V^+ = 9.0V$, $V^- = -9.0V$		-13.0	-17.0	mA
				-18.0	-23.0	mA
				-25.0	-34.0	mA
		$V_{IN} = 0.8V$ $V^+ = 9.0V$, $V^- = -9.0V$		-0.001	-0.015	mA
				-0.001	-0.015	mA
				-0.01	-2.5	mA
P_d	Power Dissipation	$V^+ = 9.0V$, $V^- = -9.0V$		252	333	mW
		$V^+ = 12V$, $V^- = -12V$		444	576	mW

Switching Characteristics ($V_{CC} = 9V$, $V_{EE} = -9V$, $T_A = 25^\circ\text{C}$)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
t_{pd1}	Propagation Delay to a Logical "1"	$R_L = 3.0\text{ k}\Omega$, $C_L = 15\text{ pF}$, $T_A = 25^\circ\text{C}$		230	350	ns
t_{pd0}	Propagation Delay to a Logical "0"	$R_L = 3.0\text{ k}\Omega$, $C_L = 15\text{ pF}$, $T_A = 25^\circ\text{C}$		70	175	ns
t_r	Rise Time	$R_L = 3.0\text{ k}\Omega$, $C_L = 15\text{ pF}$, $T_A = 25^\circ\text{C}$		75	100	ns
t_f	Fall Time	$R_L = 3.0\text{ k}\Omega$, $C_L = 15\text{ pF}$, $T_A = 25^\circ\text{C}$		40	75	ns

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. Except for "Operating Temperature Range" they are not meant to imply that the devices should be operated at these limits. The table of "Electrical Characteristics" provides conditions for actual device operation.

Note 2: Unless otherwise specified min/max limits apply across the 0°C to $+75^\circ\text{C}$ temperature range for the DS1488.

Note 3: All currents into device pins shown as positive, out of device pins as negative, all voltages referenced to ground unless otherwise noted. All values shown as max or min on absolute value basis.

Applications

By connecting a capacitor to each driver output the slew rate can be controlled utilizing the output current limiting characteristics of the DS1488. For a set slew rate the appropriate capacitor value may be calculated using the following relationship

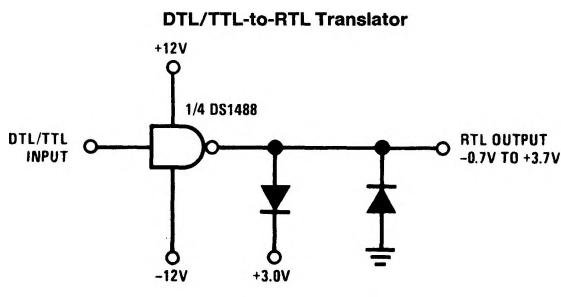
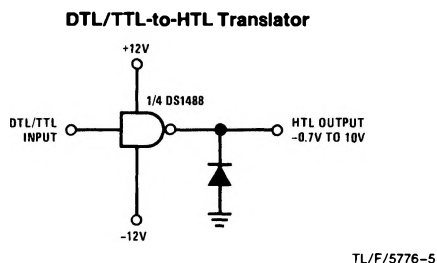
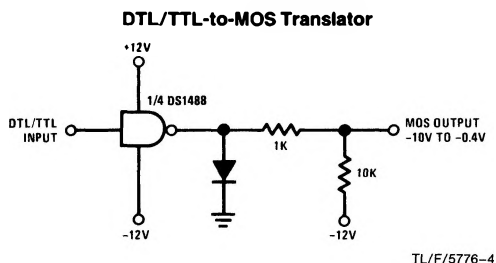
$$C = I_{SC} (\Delta T / \Delta V)$$

where C is the required capacitor, I_{SC} is the short circuit current value, and $\Delta V / \Delta T$ is the slew rate.

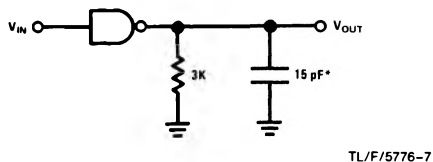
RS-232C specifies that the output slew rate must not exceed 30V per microsecond. Using the worst case output short circuit current of 12 mA in the above equation, calculations result in a required capacitor of 400 pF connected to each output.

See Typical Performance Characteristics.

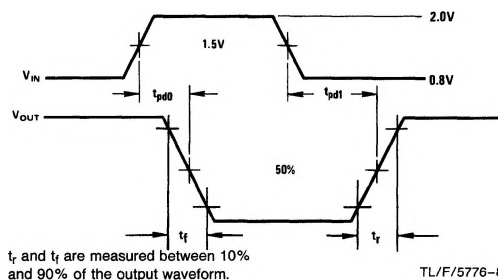
Typical Applications (Continued)



AC Load Circuit and Switching Time Waveforms



* C_L includes probe and jig capacitance.



Typical Performance Characteristics $T_A = +25^\circ\text{C}$ unless otherwise noted

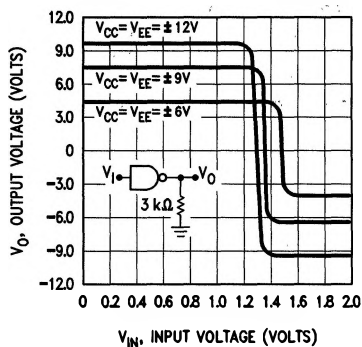


FIGURE 1. Transfer Characteristics vs Power Supply Voltage

TL/F/5776-9

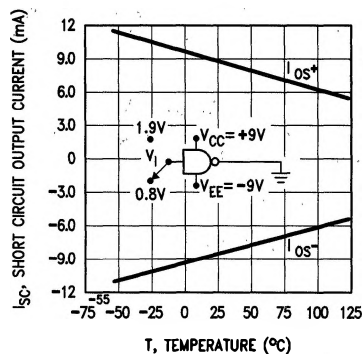


FIGURE 2. Short-Circuit Output Current vs Temperature

TL/F/5776-10

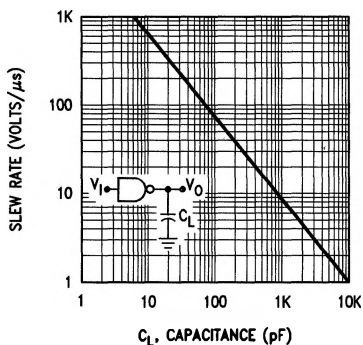


FIGURE 3. Output Slew Rate vs Load Capacitance

TL/F/5776-11

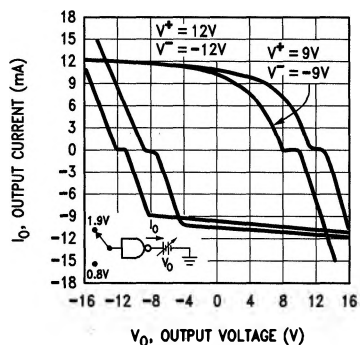


FIGURE 4. Output Voltage and Current-Limiting Characteristics

TL/F/5776-12