

DS26F31M Quad High Speed Differential Line Drivers

Check for Samples: [DS26F31M](#)

FEATURES

- Military temperature range
- Output skew—2.0 ns typical
- Input to output delay—10 ns
- Operation from single +5.0V supply
- 16-lead ceramic DIP Package
- Outputs won't load line when $V_{CC} = 0V$
- Output short circuit protection
- Meets the requirements of EIA standard RS-422
- High output drive capability for 100 Ω terminated transmission lines

DESCRIPTION

The DS26F31 is a quad differential line driver designed for digital data transmission over balanced lines. The DS26F31 meets all the requirements of EIA Standard RS-422 and Federal Standard 1020. It is designed to provide unipolar differential drive to twisted-pair or parallel-wire transmission lines.

The DS26F31 offers improved performance due to the use of state-of-the-art L-FAST bipolar technology. The L-FAST technology allows for higher speeds and lower currents by utilizing extremely short gate delay times. Thus, the DS26F31 features lower power, extended temperature range, and improved specifications.

The circuit provides an enable and disable function common to all four drivers. The DS26F31M features TRI-STATE outputs and logical OR-ed complementary enable inputs. The inputs are all LS compatible and are all one unit load.

The DS26F31M offers optimum performance when used with the DS26F32 Quad Differential Line Receiver.

Connection and Logic Diagrams

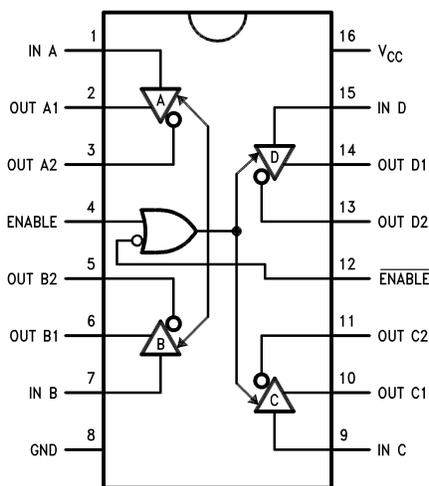


Figure 1. 16-Lead Dual-In-Line Package



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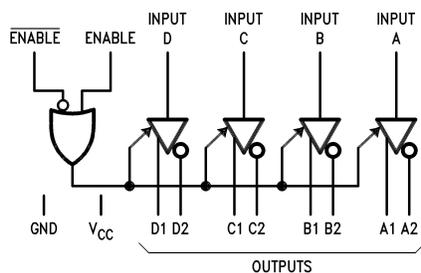


Figure 2. Logic Symbol

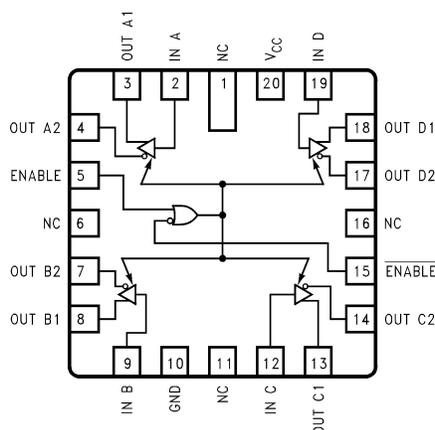


Figure 3. 20-Lead Ceramic Leadless Chip Carrier



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

Absolute Maximum Ratings ⁽¹⁾

Storage Temperature Range	
Ceramic DIP	-65°C to +175°C
Lead Temperature	
Ceramic DIP (Soldering, 60 sec.)	300°C
Maximum Power ⁽²⁾ Dissipation at 25°C	
Cavity Package	1500 mW
Supply Voltage	7.0V
Input Voltage	7.0V
Output Voltage	5.5V

- (1) "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The [Electrical Characteristics](#) provide conditions for actual device operation.
- (2) Derate cavity package 10 mW/°C above 25°C.

Operating Range

DS26F31M	
Temperature	-55°C to +125°C
Supply Voltage	4.5V to 5.5V

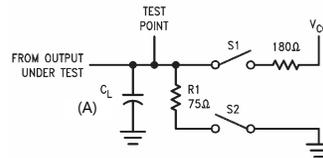
Electrical Characteristics ^{(1) (2)}

over operating range, unless otherwise specified

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V _{OH}	Output Voltage HIGH	V _{CC} = Min, I _{OH} = -20 mA	2.5	3.2		V
V _{OL}	Output Voltage LOW	V _{CC} = Min, I _{OL} = 20 mA		0.32	0.5	V
V _{IH}	Input Voltage HIGH	V _{CC} = Min	2.0			V
V _{IL}	Input Voltage LOW	V _{CC} = Max			0.8	V
I _{IL}	Input Current LOW	V _{CC} = Max, V _I = 0.4V		-0.10	-0.20	mA
I _{IH}	Input Current HIGH	V _{CC} = Max, V _I = 2.7V		0.5	20	μA
I _{IR}	Input Reverse Current	V _{CC} = Max, V _I = 7.0V		0.001	0.1	mA
I _{OZ}	Off State (High Impedance) Output Current	V _{CC} = Max V _O = 2.5V V _O = 0.5V		0.5 0.5	20 -20	μA
V _{IC}	Input Clamp Voltage	V _{CC} = Min, I _I = -18 mA		-0.8	-1.5	V
I _{OS}	Output Short Circuit	V _{CC} = Max ⁽³⁾	-30	-60	-150	mA
I _{CCX}	Supply Current	V _{CC} = Max, All Outputs Disabled			50	mA
I _{CC}		V _{CC} = Max, All Outputs Enabled			40	mA
t _{PLH}	Input to Output	V _{CC} = 5.0V, T _A = 25°C, Load = ^{(4) (5)}		10	15	ns
t _{PHL}	Input to Output	V _{CC} = 5.0V, T _A = 25°C, Load = ⁽⁴⁾		10	15	ns
SKEW	Output to Output	V _{CC} = 5.0V, T _A = 25°C, Load = ^{(4) (5)}		2.0	4.5	ns
t _{LZ}	Enable to Output	V _{CC} = 5.0V, T _A = 25°C, C _L = 10 pF		23	32	ns
t _{HZ}		V _{CC} = 5.0V, T _A = 25°C, C _L = 10 pF		15	25	ns
t _{ZL}	Enable to Output	V _{CC} = 5.0V, T _A = 25°C, Load = ⁽⁴⁾		20	30	ns
t _{ZH}	Enable to Output	V _{CC} = 5.0V, T _A = 25°C, Load = ⁽⁴⁾		23	32	ns

- (1) Unless otherwise specified min/max limits apply across the -55°C to +125°C temperature range for the DS26F31M and across the 0°C to +70°C range for the DS26F31C. All typicals are given for V_{CC} = 5V and T_A = 25°C.
- (2) All currents into the device pins are positive; all currents out of the device pins are negative. All voltages are referenced to ground unless otherwise specified.
- (3) Only one output at a time should be shorted.
- (4) C_L = 30 pF, V_I = 1.3V to V_O = 1.3V, V_{PULSE} = 0V to +3V (See AC Load Test Circuit for TRI-STATE Outputs).
- (5) Skew is defined as the difference in propagation delays between complementary outputs at the 50% point.

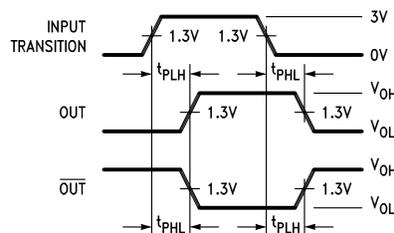
Test Circuit and Timing Waveforms⁽¹⁾⁽²⁾⁽³⁾



- A. All currents into the device pins are positive; all currents out of the device pins are negative. All voltages are referenced to ground unless otherwise specified.

Figure 4. AC Load Test Circuit for TRI-STATE Outputs

- (1) Pulse Generator for all Pulses: Rate ≤ 1.0 MHz, $Z_O = 50\Omega$, $t_r \leq 6.0$ ns, $t_f \leq 6.0$ ns.
- (2) C_L includes probe and jig capacitance.
- (3) S1 and S2 of Load Circuit are closed except where shown.



- (1) "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The [Electrical Characteristics](#) provide conditions for actual device operation.
- (2) All currents into the device pins are positive; all currents out of the device pins are negative. All voltages are referenced to ground unless otherwise specified.

Figure 5. Propagation Delay

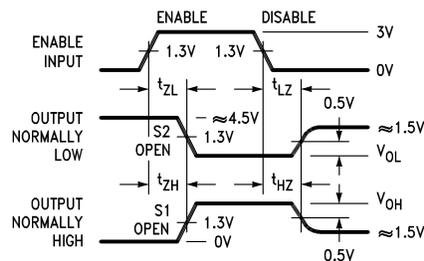


Diagram shown for Enable Low. Switches S1 and S2 open.

- (1) Unless otherwise specified min/max limits apply across the -55°C to $+125^\circ\text{C}$ temperature range for the DS26F31M and across the 0°C to $+70^\circ\text{C}$ range for the DS26F31C. All typicals are given for $V_{CC} = 5\text{V}$ and $T_A = 25^\circ\text{C}$.
- (2) All currents into the device pins are positive; all currents out of the device pins are negative. All voltages are referenced to ground unless otherwise specified.

Figure 6. Enable and Disable Times

Typical Application

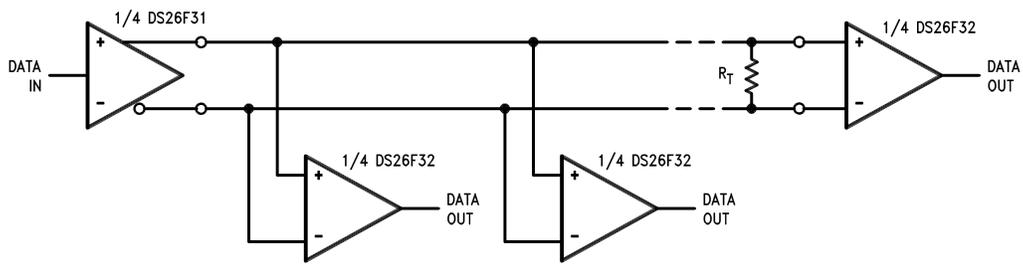


Figure 7. Typical Application

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