

DS9639A Dual Differential Line Receiver

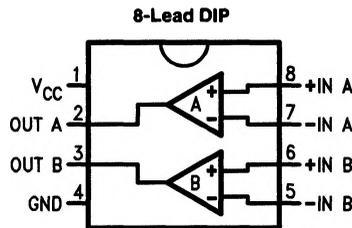
General Description

The DS9639A is a Schottky dual differential line receiver which has been specifically designed to satisfy the requirements of EIA Standards RS-422, RS-423 and RS-232C. In addition, the DS9639A satisfies the requirements of MIL-STD 188-114 and is compatible with the International Standard CCITT recommendations. The DS9639A is suitable for use as a line receiver in digital data systems, using either single ended or differential, unipolar or bipolar transmission. It requires a single 5.0V power supply and has Schottky TTL compatible outputs. The DS9639A has an operational input common mode range of $\pm 7.0V$ either differentially or to ground.

Features

- Dual channel
- Single 5.0V supply
- Satisfies EIA Standards RS-422, RS-423 and RS-232C
- Built-in ± 35 mV hysteresis
- High input common mode voltage range
- High input impedance
- TTL compatible outputs
- Schottky technology

Connection Diagram



TL/F/9623-1

Top View

**Order Number DS9639ACN
See NS Package Number N08E**

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Storage Temperature Range	-65°C to +175°C
Operating Temperature Range	0°C to +70°C
Lead Temperature	
Molded DIP (soldering, 10 sec.)	265°C
V _{CC} Lead Potential to Ground	-0.5V to +7.0V
Input Potential to Ground Lead	±25V
Differential Input Voltage	±25V
Output Differential to Ground Lead	-0.5V to 5.5V

Output Sink Current	50 mA
Maximum Power Dissipation* at 25°C	
Molded Package	930 mW
*Derate molded DIP package 7.5 mW/°C above 25°C.	

Recommended Operating Conditions

	Min	Typ	Max	Units
Supply Voltage (V _{CC})	4.75	5.0	5.25	V
Operating Temperature (T _A)	0	25	70	°C

Electrical Characteristics Over recommended operating temperature and supply voltage ranges, unless otherwise specified (Notes 2 & 3)

Symbol	Parameter	Conditions (Note 1)	Min	Typ	Max	Units
V _{TH}	Differential Input Threshold Voltage (Note 5)	-7.0V ≤ V _{CM} ≤ +7.0V	-0.2		+0.2	V
V _{TH(R)}	Differential Input Threshold Voltage (Note 6)	-7.0V ≤ V _{CM} ≤ +7.0V	-0.4		+0.4	V
I _I	Input Current (Note 7)	V _I = 10V, 0V ≤ V _{CC} ≤ 5.5V		1.1	3.25	mA
		V _I = -10V, 0V ≤ V _{CC} ≤ 5.5V		-1.6	-3.25	
V _{OL}	Output Voltage LOW	I _{OL} = 20 mA, V _{CC} = Min		0.35	0.5	V
V _{OH}	Output Voltage HIGH	I _{OH} = -1.0 mA, V _{CC} = Min	2.5	3.5		V
I _{OS}	Output Short Circuit Current (Note 4)	V _O = 0V, V _{CC} = Max	-40	-75	-100	mA
I _{CC}	Supply Current	V _{CC} = Max, V _{I+} = 0.5V, V _{I-} = GND		35	50	mA
V _{HYST}	Input Hysteresis	V _{CM} = ±7.0V (See Curves)		70		mV

Switching Characteristics V_{CC} = 5.0V, T_A = 25°C

Symbol	Parameter	Conditions	Min	Typ	Max	Units
t _{PLH}	Propagation Delay Time Low to High	See AC Test Circuit		55	85	ns
t _{PHL}	Propagation Delay Time High to Low	See AC Test Circuit		50	75	ns

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The tables of "Electrical Characteristics" provide conditions for actual device operation.

Note 2: Unless otherwise specified min/max limits apply across the 0°C to +70°C range for the DS9639A. All typicals are given for V_{CC} = 5V and T_A = 25°C.

Note 3: All currents into the device pins are positive; all currents out of the device pins are negative. All voltages are referenced to ground unless otherwise specified.

Note 4: Only one output at a time should be shorted.

Note 5: V_{DIFF} (Differential Input Voltage) = (V_{I+}) - (V_{I-}). V_{CM} (Common Mode Input Voltage) = V_{I+} or V_{I-}.

Note 6: 500Ω ± 1% in series with inputs.

Note 7: The input not under test is tied to ground.

Equivalent Circuit

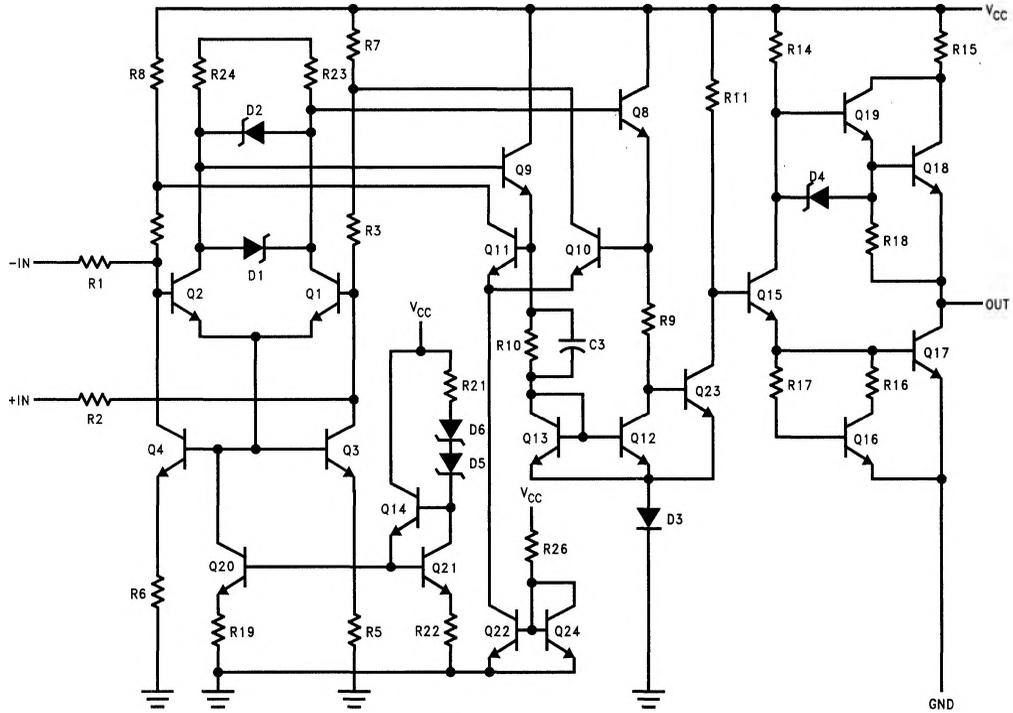


FIGURE 1. Equivalent Circuit

TL/F/9623-2

Typical Input/Output Transfer Characteristics

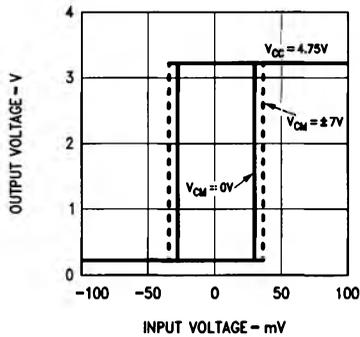


FIGURE 2

TL/F/9623-3

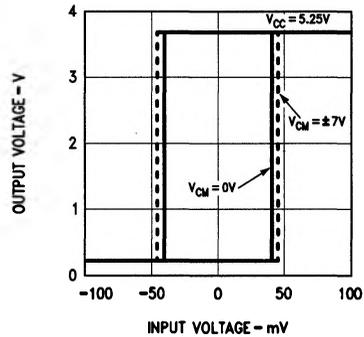
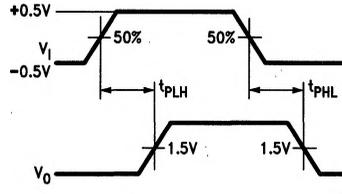
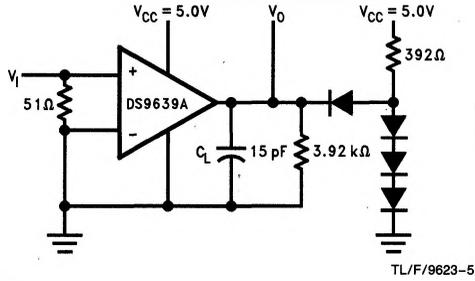


FIGURE 2a

TL/F/9623-4

AC Test Circuit and Switching Time Waveform



TL/F/9623-6

V_i
 Amplitude: 1.0V
 Offset: 0.5V
 Pulse Width: 500 ns
 PRR: 1 MHz
 $t_r = t_f \leq 5.0$ ns

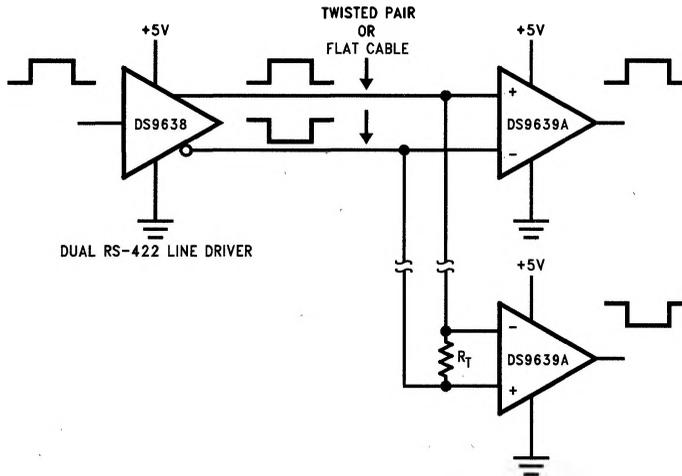
Notes:

C_L includes jig and probe capacitance.
 All diodes are FD700 or equivalent.

FIGURE 3. AC Test Circuit and Waveforms

FIGURE 3a

Typical Applications



Notes:

$R_T \geq 50\Omega$ for RS-422 operation.
 R_T combined with input impedance of receivers must be greater than 90 Ω .

FIGURE 4. RS-422 System Application (FIPS 1020) Differential Simplex Bus Transmission

TL/F/9623-7