

SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

N-Channel Silicon MOSFET

EFC4618R-P — General-Purpose Switching Device Applications

Features

- 2.5V drive
- · Best suited for LiB charging and discharging switch
- · Common-drain type
- · Protection diode in
- · Halogen free compliance

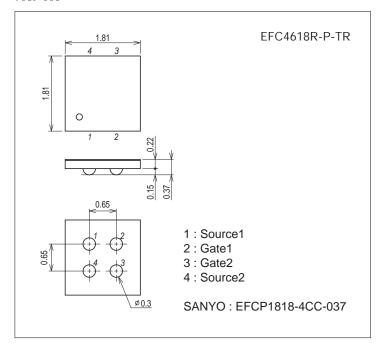
Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Source-to-Source Voltage	VSSS		24	V
Gate-to-Source Voltage	VGSS		±12	V
Source Current (DC)	IS		6	А
Source Current (Pulse)	ISP	PW≤10μs, duty cycle≤1%	60	А
Total Dissipation	PT	When mounted on ceramic substrate (5000mm ² ×0.8mm)	1.6	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Package Dimensions

unit : mm (typ) 7069-001

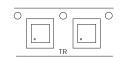


Product & Package Information

Package : EFCPJEITA, JEDEC : -

• Minimum Packing Quantity : 5,000 pcs./reel

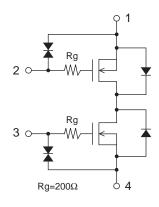
Packing Type: TR



Marking



Electrical Connection



Electrical Characteristics at Ta=25°C

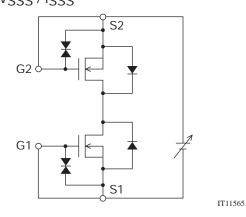
Darameter	Cumbal	Canditions		Ratings			11-14
Parameter	Symbol	Conditions		min	typ	max	Unit
Source-to-Source Breakdown Voltage	V(BR)SSS	IS=1mA, VGS=0V	Test Circuit 1	24			V
Zero-Gate Voltage Source Current	ISSS	VSS=20V, VGS=0V	Test Circuit 1			1	μΑ
Gate-to-Source Leakage Current	IGSS	VGS=±8V, VSS=0V	Test Circuit 2			±10	μΑ
Cutoff Voltage	VGS(off)	V _{SS} =10V, I _S =1mA	Test Circuit 3	0.5		1.3	V
Forward Transfer Admittance	yfs	VSS=10V, IS=3A	Test Circuit 4		6.5		S
	R _{SS} (on)1	I _S =3A, V _{GS} =4.5V	Test Circuit 5	13.5	19.8	23	mΩ
	R _{SS} (on)2	IS=3A, VGS=4.0V	Test Circuit 5	14	20.5	24	mΩ
Static Source-to-Source On-State Resistance	R _{SS} (on)3	I _S =3A, V _{GS} =3.7V	Test Circuit 5	14.5	21	25.5	mΩ
	RSS(on)4	IS=3A, VGS=3.1V	Test Circuit 5	14.9	23	30	mΩ
	RSS(on)5	IS=3A, VGS=2.5V	Test Circuit 5	18.5	27	35	mΩ
Turn-ON Delay Time	t _d (on)				200		ns
Rise Time	t _r	See specified Test Circuit.	Test Circuit 7		815		ns
Turn-OFF Delay Time	t _d (off)	See specified lest circuit.			1840		ns
Fall Time	tf]			1770		ns
Total Gate Charge	Qg	V _S S=10V, V _G S=4.5V, I _S =6A			25.4		nC
Forward Source-to-Source Voltage	V _F (S-S)	I _S =3A, V _{GS} =0V Test Circuit 6			0.76	1.2	V

Ordering Information

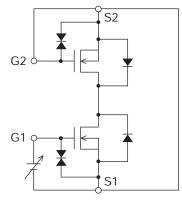
Device	Package	Shipping	memo
EFC4618R-P-TR EFCP		5,000pcs./reel	Pb Free and Halogen Free

Test circuits are example of measuring FET1 side

Test Circuit 1 VSSS / ISSS

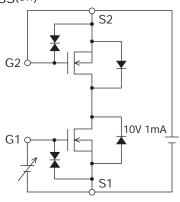


Test Circuit 2 IGSS(+) / (--)



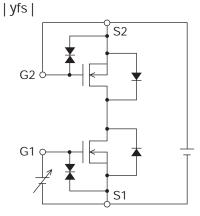
IT11566

Test Circuit 3 VGS(off)

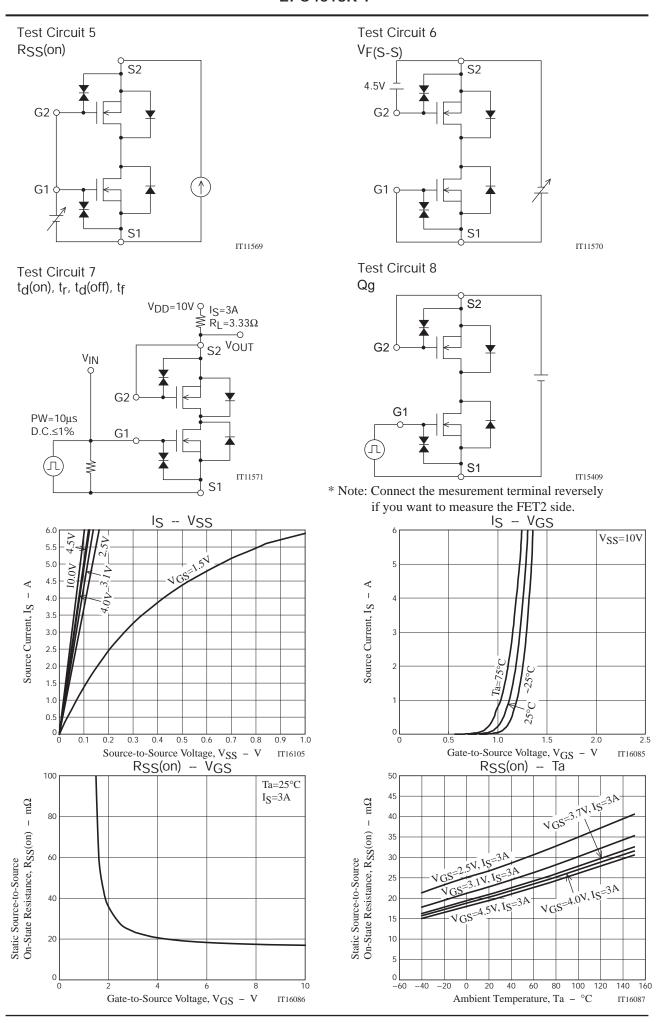


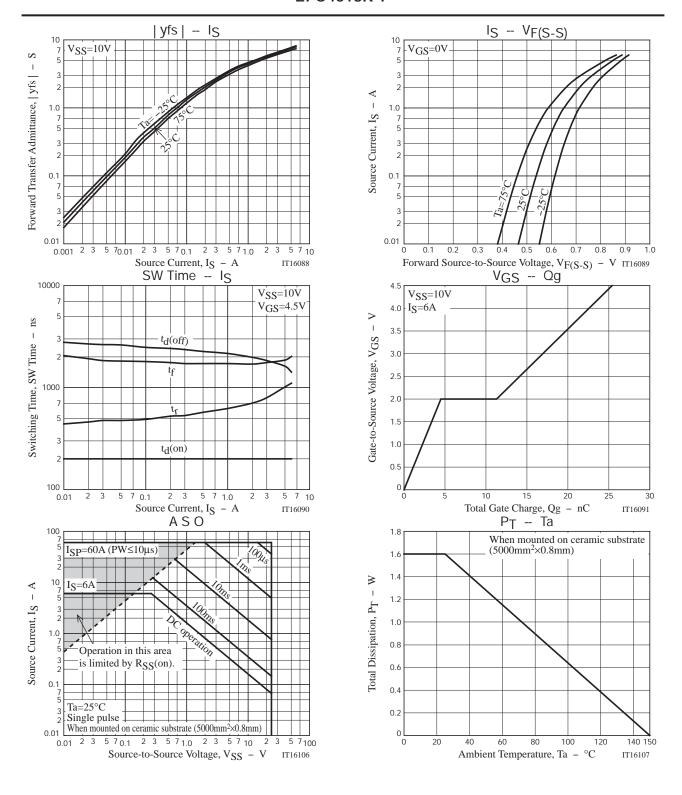
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Test Circuit 4



* Note: Connect the mesurement terminal reversely



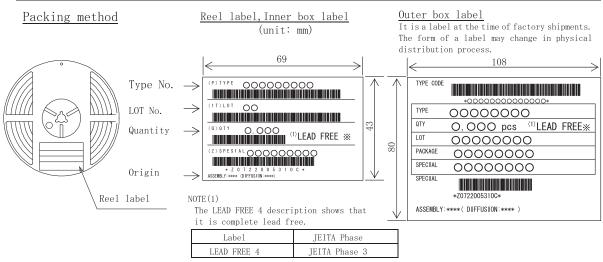


Taping Specification

EFC4618R-P-TR

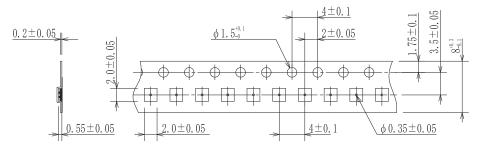
1. Packing Format

Package Name	Maximum	Number of devices o	contained (pcs)	Packing format		
	Reel	Inner box	Outer box	Inner BOX(C-1)	Outer BOX(A-7)	
EFCP1818-4CC-03	5,000	25, 000	150, 000	5 reels contained	6 inner boxes contained	
				Dimensions :mm(external)	Dimensions :mm(external)	
				183 X 72 X 185	440 X 195 X 210	

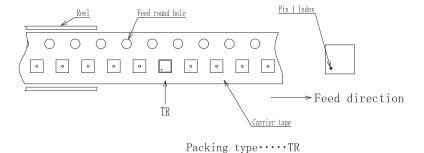


2. Taping configuration

2-1. Carrier tape size (unit: mm)



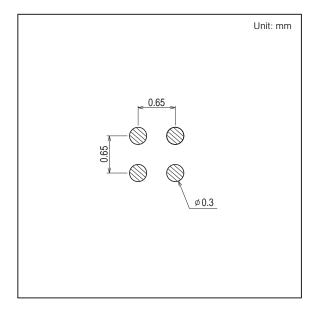
2-2. Device placement direction



Outline Drawing

EFC4618R-P-TR

Land Pattern Example



Note on usage: Since the EFC4618R-P is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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