

December 2009

FJN3303F High Voltage Fast-Switching NPN Power Transistor

Features

- · High Voltage Capability
- · High Switching Speed
- · Suitable for Electronic Ballast and Charger
- Green packaging



Absolute Maximum Ratings $T_A = 25$ °C unless otherwise noted

Symbol	Parameter	Value	Units
V_{CBO}	Collector-Base Voltage	700	V
V _{CEO}	Collector-Emitter Voltage	400	V
V _{EBO}	Emitter-Base Voltage	9	V
I _C	Collector Current (DC)	1.5	А
I _{CP}	Collector Current (Pulse) *	3	А
Ι _Β	Base Current (DC)	0.75	А
I _{BP}	Base Current (Pulse) *	1.5	A
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature range	-65 to +150	°C

^{*} Pulse Test: Pulse Width = 5ms, Duty Cycle ≤ 10%

Thermal Characteristics $T_A = 25$ °C unless otherwise noted

Symbol	Parameter		Value	Units
P _D	Total Device Dissipation	$T_C = 25^{\circ}C$ $T_A = 25^{\circ}C$	1.1 650	W mW
$R_{\theta JC}$	Thermal Resistance Junction-Ca	ase	48	°C/W
$R_{\theta JA}$	Thermal Resistance Junction-Ar	nbient	190	°C/W

Ordering Information

Part Number	Marking Info.	Package	Packing Method	Remarks
FJN3303FBU	J3303F	TO-92 (Straight)	BULK	Green EMC
FJN3303FTA	J3303F	TO-92 (Form)	AMMO	Green EMC

Electrical Characteristics $T_A = 25$ °C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_C = 500 \mu A, I_E = 0$	700			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_C = 5mA, I_B = 0$	400			٧
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = 500 \mu A, I_C = 0$	9			V
I _{CBO}	Collector Cut-off Current	V _{CB} = 700V, I _E = 0			10	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 9V, I_{C} = 0$			10	μΑ
h _{FE1} h _{FE2}	DC Current Gain	$V_{CE} = 2V, I_{C} = 0.5A$ $V_{CE} = 2V, I_{C} = 1.0A$	14 5		23	
V _{CE(sat)}	Collector-Emitter Saturation Voltage	$\begin{aligned} & I_{C} = 0.5A, \ I_{B} = 0.1A \\ & I_{C} = 1.0A, \ I_{B} = 0.25A \\ & I_{C} = 1.5A, \ I_{B} = 0.5A \end{aligned}$			0.5 1.0 3.0	> >
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 0.5A, I _B = 0.1A I _C = 1.0A, I _B = 0.25A			1.0 1.2	V V
f _T	Current Gain Bandwidth Product	$V_{CE} = 10V, I_{C} = 0.1A$	4			MHz
t _{ON}	Turn On Time	$V_{CC} = 125V, I_{C} = 1A$			1.1	μS
t _{STG}	Storage Time	$I_{B1} = -I_{B2} = -0.2A$			4.0	μS
t _F	Fall Time	$R_L = 125\Omega$			0.7	μS

Typical Performance Characteristics

Figure 1. Static Characteristic

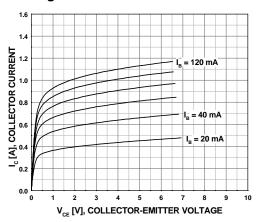


Figure 2. DC Current Gain

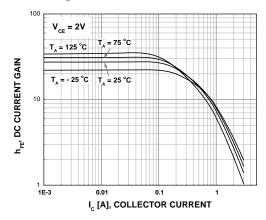


Figure 3. Collector-Emitter Saturation Voltage

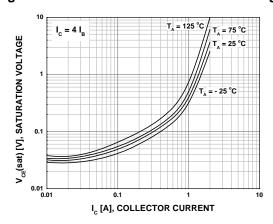


Figure 4. Base-Emitter Saturation Voltage

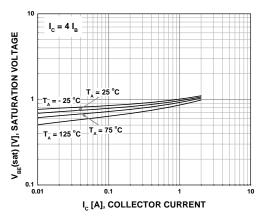


Figure 5. Resistive Load Switching Time

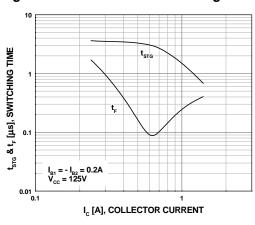
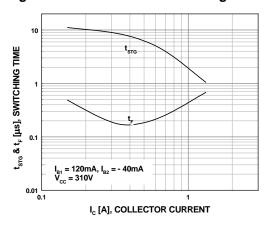


Figure 6. Resistive Load Switching Time



Typical Performance Characteristics (Continued)

Figure 7. Forward Biased Safe Operating Area

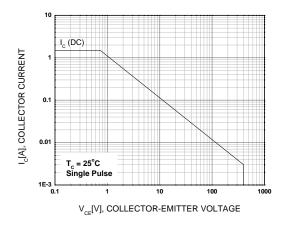
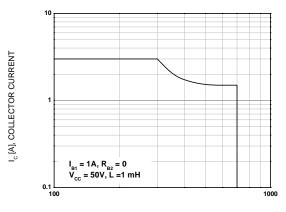
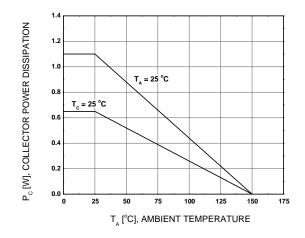


Figure 8. Reverse Biased Safe Operating Area



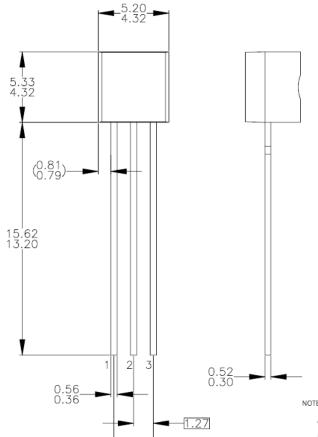
 $V_{\rm CE}$ [V], COLLECTOR-EMITTER VOLTAGE

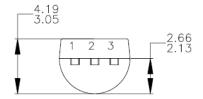
Figure 9. Power Derating



Physical Dimension

TO-92 (STD Straight Lead)





2.54

NOTES: UNLESS OTHERWISE SPECIFIED

- DRAWING WITH REFERENCE TO JEDEC TO-92 RECOMMENDATIONS. ALL DIMENSIONS ARE IN MILLIMETERS. DRAWING CONFORMS TO ASME Y14.5M-1994. TO-92 (92,94,96,97,98) PIN CONFIGURATION:

	_														
Ĭ.		92			94			96			97			98	
10	Р	F	М	Р	F	М	Ρ	F	М	Р	F	М	Р	F	М
1	Ε	S	S	Ε	S	S	В	D	G	С	G	D	С	G	D
2	В	D	G	С	G	D	Ε	S	S	В	D	G	Ε	S	S
3	С	G	D	В	D	G	С	G	D	Ε	S	S	В	D	G

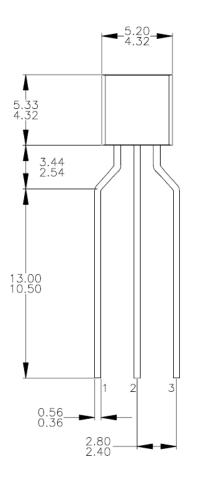
LEGEND: P - BIPOLAR F - JFET M - DMOS E - EMITTER B - BASE C - COLLECTOR D - DRAIN S - SOURCE G - GATE

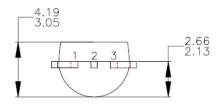
- FOR PACKAGE 92, 94, 96, 97 AND 98: PIN CONFIGURATION DRAIN "D" AND SOURCE "S" ARE INTERCHANGEAGLE AT JFET "F" OPTION. DRAWING FILENAME: MKT—ZAO3DREV3.

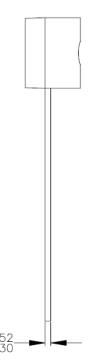
Dimensions in Millimeters

Physical Dimension (Continued)

TO-92 (Lead Form_J61Z)







NOTES: UNLESS OTHERWISE SPECIFIED

- DRAWING WITH REFERENCE TO JEDEC TO-92 RECOMMENDATIONS. ALL DIMENSIONS ARE IN MILLIMETERS. DRAWING CONFORMS TO ASME Y14.5M-1994. TO-92 (92,94,96,97,98) PIN CONFIGURATION:

z		92			94			96			97			98	
₫.	Р	F	М	Р	F	М	Ρ	F	М	Р	F	М	Р	F	М
1	Ε	S	S	Ε	S	S	В	D	G	C	G	D	C	G	D
2	В	D	G	С	G	D	Ε	S	S	В	D	G	Ε	S	S
3	С	G	D	В	D	G	С	G	D	Ε	S	S	В	D	G

LEGEND:

P - BIPOLAR F - JFET M - DMOS E - EMITTER B - BASE C - COLLECTOR

- E) FOR PACKAGE 92, 94, 96, 97 AND 98:
 PIN CONFIGURATION DRAIN "D" AND SOURCE "S"
 ARE INTERCHANGEAGLE AT JFET "F" OPTION.
 F) DRAWING FILENAME: MKT-ZAO3FREV2.

Dimensions in Millimeters





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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.					
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