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June 2015

FPF2G120BF07AS F2, 3ch Boost module with NTC

General Description

The FPF2G120BF07AS is the 3ch boost topology which is providing an optimized solution for the multi-string solar application. And the integrated high speed field stop IGBTs and SiC diodes are providing lower conduction and switching losses. Furthermore, the screw clamp provides a fast and reliable mounting method.

Electrical Features

- · High Efficiency
- · Low Conduction and Switching Losses
- · High Speed Field Stop IGBT
- · SiC SBD for Boost Diode
- · Built-in NTC for Temperature Monitoring

Mechanical Features

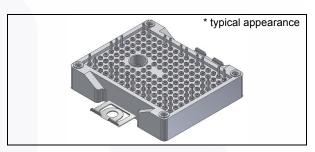
- · Compact Size: F2 Package
- · Soldering Pin
- Al₂O₃ Substrate with Low Thermal Resistance

Applications

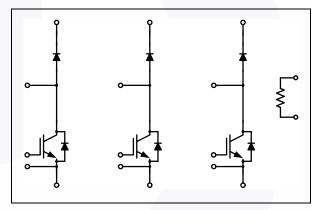
Solar Inverter

Related Materials

 AN-5077: Design Considerations for High Power Module (HPM)



Package Code: F2



Internal Circuit Diagram

Package Marking and Ordering Information

Device	Device Marking	Package	PCM	Packing Type	Quantity / Tray
FPF2G120BF07AS	FPF2G120BF07AS	F2	Х	Tray	14
FPF2G120BF07ASP	FPF2G120BF07ASP	F2	0	Tray	14

Absolute Maximum Ratings $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Description Condition		Rating	Units
Boost IGBT	•			
V _{CES}	Collector-Emitter Voltage		650	V
V _{GES}	Gate-Emitter Voltage		± 20	V
	Transient Gate-Emitter Voltage		± 25	V
I _C	Continuous Collector Current	$T_C = 80 ^{\circ}\text{C}, \ T_{Jmax} = 175 ^{\circ}\text{C}$	40	Α
Ісм	Pulsed Collector Current	limited by T _{Jmax}	80	Α
P_{D}	Maximum Power Dissipation		156	W
T _J	Operating Junction Temperature		- 40 to + 150	°C
Protection	Diode			
V _{RRM}	Peak Repetitive Reverse Voltage		650	V
l _F	Continuous Forward Current	T _C = 80 °C, T _{Jmax} = 175 °C	15	А
I _{FM}	Maximum Forward Current		30	Α
I _{FSM}	Non-repetitive Peak Surge Current	60Hz Single Half-Sine Wave	150	А
l ² t - value	Surge Current Integral Value		93	A ² s
P_{D}	Maximum Power Dissipation		140	W
TJ	Operating Junction Temperature		- 40 to + 150	°C
Boost Diod	e			<u> </u>
V _{RRM}	Peak Repetitive Reverse Voltage		650	V
l _F	Continuous Forward Current	T _C = 80 °C, T _{Jmax} = 175 °C	15	Α
I _{FM}	Maximum Forward Current		30	Α
I _{FSM}	Non-repetitive Peak Surge Current	60Hz Single Half-Sine Wave	120	А
l ² t - value	Surge Current Integral Value		60	A ² s
P _D	Maximum Power Dissipation		98	W
TJ	Operating Junction Temperature		- 40 to + 150	°C
Module				
T _{STG}	Storage Temperature		- 40 to + 125	°C
V _{ISO}	Isolation Voltage	AC 1 min.	2500	V
lsoMaterial	Internal Isolation Material		Al ₂ O ₃	-
T _{MOUNT}	Mounting Torque		2.0 to 5.0	N•m
Creepage	Terminal to Heat Sink	11.5	mm	
	Terminal to Terminal	6.3	mm	
Clearance	Terminal to Heat Sink	10.0	mm	
	Terminal to Terminal	5.0	mm	

Electrical Characteristics $T_C = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
Boost IG	ВТ		*	•	•	
Off Charac	cteristics					
BV _{CES}	Collector-Emitter Breakdown Voltage	$V_{GE} = 0 \text{ V, } I_{C} = 1 \text{ mA}$	650	-	-	V
I _{CES}	Collector Cut-off Current	V _{CE} = V _{CES} , V _{GE} = 0 V	-	-	250	μА
I _{GES}	Gate-Emitter Leakage Current	$V_{GE} = V_{GES}$, $V_{CE} = 0$ V	-	-	± 2	μА
On Charac	eteristics	02 020 02				
V _{GE(th)}	Gate-Emitter Threshold Voltage	$V_{GE} = V_{CE}$, $I_C = 40 \text{ mA}$	3.9	5.1	6.8	V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 40 A, V _{GE} = 15 V	-	1.55	2.2	V
, ,	-	I _C = 40 A, V _{GE} = 15 V, T _C = 125 °C	-	1.85	-	V
R _{LEAD}	Lead Resistance of Pin to Chip	per Chip	-	3.3	-	mΩ
	Characteristics			I.	ı	
t _{d(on)}	Turn-On Delay Time	V _{CC} = 300 V	-	24	-	ns
t _r	Rise Time	I _C = 40 A	-	24	-	ns
t _{d(off)}	Turn-Off Delay Time	$V_{GE} = 15 \text{ V}$ $R_G = 15 \Omega$	-	132	-	ns
t _f	Fall Time	Inductive Load	-	17	-	ns
E _{ON}	Turn-On Switching Loss per Pulse	T _C = 25 °C	-	0.40	-	mJ
E _{OFF}	Turn-Off Switching Loss per Pulse		-	0.28	-	mJ
t _{d(on)}	Turn-On Delay Time	V _{CC} = 300 V	-	22	-	ns
t _r	Rise Time	$I_C = 40 \text{ A}$		27	-	ns
t _{d(off)}	Turn-Off Delay Time	$V_{GE} = 15 \text{ V}$ $R_{G} = 15 \Omega$	-	148	-	ns
t _f	Fall Time	Inductive Load		17	-	ns
E _{ON}	Turn-On Switching Loss per Pulse			0.59	-	mJ
E _{OFF}	Turn-Off Switching Loss per Pulse		-	0.37	-	mJ
Qg	Total Gate Charge	V _{CC} = 300 V, I _C = 40 A, V _{GE} = 15 V	-	65	-	nC
$R_{\theta JC}$	Thermal Resistance of Junction to Case	per Chip	-	-	0.96	°C/W
Protectio	n Diode		-		1	
V _F	Diode Forward Voltage	I _F = 15 A	_	1.05	1.4	V
- F		I _F = 15 A, T _C = 125 °C	-	0.95	-	V
R _{LEAD}	Lead Resistance of Pin to Chip	per Chip	- /	2.4	-	mΩ
I _R	Reverse Leakage Current	V _R = 650 V	_	_	250	μА
R ₀ JC	Thermal Resistance of Junction to Case	per Chip	-	_	1.07	°C/W
Boost Die		I F F F F				
V _F	Diode Forward Voltage	Ι – 15 Λ		1 15	1.9	V
٧F	blode Forward voltage	I _F = 15 A I _F = 15 A, T _C = 125 °C	-	1.45 1.75		V
D	Lead Resistance of Pin to Chip	per Chip	-	2.8	-	
R _{LEAD}	Reverse Leakage Current	V _R = 650 V	-	2.0	60	mΩ μA
I _R	Reverse Recovery Current	$V_R = 300 \text{ V}, I_F = 15 \text{ A},$	-	9.2	- 00	Α
I _{rr} Q _C	Total Capacitive Charge	di / dt = 1390 A/us,	-	60	-	nC
	Reverse Recovery Energy	T _C = 25 °C	-	4.9		μJ
E _{rec}	Reverse Recovery Current	V _R = 300 V, I _F = 15 A,	-	9.2	_	Α
I _{rr}	Total Capacitive Charge	di / dt = 1390 A/us,		65		nC
Q_C	Reverse Recovery Energy	T _C = 125 °C	-	4.9	_	μJ
E _{rec}	Dovorco Docovory Engrav					

Electrical Characteristics $T_C = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
NTC(The	mistor)	•		•	•	
R _{NTC} R	Rated Resistance	T _C = 25 °C	-	10	-	kΩ
		T _C = 100 °C	-	936	-	Ω
	Tolerance	T _C = 25 °C	- 3	-	+ 3	%
P_D	Power Dissipation	T _C = 25 °C	-	-	20	mW
B _{Value}	B-Constant	B _{25/50}	-	3450	-	K
		B _{25/100}	-	3513	-	K

Typical Performance Characteristics

Fig 1. Typical Output Characteristics



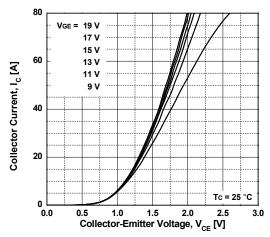


Fig 2. Typical Output Characteristics

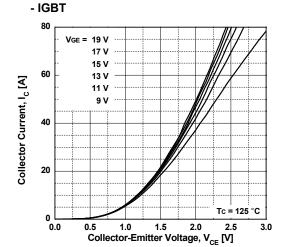


Fig 3. Typical Saturation Voltage Characteristics - IGBT

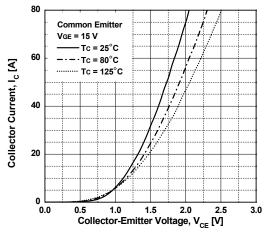


Fig 4. Switching Loss vs. Collector Current

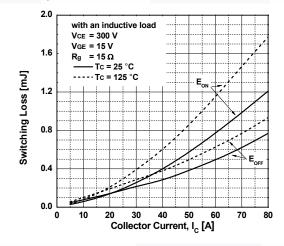


Fig 5. Switching Loss vs. Gate Resistance

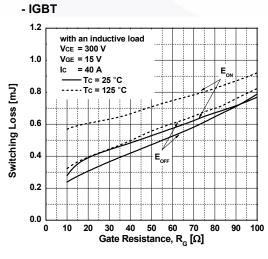
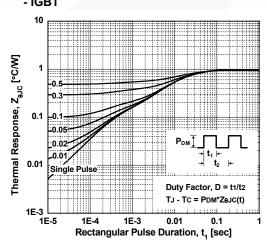


Fig 6. Transient Thermal Impedance - IGBT



Typical Performance Characteristic

Fig 7. Typical Forward Voltage Drop

- Protection Diode

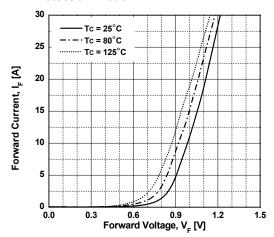


Fig 8. Transient Thermal Impedance

- Protection Diode

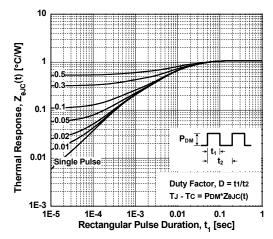


Fig 9. Typical Forward Voltage Drop

- Boost Diode

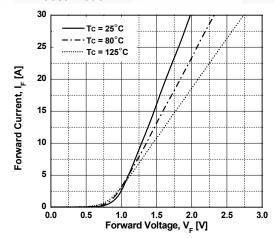


Fig 10. Reverse Recovery Energy vs. Forward Current

- Boost Diode

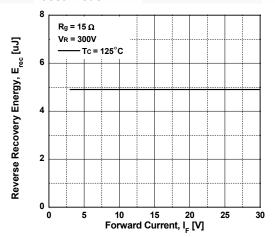
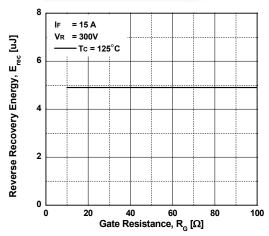
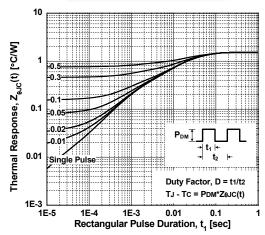


Fig 11. Reverse Recovery Energy vs. Gate Resistance Fig 12. Transient Thermal Impedance

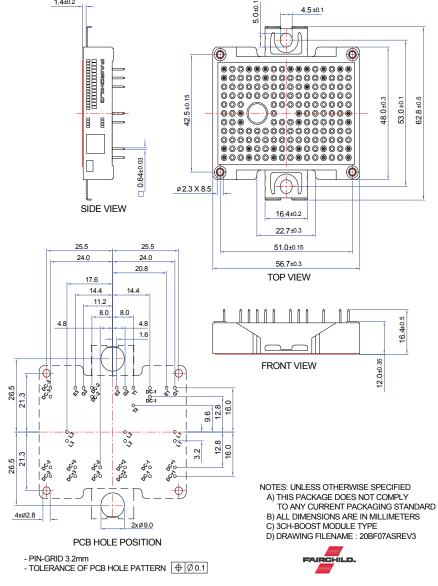
- Boost Diode







Internal Circuit Diagram DC+1 DC+2 DC+3 D2 L1 O-L2 O-L3 O G3 G2 C E2 **E**3 E1 DC-2 DC-3 DC-1 d Package Outlines [mm] 4.5 ±0.1







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