

		– HV Series	50V
I _O	Output Current		Internally limited
PD	Power Dissipation		Internally limited
Т _і	Operating Junction Temperature Range		See Order Information Table
T _{stg}	Storage Temperature		-65 to 150°C

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IP137 SERIES LM137 SERIES IP137A SERIES LM137A SERIES **IP337 SERIES IP337A SERIES**

							'A , IP13 [.] 'A , LM1:		IP1: LM1				
Parameter		Test Conditions				Min.	Тур.	Max.	Min.	Тур.	Max.	Units	
					-1.238	-1.25	-1.262	-1.225	-1.25	-1.275	V		
V _{REF}	Reference Voltage	I_{OUT} =10mA to I_{MAX} T _J = -55 to 150°C											
		$V_{IN} - V_{OUT} = 3V \text{ to } V_{MAX} \qquad P \le P_{MAX}$			$P \le P_{MAX}$	-1.220	-1.25	-1.280	-1.200	-1.250	-1.300	V	
ΔV_{OUT}	Line Regulation ¹	$V_{IN} - V_{OUT} =$	3V to	V _{MAX}			0.005	0.010		0.010	0.020	%/V	
ΔI_{OUT}		T _J = -55 to 150°C				0.010	0.030		0.020	0.050	70/ V		
		$I_{OUT} = 10$ mA to I_{MAX} $V_{OUT} \le 5V$				5	25		15	25	mV		
ΔV_{OUT}	Load Regulation ¹				$V_{OUT} \ge 5V$		0.1	0.5		0.3	0.5	%	
ΔI_{OUT}	LOAU Regulation	$I_{OUT} = 10 \text{mA}$	to I _{MA2}	х	$V_{OUT} \le 5V$		10	50		20	50	mV	
		$T_J = -55$ to $150^{\circ}C$ $V_{OUT} \ge 5V$			0.2	1		0.3	1	%			
	Thermal Regulation	$t_p = 10ms$ $T_A = 25^{\circ}C$			0.002	0.020		0.002	0.02	%/W			
		LCC4 Packag			C4 Package			0.040			0.040	- 70/ V V	
		101/			$C_{ADJ} = 0$	60	66			60		dB	
	Ripple Rejection	V _{OUT} = -10V f = 120Hz			C _{ADJ} = 10μF ·55 to 150°C	70	80		66	77		dB	
I _{ADJ}	Adjust Pin Current	T _J = -55 to 15	0°C			65	100		65	100	μA		
	Adjust Pin Current Change		١ _c	$I_{OUT} = 10$ mA to I_{MAX} $V_{IN} - V_{OUT} = 3$ V to 40V			0.2	2		0.5	5		
ΔI_{ADJ}		-	V _{IN} -				1.0	5		2	5		
			$V_{IN} - V_{OUT} = 3V \text{ to } 50V$								μA		
	-			(HV SERIES)			2.0	6		3	6		
I _{MIN}	Minimum Load			V _{IN} –	$V_{OUT} \le 40V$		2.5	5		2.5	5		
	Current	T _J = -55 to 15	-55 to 150°C 📖		$V_{OUT} \le 10V$		1.2	3		1.2	3	– mA	
				V _{IN} –	$V_{OUT} \le 15V$	1.5	2.2	3.2	1.5	2.2	3.2		
		rent Limit $T_J = -55$ to $150^{\circ}C$		V _{IN} –	$V_{OUT} = 40V$	0.24	0.24 0.4		0.24	0.4			
I _{CL}	Current Limit			V _{IN} –	$V_{OUT} = 50V$	0.2 0.4					0.8	A	
				(HV S	SERIES)			0.8	0.2	0.2 0.4			
	Temperature Stability	T _J = -55 to 150°C				0.6	1.5		0.6		%		
ΔV_{OUT}		T _A = +125°C											
	Long Term Stability	t = 1000 Hrs			0.3	1		0.3	1	%			
e _n	RMS Output Noise	f = 10 Hz to 10 kHz				0.000			0.000		o/		
	(% of V _{OUT})	T _A = 25°C				0.003			0.003		%		
		K Package				2.3	3		2.3	3			
R _{θJC}	Thermal Resistance	R Package				5	7		5	7			
	Junction to Case	G Package				3	5		3	5	°C/W		
		LCC4 Packag	je					13			13	1	

1) Regulation is measured at constant junction temperature, using pulse testing at a low duty cycle. Changes in output voltage due to heating effects are covered under thermal regulation specifications. Load regulation is measured at a point 1/8" from the bottom of the package for the TO-3 and TO-66 packages, at the junction of the wide and narrow portion of the output lead for the SMD packages, and 1/8" below the base of the package on the output pin of the TO-257 package.

2) Test Conditions unless otherwise stated: VIN - VOUT = 5V, IOUT = 0.5A, PMAX = 20W, IMAX = 1.5A, VMAX = 40V for standard series, 50V for HV series.

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IP137 SERIES LM137 SERIES IP137A SERIES LM137A SERIES **IP337 SERIES IP337A SERIES**

					I	IP337A P337AH	v	IP3 LM3					
Parameter		Test Conditions			Min.	Тур.	Max.	Min.	Тур.	Max.	Units		
		I _{OUT} = 10mA				-1.238	-1.25	-1.262	-1.213	-1.25	-1.287	V	
		I_{OUT} = 10mA to I_{MAX}											
V _{REF} R	Reference Voltage	$V_{IN} - V_{OUT} = 3V$ to V_{MAX}				-1.220	-1.25	-1.280	-1.200	-1.250	-1.300	V	
		$P \le P_{MAX}$ $T_J = 0 \text{ to } 125^{\circ}\text{C}$											
ΔV _{OUT}	· 1	$V_{IN} - V_{OUT} = 3$	3V to '	V _{MAX}			0.005	0.010		0.010	0.040	0/ 1/	
ΔI_{OUT} L	_ine Regulation ¹		[T _J =	= 0 to 125°C		0.010	0.030		0.020	0.070	- %/V	
		$I_{OUT} = 10$ mA to I_{MAX} $V_{OUT} \le 5V$				5	25		15	50	mV		
ΔV _{OUT}	1				V _{OUT} ≥ 5V		0.1	0.5		0.3	1	%	
ΔI_{OUT} L	_oad Regulation ¹	$I_{OUT} = 10 \text{mA}$	to I _{MA>}	x	V _{OUT} ≤ 5V		10	50		20	70	mV	
		$T_J = 0 \text{ to } 125^{\circ}\text{C}$ $V_{OUT} \ge 5V$				0.2	1		0.3	1.5	%		
Т	Thermal Regulation	$t_p = 10ms$			$T_A = 25^{\circ}C$		0.002	0.020		0.003	0.04	%/W	
		•		$C_{ADJ} = 0$	60	66			60		dB		
R	Ripple Rejection	V _{OUT} = 10V f = 120Hz		0	$C_{ADJ} = 10 \mu F$								
					= 0 to 125°C	70	80		66	77		dB	
I _{ADJ} A	Adjust Pin Current	T _J = 0 to 125°	-				65	100		65	100	μA	
	Adjust Pin Current Change	•		оuт = 10	0mA to I _{MAX}		0.2	2		0.5	5		
ΔI _{ADJ} A				$V_{\rm IN} - V_{\rm OUT} = 3V \text{ to } 40V$			1.0	5		2	5		
		l č L		$V_{IN} - V_{OUT} = 3V \text{ to } 50V$ (HV SERIES)			2.0	6		3	6	μA	
	-												
I _{MIN} N	Vinimum Load	T 0/ 10500		V _{IN} –	$V_{OUT} \le 40V$		2.5	5		2.5	10		
С	Current	$T_{\rm J} = 0$ to 125°	C	$V_{\rm IN} - V_{\rm OUT} \le 10$			1.2	3		1	6	mA	
				V _{IN} –	V _{OUT} ≤ 15V	1.5	2.2	3.5	1.5	2.2	3.5		
				V _{IN} –	$V_{OUT} = 40V$	0.24	0.4	1	0.15	0.4			
	Current Limit	Current Limit $T_J = 0$ to 125		C	V _{IN} –	V _{OUT} = 50V							- A
				(HV S	ERIES)	0.2 0.4		0.8	0.1	0.4	0.8		
ΔV _{OUT} Τ	Temperature	T 01 1050		I				4 5					
ATEMP S		$T_{J} = 0$ to 125°C			0.6	1.5		0.6		%			
ΔV _{OUT}	T OLLIN												
	_ong Term Stability	t = 1000 Hrs					0.3	1		0.3	1	%	
e _n R	RMS Output Noise	f = 10 Hz to 10 kHz			0.000				0.000		%		
	(% of V_{OUT}) $T_A = 25^{\circ}C$				0.003	0.003		0.003					
R _{θJC} T	Thermal Resistance	K Package				2.3	3		2.3	3			
	Junction to Case	T Package				4	5		4		°C/W		
		LCC4 Package					13			13	1		

1) Regulation is measured at constant junction temperature, using pulse testing at a low duty cycle. Changes in output voltage due to heating effects are covered under thermal regulation specifications. Load regulation is measured at a point 1/8" from the bottom of the package for the TO-3 and TO-66 packages, at the junction of the wide and narrow portion of the output lead for the SMD packages, and ¹/₈" below the base of the package on the output pin of the TO-257 package.

2) Test Conditions unless otherwise stated:

 $V_{IN} - V_{OUT} = 5V$, $I_{OUT} = 0.5A$, $P_{MAX} = 20W$, $I_{MAX} = 1.5A$

 V_{MAX} = 40V for standard series , 50V for HV series.

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	IP137 SERIES	LM137 SERIES
	IP137A SERIES	LM137A SERIES
SEME	IP337 SERIES	
LAB	IP337A SERIES	

APPLICATIONS INFORMATION

High Stability Regulator

-

The output stability, load regulation, line regulation, thermal regulation, temperature drift, long term drift, and noise, can be improved by a factor of 6.6 over the standard regulator configuration. This assumes a zener has 20PPM/°C maximum drift and about 10 times lower noise than the regulator.



In the application shown below, regulator #2 to "N" will track regulator #1 to within ± 24 mV initially, and to ± 60 mV over all load, line, and temperature conditions.

If any regulator output is shorted to ground, all other outputs will drop to -2V. Load regulation of regulators 2 to "N" will be improved by V_{OUT} / 1.25V compared to a standard regulator, so regulator #1 should be the one which has the lowest load current.

C 1

SOLID TANTALUN



Current Regulator

Order Information

Part Number	IG–Pack G–Pack (TO257)	SMD1	SMD05	LCC4	K–Pack (TO3)	R–Pack (TO66)	T–Pack (TO220)	Temp. Range
LM137	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark		-55 to +150°C
LM137HV	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark		"
LM137A	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark		"
LM137AHV	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark		"
IP137	✓	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark		"
IP137HV	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark		"
IP137A	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark		"
IP137AHV	✓	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark		"
LM337					\checkmark		\checkmark	0 to 125°C
LM337HV					\checkmark		\checkmark	"
IP337					\checkmark		\checkmark	"
IP337HV					\checkmark		\checkmark	"
IP337A					\checkmark		\checkmark	"
IP337AHV					\checkmark		\checkmark	"

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* Solid Tantalum ** R1 or R5 may be trimmed slightly to improve tracking.

Dual Tracking Supply