

50 V QUAD DARLINGTON SWITCHES

- FOUR NPN DARLINGTONS WITH ISOLATED CONNECTIONS
- OUTPUT CURRENT TO 1.5 A EACH DARLING-TON
- MINIMUM BREAKDOWN 50 V
- MULTIWATT PACKAGE ALLOWS OPERA-TION AT 1.5 A, 50 V, 100 % DUTY CYCLE, ALL FOUR DEVICES ON
- INTEGRAL SUPPRESSION DIODES
- VERSIONS FOR 5 VAND 6-15 V LOGIC FAMI-LIES

The L7150 has 350 input resistors and is compatible with TTL, DTL, LSTTL and 5 V CMOS logic. The L7152 has 3 K Ω input resistors for use with 6-15 V CMOS and PMOS logic.

These devices are suitable for driving a wide range of inductive and non-inductive loads including DC motors, stepper motors, solenoids, relays, lamps, multiplexed LEDs and heaters.



DESCRIPTION

The L7150 and L7152 are 1.5 A quad darlington arrays mounted in the 15-lead Myltiwatt[®] plastic package. Each darlington is equipped with a suppression diode for inductive loads and all three terminals are isolated.

CONNECTION DIAGRAM (top view)



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
VCEX	Output Voltage	50	V
l _o	Output Current	1.75	A
Vi	Input Voltage	30	V
l _B	Input Current	25	mA
Ptot	Power Dissipation (T _{case} = 75 °C)	25	w
Tamb	Operating Ambient Temperature Range	0 to 70	°C
Tstg	Storage Temperature	– 55 to 150	°C

SCHEMATIC DIAGRAM



ELECTRICAL CHARACTERISTICS ($T_{amb} = 25 \ ^{\circ}C$ unless otherwise specified)

Symbol	Parameter	Test Condtions		Min.	Тур.	Max.	Unit	Fig.
ICEX	Output Leakage Current	V _{CE} = 50 V V _{CE} = 50 V	$T_{amb} = 70^{\circ}C$			100 500	μΑ μΑ	1
V _{CER (sus)}	Collector-emitter Sustaining Voltage*	I _C = 100 mA	V _i = 0.4 V	35			v	2
V _{CE (sat)}	Collector-emitter Saturation Voltage	$I_{C} = 500 \text{ mA}$ $I_{C} = 750 \text{ mA}$ $I_{C} = 1 \text{ A}$ $I_{C} = 1.25 \text{ A}$				1.15 1.3 1.4 1.5	V V V V	3
l _{i(on)}	Input Current	for L7150 for L7150 for L7152 for L7152		1.4 3.3 0.6 0.7		4.3 9.6 1.8 5.2	mA mA mA mA	4
V _{i(on)}	Input Voltage	for L7150 $V_{CE} = 2 V$ $V_{CE} = 2 V$ for L7152 $V_{CE} = 2 V$ $V_{CE} = 2 V$	$I_{C} = 1 A$ $I_{C} = 1.5 A$ $I_{C} = 1 A$ $I_{C} = 1.5 A$			2 2.5 6.5 10	V V V V	5
tPLH	Turn-on Delay Time	0.5 V_i to 0.5 V_o				1	μs	
t PHL	Turn-off Delay Time	0.5 V _i to 0.5 V	0			1.5	μs	

(*) $t_{(sus)} = 10 \ \mu s$.



THERMAL DATA

Rth i-case	Themal Resistance Junction-case	Max	3	°C/W
Rth j-amb	Thermal Resistance Junction-ambient	Max	35	°C/W

TEST CIRCUIT

Figure 1.



Figure 3.



Figure 5.



Figure 2.









MOUNTING INSTRUCTIONS

The power dissipated in the circuit must be removed by adding an external heatsink.

Thanks to the Multiwatt[®] package attaching the heatsink is very simple, a screw or compression spring (clip) being sufficient. Between the heatsink

Figure 6 : Mounting Example.

and the package it is better to insert a layer of silicon grease, to optimize the thermal contact ; no electrical isolation is needed between the two surfaces.



