Monolithic Digital IC

### LB1840M

SANYO : MFP14S



## Low-voltage/Low-saturation Bidirectional Variable Constant-voltage Motor Driver

[LB1840M]

1.0

B

AAAA

888887

8.0

**Package Dimensions** 

14

A A

unit: mm

3111-MFP14S

# Overview

The LB1840M is a low-voltage, low-saturation, three-input type two-channel bidirectional motor driver that permits switching between constant-voltage regulated output and saturated output. The design is ideal for a two-phase bipolar driver for stepping motors.

# Features

- Wide operating voltage range (3.0 to 9.0 V).
- Low saturation voltage  $V_{O(sat)} = 0.40$  V at  $I_O = 200$  mA.
- Consumes almost no current in standby mode (0.1 µA or less).
- Permits setting of bidirectional constant-voltage regulated value.
- Three-input type that is ideal for a two-phase bipolar driver.
- Permits switching between constant-voltage regulated output and saturated output.
- Built in reference voltage coupled to input.
- Compact MFP-14S package.

# **Specifications**

### Absolute Maximum Ratings at Ta = 25 $^\circ C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max		10.5	V
Output current	Im max		250	mA
Applied input voltage	V <sub>IN</sub>		-0.3 to +10	V
Allowable power dissipation	Pd max	With board ( $30 \times 30 \times 1.5 \text{ mm}^3$ )	800	mW
Operating temperature	Topr		-20 to +80	°C
Storage temperature	Tstg		-40 to +150	°C

### Allowable Operating Ranges at Ta = 25 °C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V <sub>CC</sub>		3.0 to 9.0	V
IN pin high level voltage	V <sub>INH</sub>		3.0 to 9.0	V
IN pin low level voltage	V <sub>INL</sub>		-0.3 to +0.7	V
Control voltage	V <sub>C</sub>		0.2 to 6.0	V
VM pin high level voltage	VMH		$V_{CC}$ – 0.3 to $V_{CC}$	V
VM pin low level voltage	VML		–0.3 to V <sub>CC</sub> –2.5	V

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# Electrical Characteristics at Ta = 25 $^\circ C,$ $V_{CC}$ = 6 V

Parameter	Symbol	Conditions	min	typ	max	Unit
Supply current	I <sub>CC</sub> 0	During standby		0.1	10	μA
	I <sub>CC</sub> 1	During bidirectional operation (for two channels):		3.5	5.0	mA
	Icc2 during control, load open   during saturation, load open			4.5	6.5	mA
Output saturation voltage	V <sub>sat1</sub>	I <sub>O</sub> = 100 mA (upper side + lower side)		0.30	0.40	V
	V <sub>sat2</sub>	I <sub>O</sub> = 200 mA (upper side + lower side)		0.40	0.55	V
	V <sub>Osat3</sub>	I <sub>O</sub> = 200 mA (lower side)	0.07	0.10	0.15	V
Reference voltage	V <sub>ref</sub>	I <sub>Vref</sub> = 1 mA	1.85	2.0	2.15	V
Output voltage voltage characteristics	$\frac{\Delta V_{O}}{\Delta V_{CC}}$	$V_{O} = 5 \text{ V}, V_{CC} = 5.5 \text{ to } 9 \text{ V},$ $I_{O} = 100 \text{ mA}$			20	mV
Output voltage current characteristics	$\frac{\Delta V_{O}}{\Delta I_{O}}$	$V_{O} = 5 V, V_{CC} = 6 V,$ $I_{O} = 10 \text{ to } 100 \text{ mA}$			50	mV
Input ourrent	I <sub>IN</sub>	$V_{IN} = 5 V$		90	150	μA
Input current	I <sub>VM</sub>	V <sub>M</sub> = GND		210	300	μA
Output voltage	Vo	Between OUT and GND	$2.45 \times V_{C}$		$2.65 \times V_{C}$	V

## Equivalent Circuit Block Diagram



**Pin Assignment** 



### **Truth Table**

	Input		Output		Mode
ENA	IN 1/2	VM	OUT 1/3	OUT 2/4	Mode
L	—		OFF	OFF	Standby
н	L	Н	Н	L	Constant-voltage regulated forward operation
н	L	L	Н	L	Saturated forward operation
н	н	Н	L	н	Constant-voltage regulated reverse operation
н	н	L	L	Н	Saturated reverse operation

The constant-voltage regulated output  $V_O$  (= voltage between H side output and GND) is controlled by  $V_O = 2.5 \times V_C$ . The output is in the saturated state when the  $V_C$  input range is 0.2 to 6 V and  $V_O \ge V_{CC}$ .

#### **Pin Functions**

Pin No.	Symbol	Equivalent Circuit Diagram	Pin Function
14	V <sub>CC</sub>		Power supply pin for output and controller.
9 12	GND		GND pins for output and controller. Both must be grounded.
3 6	IN2 IN1	Vcc Ska Ska Ska Ska Ska Ska Ska Ska	Input pins that determine the excitation of the outputs. IN1 control outputs OUT1 and OUT2; IN2 control outputs OUT3 and OUT4. L: -0.3 to +0.7 V or open H: 3.0 to 9.0 V There are no limitations on the magnitude relationships between the V <sub>CC</sub> and V <sub>IN</sub> supply voltages.
8 10 11 13	OUT4 OUT3 OUT1 OUT2		Output pins. Have built-in spark killer diodes.
4	Vref	Vref 5.6k0 10k0 77 A03841	Reference voltage (= 2.0 V).

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Pin No.	Symbol	Equivalent Circuit Diagram	Pin Function
5	V <sub>C</sub>	Vcc Vc Vc Vc Vc Vc Vc Vc	Input pins that determine the constant- voltage regulated output level. The constant-voltage regulated output V <sub>O</sub> (= voltage between H side output and GND) is controlled by V <sub>O</sub> = $2.5 \times V_C$ . There are no limitations on the magnitude relationships between the V <sub>CC</sub> and V <sub>C</sub> supply voltages.
2	ENA		Standby/drive control input pin. Current drain in standby mode is 10 μA or less. L: -0.3 to + 0.7 V or open H: 3.0 to 9.0 V There are no limitation on the magnitude relationships between V <sub>CC</sub> and V <sub>ENA</sub> supply voltage.
7	VM		Control input pin for switching between constant voltage output and saturated output. L: -0.3 to V <sub>CC</sub> - 2.5 V (saturated output) H: V <sub>CC</sub> - 0.3 to V <sub>CC</sub> or open (constant-voltage regulated output)

## **Sample Application Circuit**





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