

# LH0004/LH0004C High Voltage Operational Amplifier

#### **General Description**

The LH0004/LH0004C is a general purpose operational amplifier designed to operate from supply voltages up to  $\pm\,40V.$  The device dissipates extremely low quiescent power, typically 8 mW at 25°C and V<sub>S</sub> =  $\pm\,40V.$ 

The LH0004's high gain and wide range of operating voltages make it ideal for applications requiring large output swing and low power dissipation.

The LH0004 is specified for operation over the  $-55^{\circ}$ C to  $+125^{\circ}$ C military temperature range. The LH0004C is specified for operation over the 0°C to  $+85^{\circ}$ C temperature range.

- Low input offset current typically 20 nA for the LH0004 and 45 nA for the LH0004C
- □ Low input offset voltage typically 0.3 mV
- ☐ Frequency compensation with 2 small capacitors
- Low power consumption 8 mW at ±40V

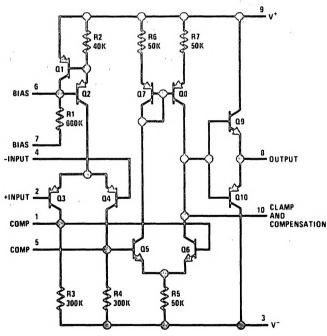
#### **Applications**

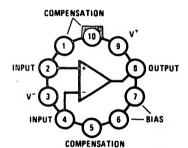
- ☐ Precision high voltage power supply
- Resolver excitation
- Wideband high voltage amplifier
- Transducer power supply

#### **Features**

- $\Box$  Capable of operation over the range of  $\pm 5V$  to  $\pm 40V$
- □ Large output voltage typically ±35V for the LH0004 and ±33V for the LH0004C into a 2 kΩ load with ±40V supplies

## **Schematic and Connection Diagrams**





TL/H/5559-2

Note: Pin 7 must be grounded or connected to a voltage at least 5V more negative than the positive supply (Pin 9). Pin 7 may be connected to the negative supply; however, the standby current will be increased. A resistor may be inserted in series with Pin 7 to Pin 9. The value of the resistor should be a maximum of 100  $k\Omega$  per volt of potential between Pin 3 and Pin 9.

Order Number LH0004H or LH0004CH See NS Package Number H10G

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### **Absolute Maximum Ratings**

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications. (Note 2)

Supply Voltage, Continuous Power Dissipation (see Curve) Differential Input Voltage

Input Voltage

± 45V

400 mW ±7V

**Equal to Supply** 

**Short Circuit Duration** 

Operating Temperature Range I H0004

LH0004C Storage Temperature Range

Lead Temperature (Soldering, 10 sec.)

ESD rating to be determined.

3 sec

0°C to +85°C -65°C to +150°C

-55°C to +125°C

260°C

#### **Electrical Characteristics** (Note 1)

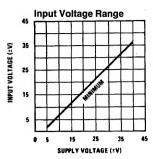
Parameter	Conditions	LH0004			LH0004C			Units
		Min	Тур	Max	Min	Тур	Max	Cills
Input Offset Voltage	$R_S \le 100\Omega$ , $T_A = 25^{\circ}C$ $R_S \le 100\Omega$		0.3	1.0 2.0		0.3	1.5 3.0	mV
Input Bias Current	T <sub>A</sub> = 25°C		20	100 300		30	120 300	nA
Input Offset Current	T <sub>A</sub> = 25°C		3	20 100		10	45 150	nA
Positive Supply Current	$V_S = \pm 40V, T_A = 25^{\circ}C$ $V_S = \pm 40V$		110	150 175		110	150 175	μА
Negative Supply Current	$V_S = \pm 40V, T_A = 25^{\circ}C$ $V_S = \pm 40V$		80	100 135		80	100 135	μА
Voltage Gain	$V_S = \pm 40V, R_L = 100k, T_A = 25^{\circ}C$ $V_{OUT} = \pm 30V$	30	60		30	60		V/mV
	$V_S = \pm 40V, R_L = 100k$ $V_{OUT} = \pm 30V$	10			10			V/m\
Output Voltage	$V_S = \pm 40V, R_L = 10k$		±35	±30		±33	±30	٧
CMRR	$V_S = \pm 40V, R_S \le 5k$ $V_{IN} = \pm 33V$	70	90		70	90		dB
PSRR	$V_S = \pm 40V$ , $R_S \le 5k$ $\Delta V = 20V$ to $40V$	70	90		70	90		dB
Average Temperature Coefficient Offset Voltage	$R_S \le 100\Omega$		4.0			4.0		μV/°(
Average Temperature Coefficient of Offset Current			0.4			0.4		nA/°
Equivalent Input Noise Voltage	$R_S = 100\Omega, V_S = \pm 40V$ f = 500 Hz to 5 kHz, $T_A = 25^{\circ}C$		3.0			3.0		μVrm

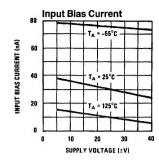
Note 1: These specifications apply for ±5V ≤ V<sub>S</sub> ≤ ±40V, Pin 7 grounded, with capacitors C1 = 39 pF between Pin 1 and Pin 10, C2 = 22 pF between Pin 5 and ground, -55°C to +125°C for the LH0004, and 0°C to +85°C for the LH0004C unless otherwise specified.

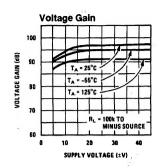
Note 2: Refer to RETS0004X for LH0004H military specifications.

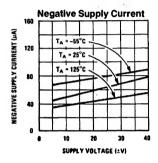
# **Typical Applications Voltage Follower** Input Offset Voltage Adjust LH0004 LH0004 - OUTPUT \*May be zero or equal to source resistance for minimum offset. TL/H/5559-3 TL/H/5559-4 **External Current High Compliance Limiting Method Current Source** 10K 10K INPUTS $I_O = \frac{\pm E_{1N}}{10K}$ LH0004 OUTPUT LH0004 RLIM $^{*}V_{f}=$ average forward voltage drop of diodes D1 to D4 at 20 $\mu A$ to 50 $\mu A$ . $I_{OUT} \le \frac{1}{R_{LIM}}$ 10K TL/H/5559-5 10K TL/H/5559-6

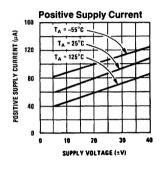
# **Typical Performance Characteristics**

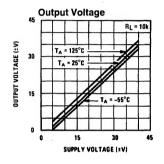


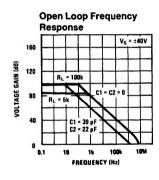


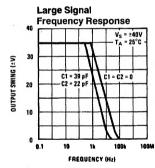


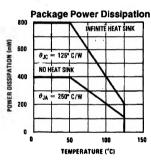












TL/H/5559-7