

National Semiconductor Corporation

LM137/LM337 3-Terminal Adjustable Negative Regulators

General Description

The LM137/LM337 are adjustable 3-terminal negative voltage regulators capable of supplying in excess of -1.5Aover an output voltage range of -1.2V to -37V. These regulators are exceptionally easy to apply, requiring only 2 external resistors to set the output voltage and 1 output capacitor for frequency compensation. The circuit design has been optimized for excellent regulation and low thermal transients. Further, the LM137 series features internal current limiting, thermal shutdown and safe-area compensation, making them virtually blowout-proof against overloads.

The LM137/LM337 serve a wide variety of applications including local on-card regulation, programmable-output voltage regulation or precision current regulation. The LM137/ LM337 are ideal complements to the LM117/LM317 adjustable positive regulators.

Features

- Output voltage adjustable from -1.2V to -37V
- 1.5A output current guaranteed, -55°C to +150°C
- Line regulation typically 0.01%/V
- Load regulation typically 0.3%
- Excellent thermal regulation, 0.002%/W

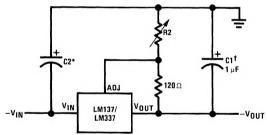
Typical Applications

- 77 dB ripple rejection
- Excellent rejection of thermal transients
- 50 ppm/°C temperature coefficient
- Temperature-independent current limit
- Internal thermal overload protection
- 100% electrical burn-in
- Standard 3-lead transistor package
- Output is short circuit protected

LM137	Series	Packages	and Pow	er Capability

Device	Package	Rated Power Dissipation	Design Load Current		
LM137/337	TO-3 TO-39	20W 2W	1.5A 0.5A		
LM337T	TO-220	15W	1.5A		
LM337M	TO-202	7.5W	0.5A		





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Full output current not available at high input-output voltages

$$-V_{OUT} = -1.25V \left(1 + \frac{R2}{120\Omega}\right) + \left(-I_{ADJ} \times R2\right)$$

 $\dagger C1 = 1 \ \mu F$ solid tantalum or 10 μF aluminum electrolytic required for stability

*C2 = 1 µF solid tantalum is required only if regulator is more than 4" from power-supply filter capacitor

Output capacitors in the range of 1 μ F to 1000 μ F of aluminum or tantalum electrolytic are commonly used to provide improved output impedance and rejection of transients

Absolute Maximum Ratings

If Military/Aerospace specified devices are required, contact the National Semiconductor Sales Office/ Distributors for availability and specifications. (Note 4)

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Power Dissipation	Internally Limited
Input-Output Voltage Differential	40V
Operating Junction Temperature Range	
LM137	-55°C to +150°C
LM337	0°C to + 125°C

Storage Temperature-65°C to+ 150°CLead Temperature (Soldering, 10 sec.)300°CPlastic Package (Soldering, 4 sec.)260°CESD Rating2k Volts

Preconditioning

Burn-In in Thermal Limit

100% All Devices

Electrical Characteristics (Note 1)

Parameter	Conditions	LM137			LM337			Units
rarameter		Min	Тур	Max	Min	Тур	Max	
Line Regulation	$\begin{array}{l} T_{j}=25^{\circ}\text{C}, 3V\leq \left V_{IN}-V_{OUT}\right \leq 40V\\ (\text{Note 2}) \text{ I}_{L}=10 \text{ mA} \end{array}$		0.01	0.02		0.01	0.04	%/V
Load Regulation	T_j = 25°C, 10 mA $\leq I_{OUT} \leq I_{MAX}$		0.3	0.5		0.3	1.0	%
Thermal Regulation	$T_j = 25^{\circ}C$, 10 ms Pulse		0.002	0.02		0.003	0.04	%/W
Adjustment Pin Current			65	100		65	100	μA
Adjustment Pin Current Charge	$ \begin{array}{l} 10 \text{ mA} \leq I_L \leq I_{MAX} \\ 3.0V \leq V_{IN} - V_{OUT} \leq 40V, \\ T_A = 25^\circ\text{C} \end{array} $		2	5		2	5	μA
Reference Voltage	$ \begin{array}{l} T_j = 25^\circ C \text{ (Note 3)} \\ 3V \leq V_{IN} - V_{OUT} \leq 40 \text{V}, \text{ (Note 3)} \\ 10 \text{ mA} \leq I_{OUT} \leq I_{MAX}, P \leq P_{MAX} \end{array} $		1.250 1.250			-1.250 -1.250		v v
Line Regulation	$ 3V \le V_{IN} - V_{OUT} \le 40V$, (Note 2)		0.02	0.05		0.02	0.07	%/V
Load Regulation	$10 \text{ mA} \le I_{OUT} \le I_{MAX}$, (Note 2)		0.3	1		0.3	1.5	%
Temperature Stability	$T_{MIN} \le T_j \le T_{MAX}$		0.6			0.6		%
Minimum Load Current	$ V_{IN} - V_{OUT} \le 40V$ $ V_{IN} - V_{OUT} \le 10V$		2.5 1.2	5 3		2.5 1.5	10 6	mA mA
Current Limit	$\begin{split} V_{IN} - V_{OUT} &\leq 15V\\ K \text{ and T Package}\\ H \text{ and P Package}\\ V_{IN} - V_{OUT} &= 40V, T_j = 25^\circ\text{C}\\ K \text{ and T Package}\\ H \text{ and P Package} \end{split}$	1.5 0.5 0.24 0.15	2.2 0.8 0.4 0.17	3.5 1.8	1.5 0.5 0.15 0.10	2.2 0.8 0.4 0.17	3.7 1.9	A A A
RMS Output Noise, % of VOUT	$T_j = 25^{\circ}C$, 10 Hz $\leq f \leq 10$ kHz		0.003			0.003		%
Ripple Rejection Ratio		66	60 77		66	60 77		dB dB
Long-Term Stability	T _j = 125°C, 1000 Hours		0.3	1		0.3	1	%
Thermal Resistance, Junction to Case	H Package K Package T Package P Package		12 2.3	15 3		12 2.3 4 7	15 3	°C/W °C/W °C/W °C/W
Thermal Resistance, Junction to Ambient (No Heat Sink)	H Package K Package T Package P Package		140 35			140 35 50 80		°C/W °C/W °C/W °C/W

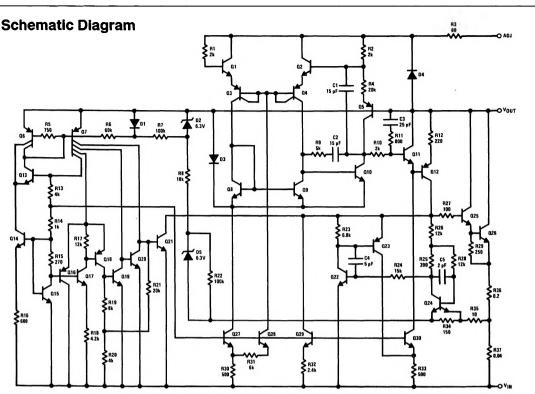
Note 1: Unless otherwise specified, these specifications apply -55° C $\leq T_{j} \leq +150^{\circ}$ C for the LM137, 0°C $\leq T_{j} \leq +125^{\circ}$ C for the LM337; V_{IN} - V_{OUT} = 5V; and I_{OUT} = 0.1A for the TO-39 and TO-202 packages and I_{OUT} = 0.5A for the TO-3 and TO-220 packages. Although power dissipation is internally limited, these specifications are applicable for power dissipations of 2W for the TO-39 and TO-202 nackages, and 0.5A for the TO-32 package and 0.2A for the TO-39 package.

Note 2: Regulation is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output voltage due to heating effects are covered under the specification for thermal regulation. Load regulation is measured on the output pin at a point 1/e" below the base of the TO-3 and TO-39 packages.

Note 3: Selected devices with tightened tolerance reference voltage available.

Note 4: Refer to RETS137H drawing for LM137H or RETS137K drawing for LM137K military specifications.

LM137/LM337



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In Figure 1, a typical LM137's output drifts only 3 mV (or

0.03% of $V_{OUT} = -10V$) when a 10W pulse is applied for 10 ms. This performance is thus well inside the specification

limit of 0.02%/W \times 10W = 0.2% max. When the 10W

pulse is ended, the thermal regulation again shows a 3 mV

step at the LM137 chip cools off. Note that the load regula-

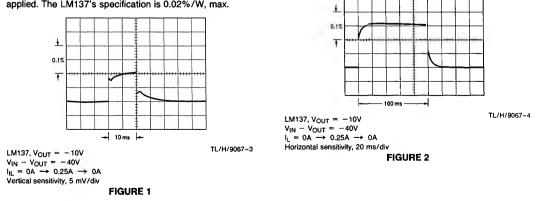
tion error of about 8 mV (0.08%) is additional to the thermal

regulation error. In Figure 2, when the 10W pulse is applied

for 100 ms, the output drifts only slightly beyond the drift in the first 10 ms, and the thermal error stays well within 0.1%

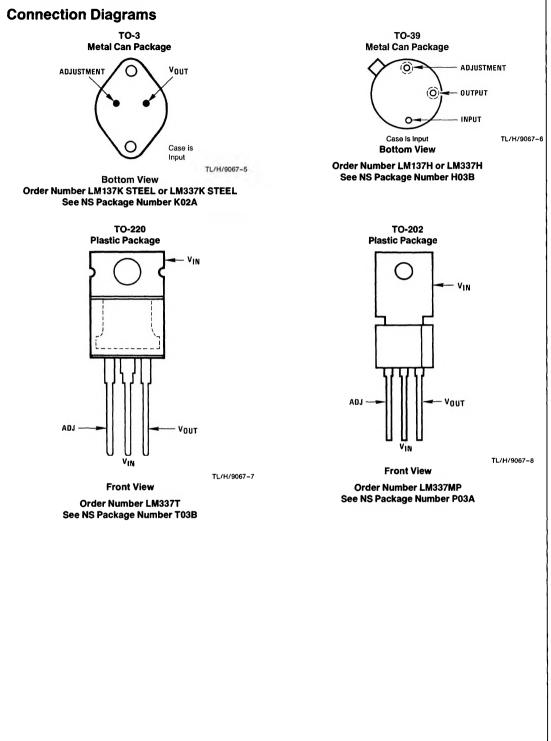
Thermal Regulation

When power is dissipated in an IC, a temperature gradient occurs across the IC chip affecting the individual IC circuit components. With an IC regulator, this gradient can be especially severe since power dissipation is large. Thermal regulation is the effect of these temperature gradients on output voltage (in percentage output change) per Watt of power change in a specified time. Thermal regulation error is independent of electrical regulation or temperature coefficient, and occurs within 5 ms to 50 ms after a change in power dissipation. Thermal regulation depends on IC layout as well as electrical design. The thermal regulation of a voltage regulator is defined as the percentage change of V_{OUT}, per Watt, within the first 10 ms after a step of power is applied. The LM137's specification is 0.02%/W, max.



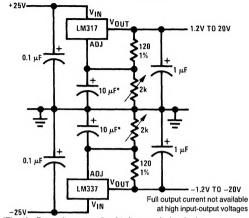
(10 mV).

LM137/LM337

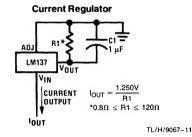


Typical Applications (Continued)

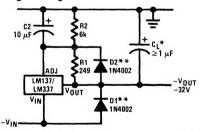
Adjustable Lab Voltage Regulator



*The 10 μ F capacitors are optional to improve ripple rejection TL/H/9067-9



Negative Regulator with Protection Diodes

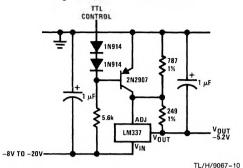


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*When C_L is larger than 20 $\mu F,$ D1 protects the LM137 in case the input supply is shorted

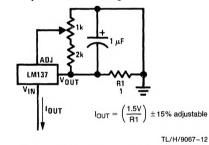
• When C2 is larger than 10 μF and $-V_{OUT}$ is larger than -25V, D2 protects the LM137 in case the output is shorted

-5.2V Regulator with Electronic Shutdown*

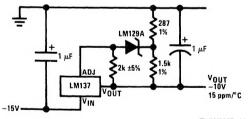


*Minimum output $\simeq -1.3V$ when control input is low

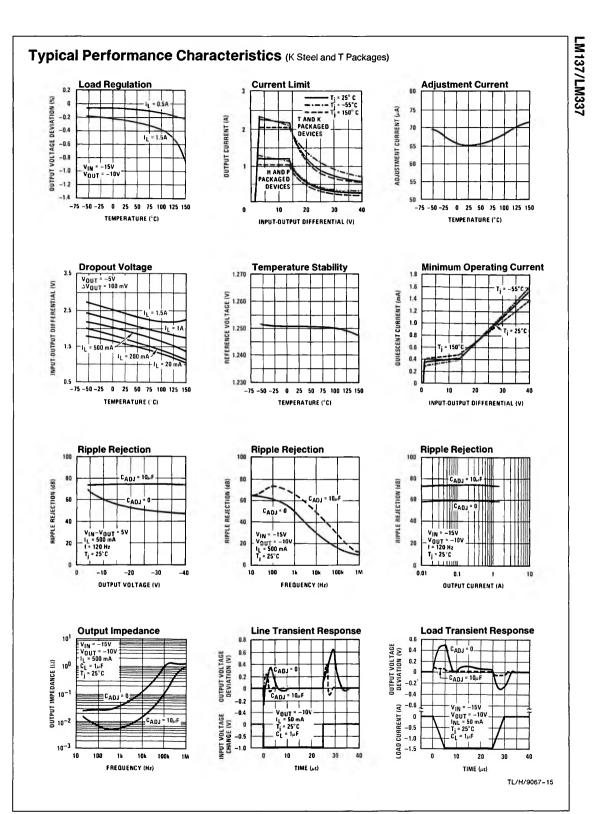
Adjustable Current Regulator



High Stability - 10V Regulator



TL/H/9067-14



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