

LM195QML Ultra Reliable Power Transistors

FEATURES

- Internal Thermal Limiting
- Greater Than 1.0A Output Current
- 3.0 µA Typical Base Current
- 500 ns Switching Time
- 2.0V Saturation
- Base Can be Driven up to 40V Without Damage
- · Directly Interfaces with CMOS or TTL
- 100% Electrical Burn-in

DESCRIPTION

The LM195 is a fast, monolithic power integrated circuit with complete overload protection. This device, which acts as a high gain power transistor, has included on the chip, current limiting, power limiting, and thermal overload protection making it virtually impossible to destroy from any type of overload.

The inclusion of thermal limiting, a feature not easily available in discrete designs, provides virtually absolute protection against overload. Excessive power dissipation or inadequate heat sinking causes the thermal limiting circuitry to turn off the device preventing excessive heating.

The LM195 offers a significant increase in reliability as well as simplifying power circuitry. In some applications, where protection is unusually difficult, such as switching regulators, lamp or solenoid drivers where normal power dissipation is low, the LM195 is especially advantageous.

The LM195 is easy to use and only a few precautions need be observed. Excessive collector to emitter voltage can destroy the LM195 as with any power transistor. When the device is used as an emitter follower with low source impedance, it is necessary to insert a 5.0k resistor in series with the base lead to prevent possible emitter follower oscillations. Although the device is usually stable as an emitter follower, the resistor eliminates the possibility of trouble without degrading performance. Finally, since it has good high frequency response, supply bypassing is recommended.

Connection Diagram

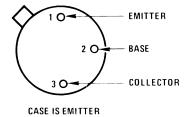


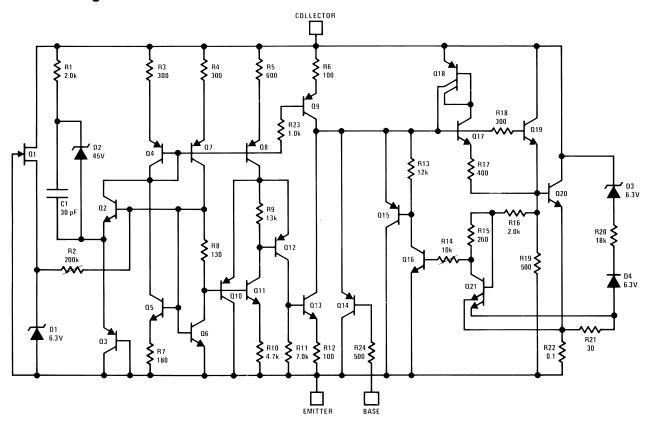
Figure 1. 5-Pin TO - Bottom View See NDT0003A Package

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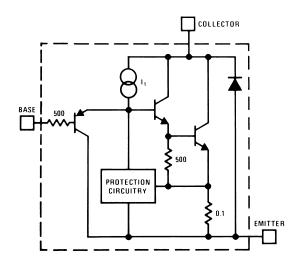
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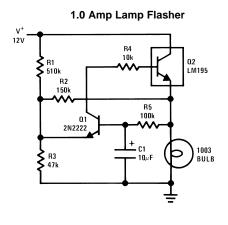


Schematic Diagram



Simplified Circuit







These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

www.ti.com

Absolute Maximum Ratings(1)

	<u> </u>			
Collector to Emitter Voltage	42V			
Collector to Base Voltage	42V			
Base to Emitter Voltage (Forward)			42V	
Base to Emitter Voltage (Reverse)			20V	
Collector Current			Internally Limited	
Power Dissipation (2)	Internally Limited			
Operating Temperature Range	TO package	-55°C ≤ T _A ≤ +125°C		
Storage Temperature Range	•		-65°C ≤ T _A ≤ +150°C	
Lead Temperature (Soldering, 10	sec.)		260°C	
Thermal Resistance	Thermal Resistance θ _{JA} TO package; Still Air at 0.5W			
		TO package; 500LF/Min Air Flow at 0.5W	66°C/W	
	θ_{JC}	TO package at 1.0W	29°C/W	

⁽¹⁾ Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not ensure specific performance limits. For ensured specifications and test conditions, see the Electrical Characteristics. The ensured specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.

Quality Conformance Inspection

Table 1. Mil-Std-883, Method 5005 - Group A

Subgroup	Description	Temp (°C)
1	Static tests at	+25
2	Static tests at	+125
3	Static tests at	-55
4	Dynamic tests at	+25
5	Dynamic tests at	+125
6	Dynamic tests at	-55
7	Functional tests at	+25
8A	Functional tests at	+125
8B	Functional tests at	-55
9	Switching tests at	+25
10	Switching tests at	+125
11	Switching tests at	-55
12	Settling time at	+25
13	Settling time at	+125
14	Settling time at	-55

LM195H/883 Electrical Characteristics DC Parameter Collector to Emitter

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
V_{CE}	Operating Voltage	I _C ≤ I _{Max}	See ⁽¹⁾		42	V	1, 2, 3

Product Folder Links: LM195QML LM195QML-SP

(1) Parameter tested go-no-go only.

⁽²⁾ The maximum power dissipation must be derated at elevated temperatures and is dictated by T_{Jmax} (maximum junction temperature), θ_{JA} (package junction to ambient thermal resistance), and T_A (ambient temperature). The maximum allowable power dissipation at any temperature is P_{Dmax} = (T_{Jmax} - T_A)/θ_{JA} or the number given in the Absolute Maximum Ratings, whichever is lower.



LM195H/883 Electrical Characteristics DC Parameter Base to Emitter

Symbol	nbol Parameter Cond		Notes	Min	Max	Unit	Sub- groups
BV _{BE}	Breakdown Voltage	V _{CE} ≤ 42V	See ⁽¹⁾	42		V	1, 2, 3
	0-11)/ 47\/		1.2		Α	1
I _{SC}	Collector Current	V _{CE} ≤ 7V		1		Α	2, 3
1.7	0-1	1. 40			2	V	1, 2
V_{Sat}	Saturation Voltage	I _C = 1A			2.5	V	3
I _B	Base Current	$0 \le V_{BE} \le 42V$, $I_C \le I_{Max}$					1, 2, 3
IQ	Quiescent Current	V _{CE} = 42V, V _{BE} = 0V			5	mA	1, 2, 3
		$V_C = 46-42V,$ $I_L = 50mA$		-0.03	0.01	V	1
V_{Bk}	Breakdown Delta V _{BE}	V _C = 46-38V		-0.03	0.01	V	1
		V _C = 50-42V		-0.03	0.01	V	1
Thr	Thermal Response	100µS		-10	100	mV	1
		500µS		-10	70	mV	1
		2mS		-10	50	mV	1
		20mS		-10	10	mV	1

⁽¹⁾ Parameter tested go-no-go only.

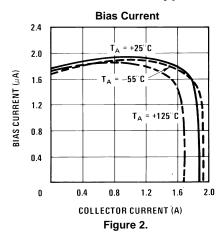
LM195H/883 Electrical Characteristics AC Parameter

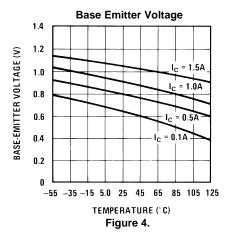
Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
t _{ON}	Response Time	V_I = 0-2V, R_L = 36 Ω , V+ = 36V			1.8	μS	9, 10, 11
t _{OFF}	Response Time	V_I = 2-0V, R_L = 36 Ω , V+ = 36V			1.8	μS	9, 10, 11

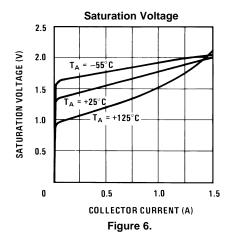
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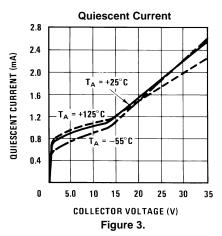


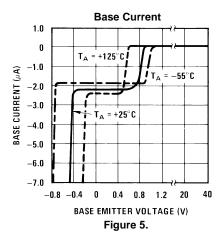
Typical Performance Characteristics

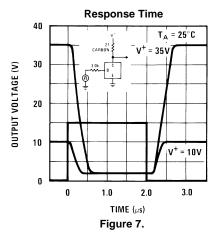














Typical Performance Characteristics (continued)

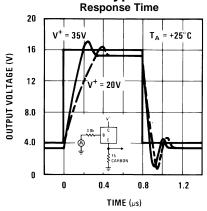
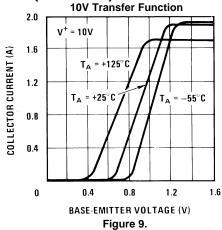
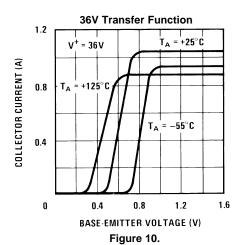


Figure 8.





Transconductance

10

T_A = +25°C

T_A = +25°C

1.0

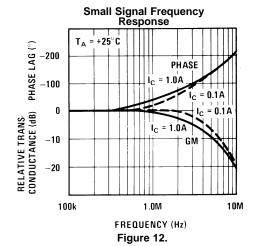
1.0

0.1

0.1

COLLECTOR CURRENT (A)

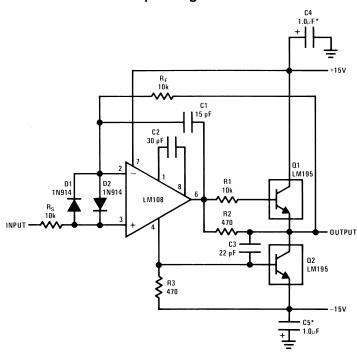
Figure 11.





Typical Applications

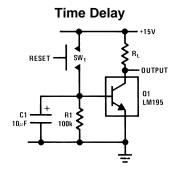
1.0 Amp Voltage Follower



*Solid Tantalum

Power PNP R1 5.0k* 500 pF** A1 2N2905 Q2 LM195 COLLECTOR

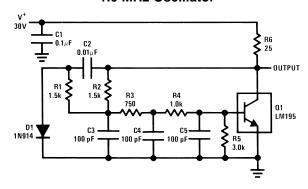
*Protects against excessive base drive



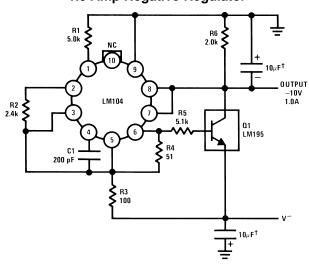
^{**}Needed for stability



1.0 MHz Oscillator

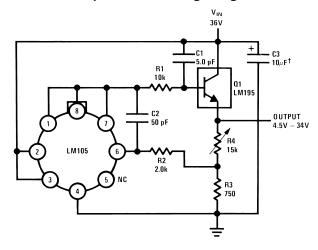


1.0 Amp Negative Regulator



†Solid Tantalum

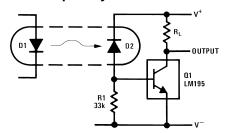
1.0 Amp Positive Voltage Regulator



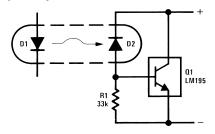
†Solid Tantalum



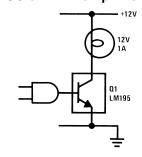
Fast Optically Isolated Switch



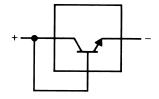
Optically Isolated Power Transistor



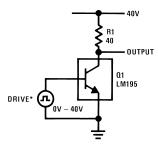
CMOS or TTL Lamp Interface



Two Terminal Current Limiter



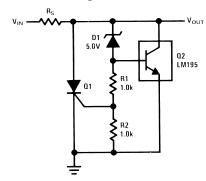
40V Switch



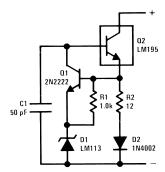
*Drive Voltage 0V to ≥ 10V ≤ 42V



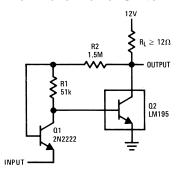
6.0V Shunt Regulator with Crowbar



Two Terminal 100 mA Current Regulator



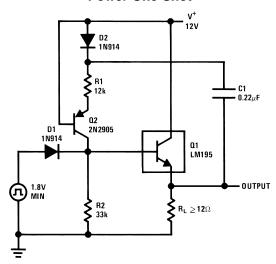
Low Level Power Switch



Turn ON = 350 mV Turn OFF = 200 mV

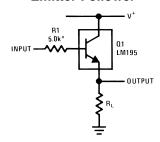


Power One-Shot



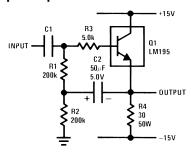
T = R1C R2 = 3R1 $R2 \le 82k$

Emitter Follower

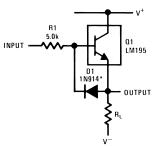


*Need for Stability

High Input Impedance AC Emitter Follower

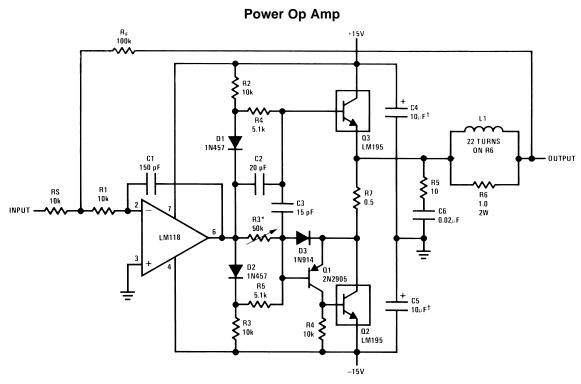


Fast Follower



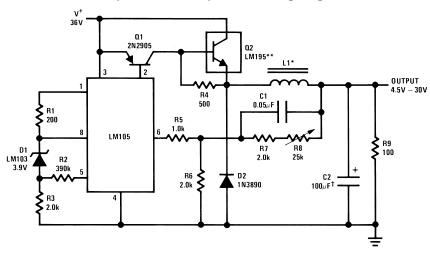
*Prevents storage with fast fall time square wave drive





^{*}Adjust for 50 mA quiescent current †Solid Tantalum

6.0 Amp Variable Output Switching Regulator



^{*}Sixty turns wound on Arnold Type A-083081-2 core.

REVISION HISTORY SECTION

Released	Revision	Section	Changes
11/30/2010	А	New Release, Corporate format	1 MDS data sheets converted into one Corp. data sheet format. MNLM195-H Rev 0BL will be archived.
03/20/2013	Α	All	Changed layout of National Data Sheet to TI format

Submit Documentation Feedback

^{**}Four devices in parallel

[†]Solid tantalum





27-Mar-2013

PACKAGING INFORMATION

Orderable Device	Status	Package Type	Package	Pins	Package	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Top-Side Markings	Samples
	(1)		Drawing		Qty	(2)		(3)		(4)	
5962-8777801XA	ACTIVE	ТО	NDT	3	20	TBD	Call TI	Call TI	-55 to 125	LM195H/883 5962-8777801XA Q A CO 5962-8777801XA Q > T	Samples
LM195H/883	ACTIVE	ТО	NDT	3	20	TBD	Call TI	Call TI	-55 to 125	LM195H/883 5962-8777801XA Q A CO 5962-8777801XA Q > T	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes. **Pb-Free** (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between

the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

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⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

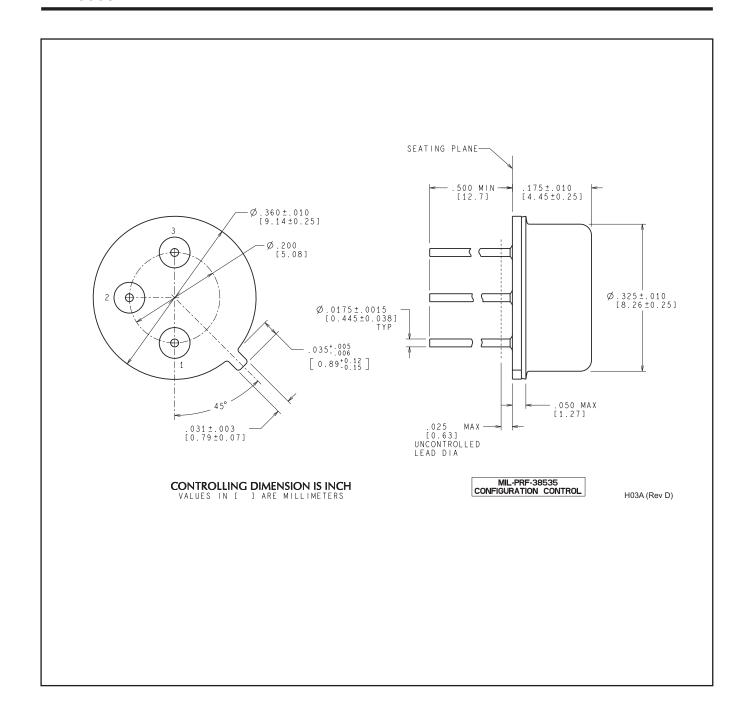
⁽⁴⁾ Multiple Top-Side Markings will be inside parentheses. Only one Top-Side Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Top-Side Marking for that device.



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27-Mar-2013

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