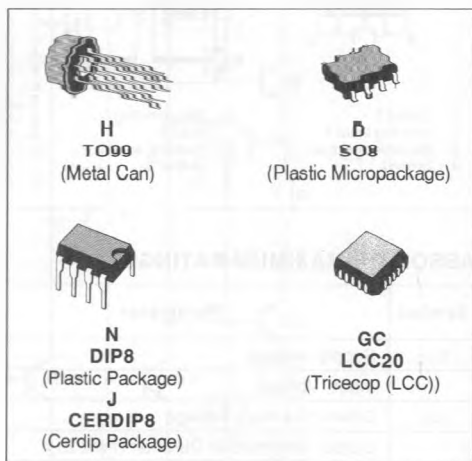




LOW POWER DUAL OPERATIONAL AMPLIFIERS

- INTERNALLY FREQUENCY COMPENSATED.
- LARGE DC VOLTAGE GAIN : 100 dB
- WIDE BANDWIDTH (unity gain) : 1.1 MHz (temperature compensated)
- VERY LOW SUPPLY CURRENT/AMPLI (500 μ A) - ESSENTIALLY INDEPENDENT OF SUPPLY VOLTAGE
- LOW INPUT BIAS CURRENT : 20 nA (temperature compensated)
- LOW INPUT OFFSET VOLTAGE : 2 mV
- LOW INPUT OFFSET CURRENT : 2 nA
- INPUT COMMON-MODE VOLTAGE RANGE INCLUDES GROUND
- DIFFERENTIAL INPUT VOLTAGE RANGE EQUAL TO THE POWER SUPPLY VOLTAGE
- LARGE OUTPUT VOLTAGE SWING 0 V TO ($V_{cc} - 1.5$ V)



DESCRIPTION

These circuits consist of two independent, high gain, internally frequency compensated which were designed specifically to operate from a single power supply over a wide range of voltages. The low power supply drain is independent of the magnitude of the power supply voltage.

Application areas include transducer amplifiers, dc gain blocks and all the conventional op-amp circuits which now can be more easily implemented in single power supply systems. For example, these circuits can be directly operated off the standard + 5 V power supply voltage which is used in logic systems and will easily provide the required interface electronics without requiring any additional power supply.

In the linear mode the input common-mode voltage range includes ground and the output voltage can also swing to ground, even through operated from only a single power supply voltage.

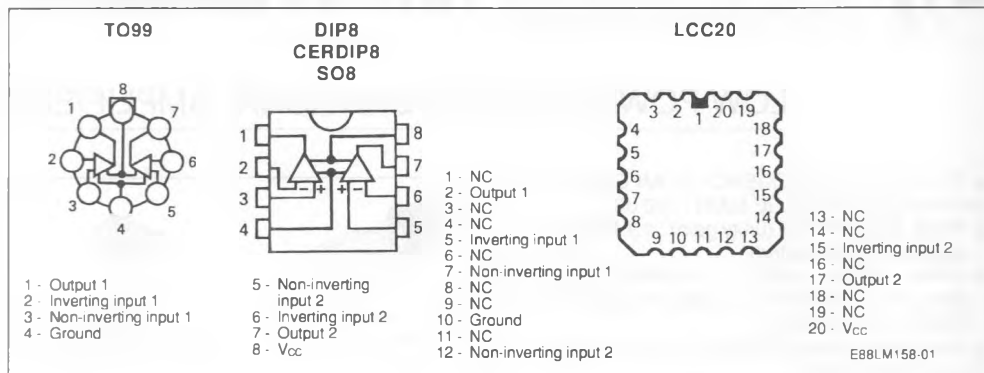
The gain-bandwidth product is temperature compensated.

The input bias current is temperature compensated.

ORDER CODES

Part Number	Temperature Range	Package				
		H	N	J	GC	D
LM158, A	- 55 °C to + 125 °C	•	•	•	•	•
LM258, A	- 40 °C to + 105 °C	•	•	•		•
LM358, A	0 °C to + 70 °C	•	•	•		•
NE532	0 °C to + 70 °C		•	•		•
LM2904	- 40 °C to + 105 °C		•	•		•
Note : Hi-Rel Versions Available						
Examples : LM158H, LM258N, LM2904D.						

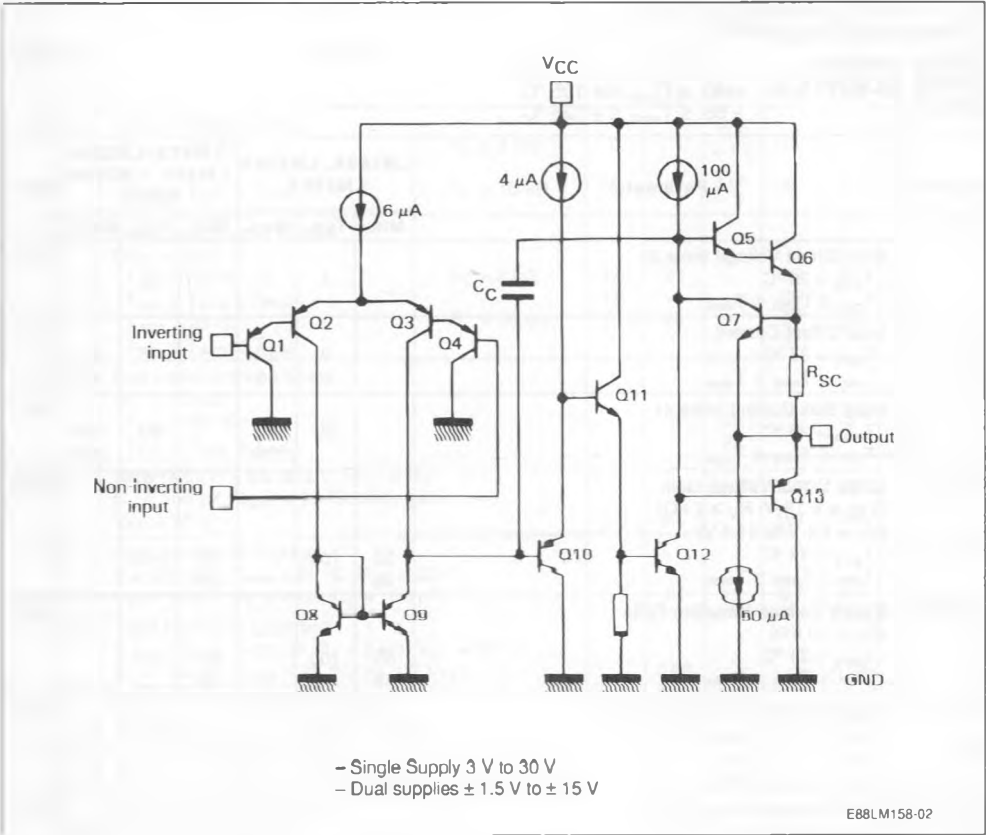
PIN CONNECTIONS (top views)



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	LM158, A	LM258, A LM2904	LM358, A NE532	Unit
V _{CC}	Supply Voltage	+ 32	+ 32	+ 32	V
V _I	Input Voltage	- 0.3 to + 32	- 0.3 to + 32	- 0.3 to + 32	V
V _{ID}	Differential Input Voltage	+ 32	+ 32	+ 32	V
	Output Short-circuit Duration (note 2)	Indefinite	Indefinite	Indefinite	
P _{tot}	Power Dissipation	500 665	500	500	mW
I _{ID}	Input Current (note 1)	50	50	50	mA
T _{oper}	Operating Free-air Temperature Range	- 55 to + 125	- 40 to + 105	0 to + 70	°C
T _{stg}	Storage Temperature Range	- 65 to + 150	- 65 to + 150	- 65 to + 150	°C

SCHEMATIC DIAGRAM (1/2 LM158)



CASE	Inverting Inputs	Non-inverting Inputs	GND	V _{CC}	Outputs	N.C.
TO99 - DIP8 - Cerdip8 - SO8	2-6	3-5	4	8	1-7	
LCC20	5-15	7-12	10	20	2-17	*

* LCC20 : Other pins are not connected.

ELECTRICAL CHARACTERISTICS

V_{CC} = + 5 V, V_{CC} = Ground, V_O = 1.4 V
(unless otherwise specified)

LM358,A/NE532 : 0 ≤ T_{amb} ≤ + 70 °C
LM258,A/LM2904 : - 40 ≤ T_{amb} ≤ + 105 °C
LM158,A : - 55 ≤ T_{amb} ≤ + 125 °C

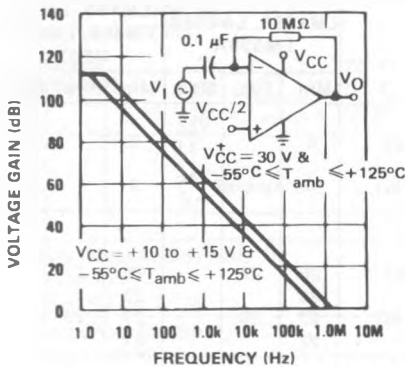
Symbol	Parameter	LM158A, LM258A LM358A			LM158, LM258 LM358, LM2904 NE532			Unit
		Min.	Typ.	Max.	Min.	Typ.	Max.	
V _{IO}	Input Offset Voltage (note 3) T _{amb} = 25 °C T _{min} ≤ T _{amb} ≤ T _{max}		1	2 4		2	5 7	mV
I _{IO}	Input Offset Current T _{amb} = 25 °C T _{min} ≤ T _{amb} ≤ T _{max}		2	10 30		2	20 40	nA
I _{IB}	Input Bias Current (note 4) T _{amb} = 25 °C T _{min} ≤ T _{amb} ≤ T _{max}		20	50 100		20	100 200	nA
A _{VD}	Large Signal Voltage Gain (V _{CC} = + 15 V, R _L ≥ 2 kΩ) (V _O = 1.4 V to 11.4 V) T _{amb} = 25 °C T _{min} ≤ T _{amb} ≤ T _{max}	50 25	100		50 25	100		V/mV
SVR	Supply Voltage Rejection Ratio (R _S ≤ 10 kΩ) T _{amb} = 25 °C T _{min} ≤ T _{amb} ≤ T _{max}	65 65	100		65 65	100		dB
I _{CC}	Supply Current, all Amp, no Load T _{amb} = 25 °C, V _{CC} = + 5 V T _{amb} = 25 °C, V _{CC} = + 30 V T _{min} ≤ T _{amb} ≤ T _{max}		0.7 1	1.2 2 2		0.7 1	1.2 2 2	mA
V _I	Input Voltage Range (note 6) T _{amb} = 25 °C T _{min} ≤ T _{amb} ≤ T _{max}	0 0		V _{CC} - 1.5 V _{CC} - 2	0 0		V _{CC} - 1.5 V _{CC} - 2	V
CMR	Common-mode Rejection Ratio (R _S < 10 kΩ) (note 3) T _{amb} = 25 °C T _{min} ≤ T _{amb} ≤ T _{max}	70 60	85		70 60	85		dB
I _O	Output Short-circuit Current (V _I * = + 1 V, V _I = 0 V) T _{amb} = 25 °C, V _{CC} = + 15 V (note 2) T _{min} ≤ T _{amb} ≤ T _{max}	20 10	40	60	20 10	40	60	mA
I _{sink}	Output Current Sink (V _I * = - 1 V, V _I = 0 V) V _{CC} = + 15 V V _O = + 0.2 V							
	T _{amb} = + 25 °C	10	20		10	20		mA
	T _{min} ≤ T _{amb} ≤ T _{max}	10			10			
	T _{amb} = + 25 °C	12	50		12	50		μA
	T _{min} ≤ T _{amb} ≤ T _{max}	12			12			

ELECTRICAL CHARACTERISTICS (continued)

Symbol	Parameter	LM158A, LM258A LM358A			LM158, LM258 LM358, LM2904 NE532			Unit
		Min.	Typ.	Max.	Min.	Typ.	Max.	
V_{OPP}	Output Voltage Swing $T_{amb} = 25^{\circ}\text{C}$ $R_L \geq 2\text{ k}\Omega$	0		$V_{CC} - 1.5$	0		$V_{CC} - 1.5$	V
	$T_{min} \leq T_{amb} \leq T_{max}$ $R_L \geq 10\text{ k}\Omega$	0		$V_{CC} - 2$	0		$V_{CC} - 2$	
V_{OH}	High Level Output Voltage ($V_{CC} = 30\text{ V}$)							V
	$T_{amb} = 25^{\circ}\text{C}$ $R_L = 2\text{ k}\Omega$	26	27		26	27		
	$T_{min} \leq T_{amb} \leq T_{max}$ $T_{amb} = 25^{\circ}\text{C}$ $R_L = 10\text{ k}\Omega$	26	28		26	28		
	$T_{min} \leq T_{amb} \leq T_{max}$	27			27			
V_{OL}	Low Level Output Voltage ($R_L \geq 10\text{ k}\Omega$)							mV
	$T_{amb} = 25^{\circ}\text{C}$		5	20		5	20	
	$T_{min} \leq T_{amb} \leq T_{max}$			20			20	
S_{VO}	Slew-rate ($V_I = 0.5$ to 3 V , $R_L = 2\text{ k}\Omega$ $C_L \leq 100\text{ pF}$, $T_{amb} = 25^{\circ}\text{C}$, unity gain)							V/ μs
	$V_{CC} = 15\text{ V}$	0.3	0.6		0.3	0.6		
GBP	Gain Bandwidth Product ($f = 100\text{ kHz}$, $T_{amb} = 25^{\circ}\text{C}$, $V_{CC} = 30\text{ V}$ $V_{IN} = 10\text{ mV}$, $R_L = 2\text{ k}\Omega$, $C_L = 100\text{ pF}$)							MHz
		0.7	1.1	1.6	0.7	1.1	1.6	
THD	Total Harmonic Distortion ($f = 1\text{ kHz}$, $A_v = 20\text{ dB}$, $R_L = 2\text{ k}\Omega$, $V_{CC} = 30\text{ V}$ $C_L \leq 100\text{ pF}$, $T_{amb} = 25^{\circ}\text{C}$, $V_O = 2 V_{PP}$)		0.02			0.02		%
V_n	Equivalent Input Noise voltage ($f = 1\text{ kHz}$, $R_g = 100\text{ }\Omega$, $V_{CC} = 30\text{ V}$)		55			55		nV/ $\sqrt{\text{Hz}}$
DV_{IO}	Input Offset Voltage Drift $T_{min} \leq T_{amb} \leq T_{max}$		7	30		7	30	$\mu\text{V}/^{\circ}\text{C}$
DI_{IO}	Input Offset Current Drift $T_{min} \leq T_{amb} \leq 25^{\circ}\text{C}$		10	300		10	300	pA/ $^{\circ}\text{C}$
V_{O1}/V_{O2}	Channel Separation (note 5) $1\text{ kHz} \leq f \leq 20\text{ kHz}$		120			120		dB

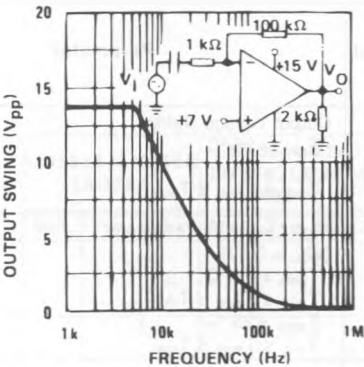
- Notes :**
- This input only exist when the voltage at any of the input leads is driven negative. It is due to the collector-base junction of the input PNP transistor becoming forward biased and thereby acting as input diode clamps. In addition to this diode action, there is also NPN parasitic action on the IC chip. This transistor action can cause the output voltages of the Op-amps to go to the V_{CC} voltage level (or to ground for a large overdrive) for the time duration that an input is driven negative. This is not destructive and normal output will set up again for input voltage higher than -0.3 V .
 - Short-circuits from the output to V_{CC} can cause excessive heating if $V_{CC} > 15\text{ V}$. The maximum output current is approximately 40 mA independent of the magnitude of V_{CC} . Destructive dissipation can result from simultaneous short-circuits on all amplifiers.
 - $V_O = 1.4\text{ V}$, $R_S = 0$, $5\text{ V} < V_{CC} < 30\text{ V}$, $0 < V_I < V_{CC} - 1.5\text{ V}$.
 - The direction of the input current is out of the IC. This current is essentially constant, independent of the state of the output so no loading change exists on the input lines.
 - Due to the proximity of external components insure that coupling is not originating via stray capacitance between these external parts. This typically can be detected as this type of capacitance increases at higher frequencies.
 - The input common-mode voltage of either input signal voltage should not be allowed to go negative by more than 0.3 V . The upper end of the common-mode voltage range is $V_{CC} - 1.5\text{ V}$. But either or both inputs can go to $+32\text{ V}$ without damage.

OPEN LOOP FREQUENCY RESPONSE (Note 3)



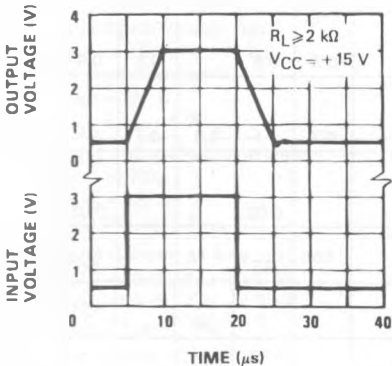
E88LM158-03

LARGE SIGNAL FREQUENCY RESPONSE



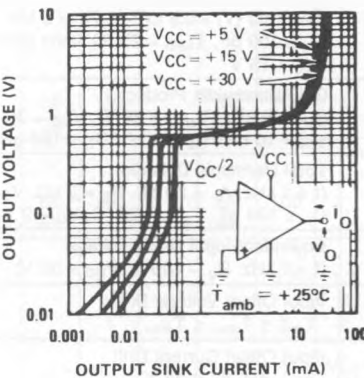
E88LM158-04

VOLTAGE FOLLOWER PULSE RESPONSE



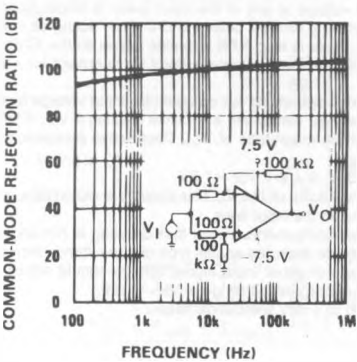
E88LM158-05

OUTPUT CHARACTERISTICS

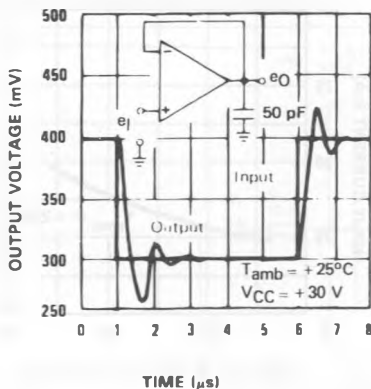


E88LM158-06

COMMON-MODE REJECTION RATIO

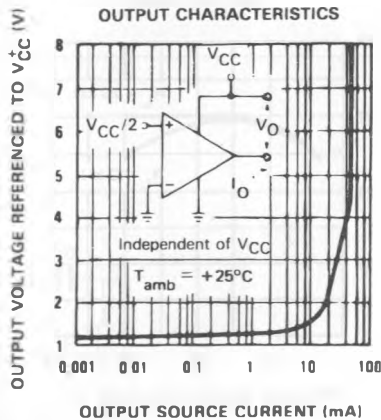


E88LM158-07

VOLTAGE FOLLOWER PULSE RESPONSE
(SMALL SIGNAL)

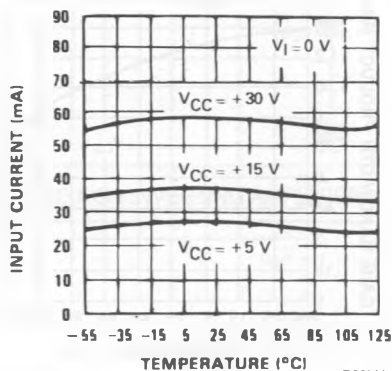
E88LM158-08

OUTPUT CHARACTERISTICS



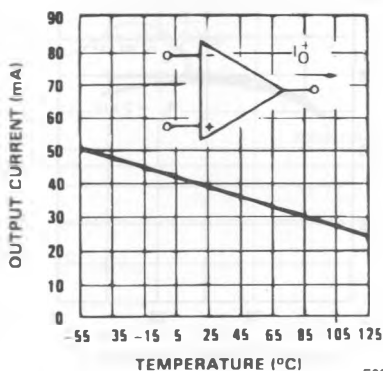
E88LM158-09

INPUT CURRENT (Note 1)



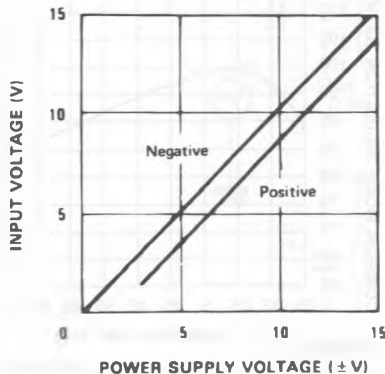
E88LM158-10

CURRENT LIMITING (Note 1)



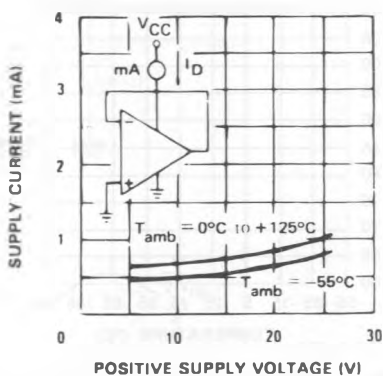
E88LM158-11

INPUT VOLTAGE RANGE



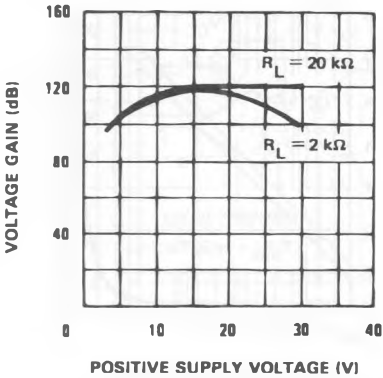
E88LM158-12

SUPPLY CURRENT



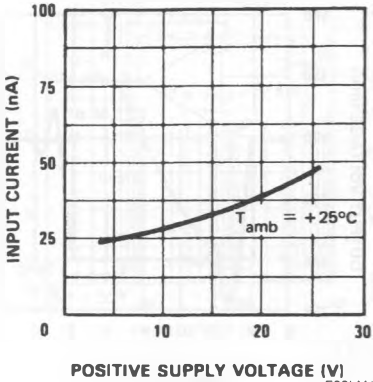
E88LM158-13

VOLTAGE GAIN



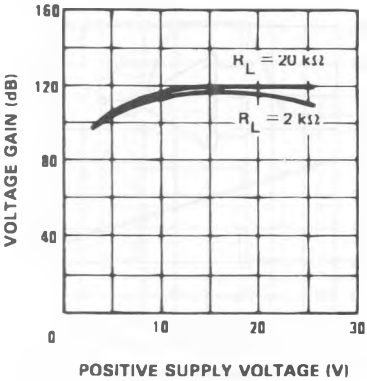
E88LM158-14

INPUT CURRENT



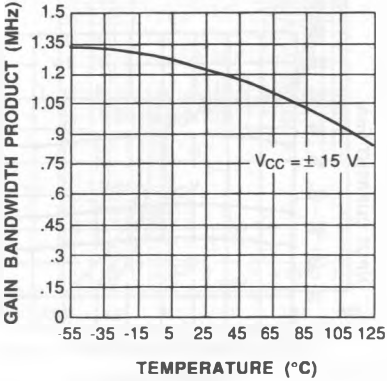
E88LM158-15

VOLTAGE GAIN



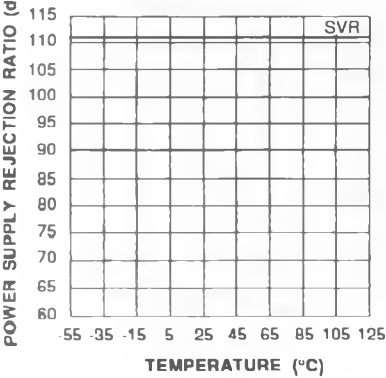
E88LM158-16

GAIN BANDWIDTH PRODUCT



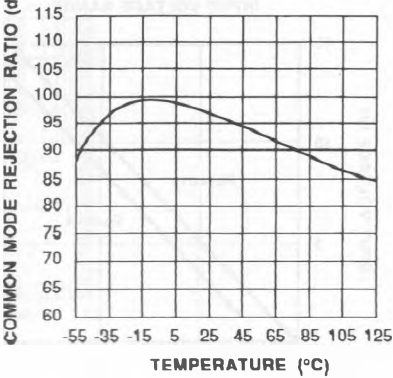
E88LM158-17

POWER SUPPLY REJECTION RATIO



E88LM158-18

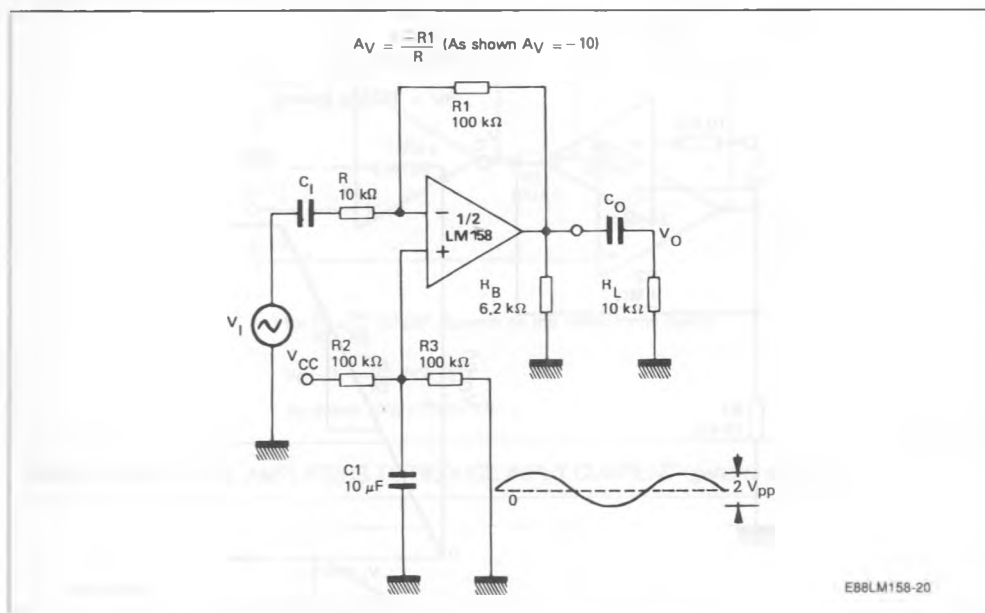
COMMON MODE REJECTION RATIO



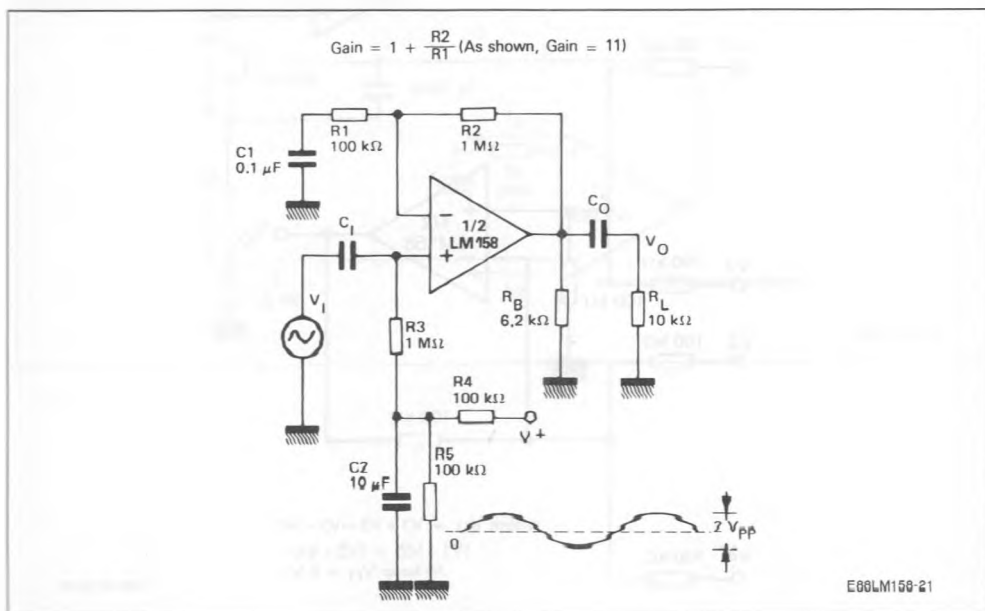
E88LM158-19

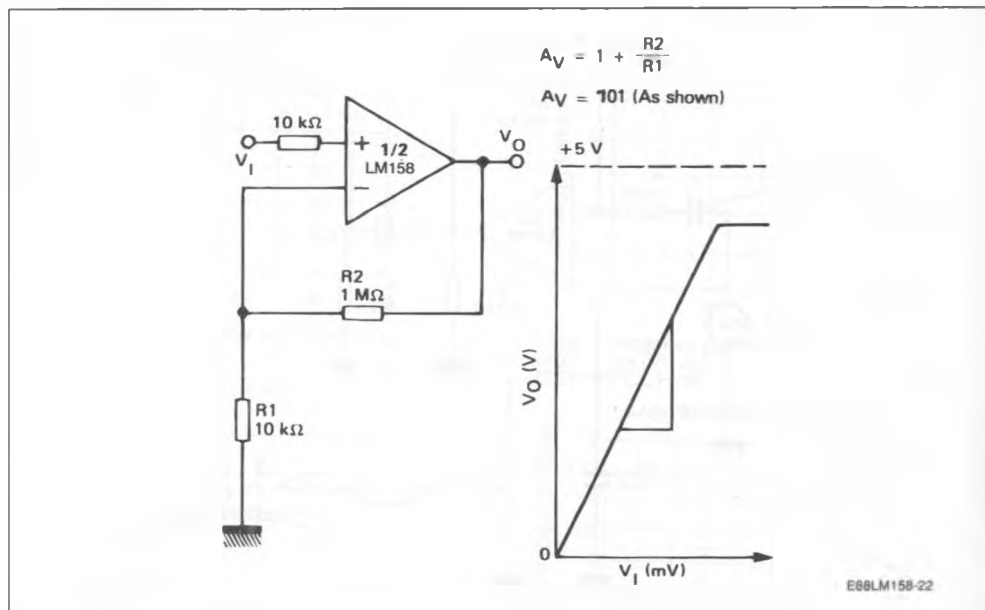
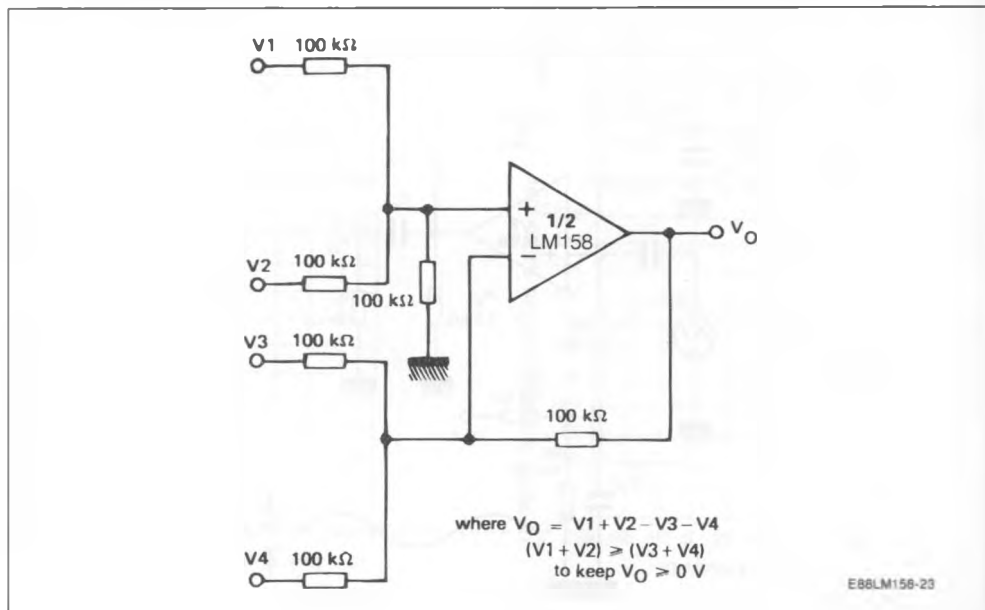
TYPICAL APPLICATIONS (single supply voltage) $V_{CC} = +5\text{ V}_{DC}$

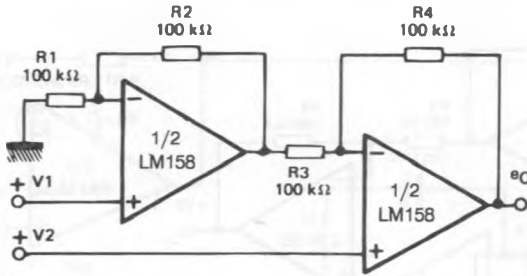
AC COUPLED INVERTING AMPLIFIER



AC COUPLED NON INVERTING AMPLIFIER



TYPICAL APPLICATIONS (single supply voltage) $V_{CC} = +5 V_{DC}$ (continued)**NON INVERTING DC AMPLIFIER****DC SUMMING AMPLIFIER**

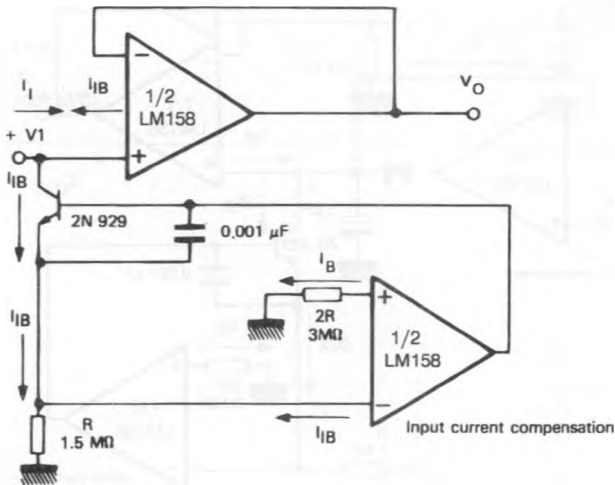
TYPICAL APPLICATIONS (single supply voltage) $V_{CC} = +5 V_{DC}$ (continued)**HIGH INPUT IMPEDANCE, DC DIFFERENTIAL AMPLIFIER**

for $\frac{R1}{R2} = \frac{R4}{R3}$ (CMRR depends on this resistor ratio match)

$$V_O = (1 + \frac{R4}{R3}) (V_2 - V_1)$$

As shown : $V_O = 2(V_2 - V_1)$

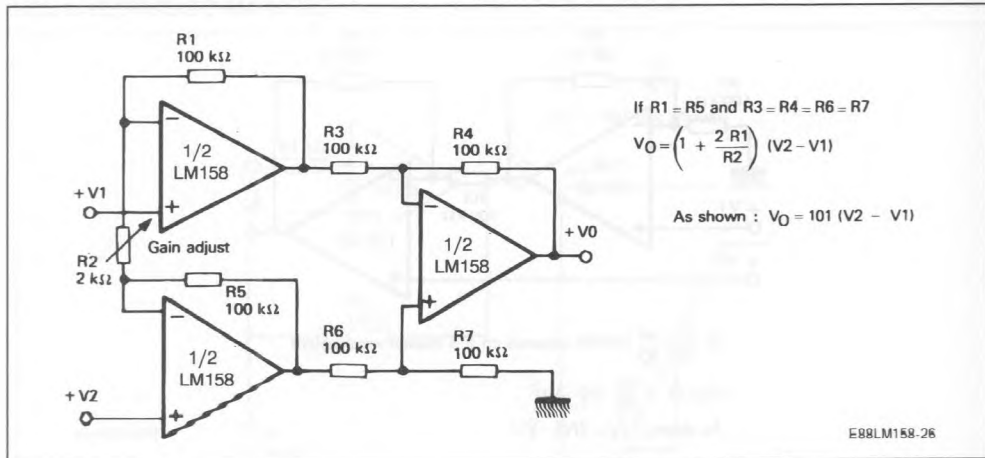
E88LM158-24

USING SYMMETRICAL AMPLIFIERS TO REDUCE INPUT CURRENT (general concept)

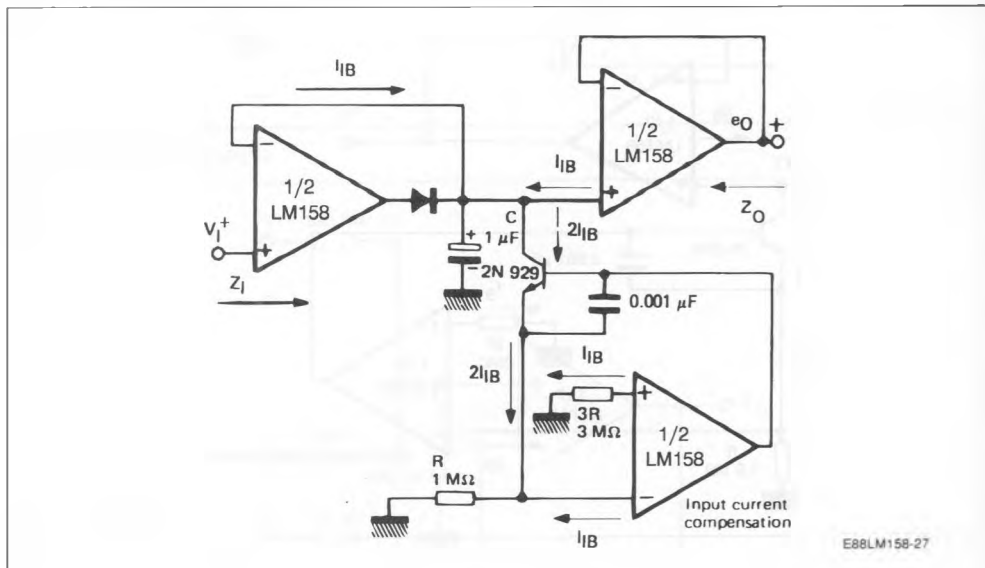
E88LM158-25

TYPICAL APPLICATIONS (single supply voltage) $V_{CC} = +5\text{ V}_{DC}$ (continued)

HIGH INPUT Z ADJUSTABLE-GAIN DC INSTRUMENTATION AMPLIFIER

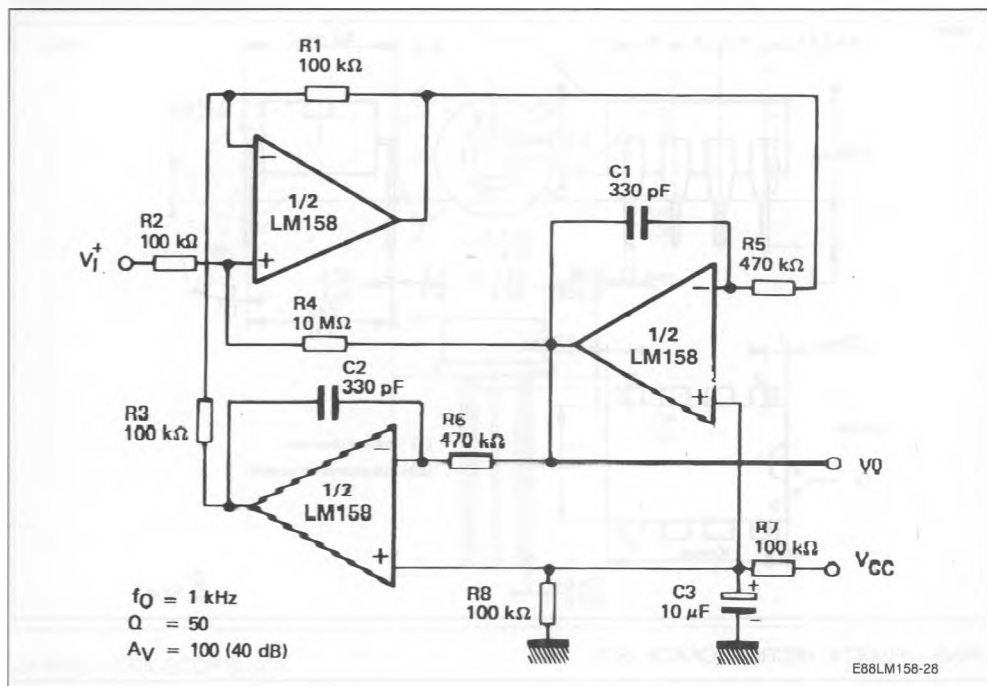


LOW DRIFT PEAK DETECTOR



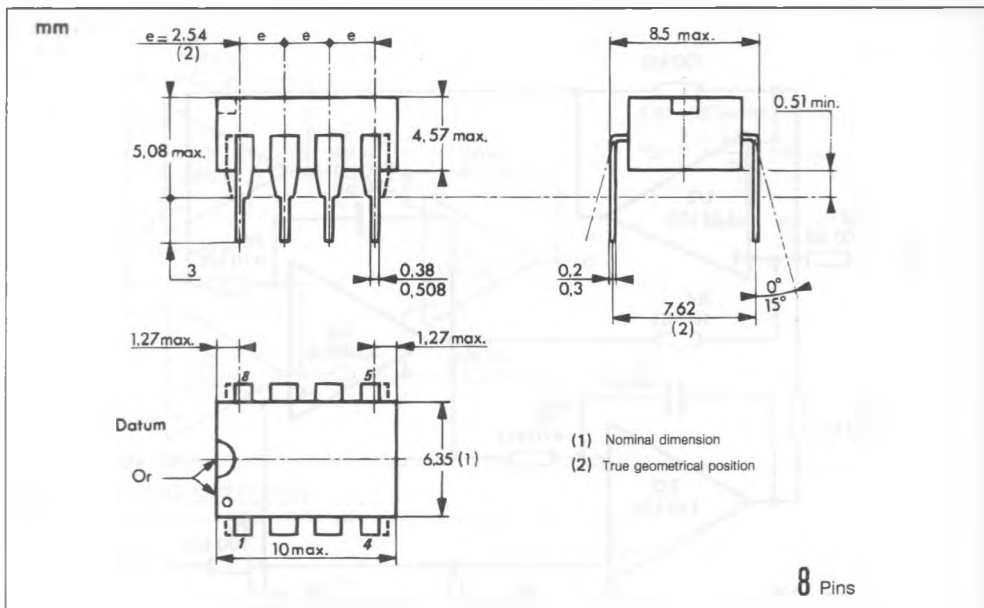
TYPICAL APPLICATIONS (single supply voltage) $V_{CC} = +5 V_{DC}$ (continued)

ACTIVE BAND-PASS FILTER

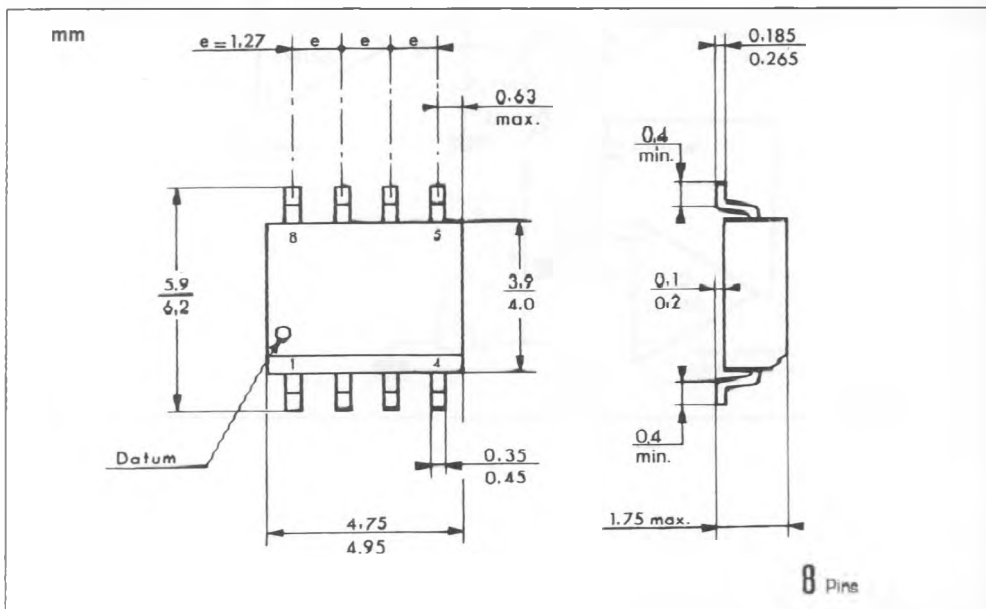


PACKAGE MECHANICAL DATA

8 PINS - PLASTIC DIP OR CerdIP

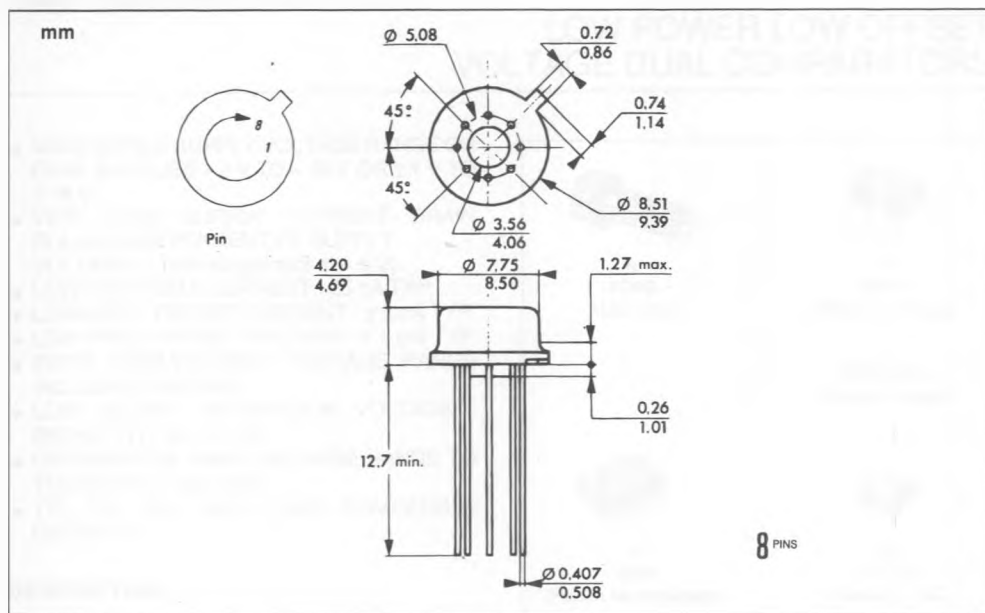


8 PINS - PLASTIC MICROPACKAGE (SO)



PACKAGE MECHANICAL DATA (continued)

8 PINS – METAL CAN - TO99



20 PINS – TRICECOP (LCC)

