

National Semiconductor

LM133/LM333 3-Ampere Adjustable Negative Regulators

General Description

The LM133/LM333 are adjustable 3-terminal negative voltage regulators capable of supplying in excess of -3.0A over an output voltage range of -1.2V to -32V. These regulators are exceptionally easy to apply, requiring only 2 external resistors to set the output voltage and 1 output capacitor for frequency compensation. The circuit design has been optimized for excellent regulation and low thermal transients. Further, the LM133 series features internal current limiting, thermal shutdown and safe-area compensation, making them substantially immune to failure from overloads.

The LM133/LM333 serve a wide variety of applications including local on-card regulation, programmable-output voltage regulation or precision current regulation. The LM133/ LM333 are ideal complements to the LM150/LM350 adjustable positive regulators.

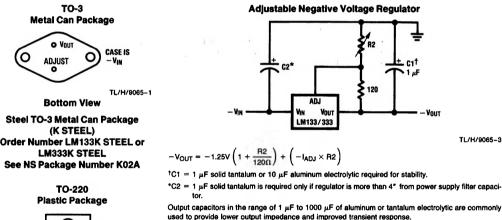
Features

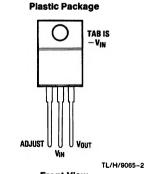
- Output voltage adjustable from -1.2V to -32V
- 3.0A output current guaranteed, -55°C to +150°C
- Line regulation typically 0.01%/V
- Load regulation typically 0.2%
- Excellent rejection of thermal transients
- 50 ppm/°C temperature coefficient
- Temperature-independent current limit
- Internal thermal overload protection
- P+ Product Enhancement tested
- Standard 3-lead transistor package
- Output is short circuit protected

Connection Diagrams

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Front View

3-Lead TO-220 Plastic Package (T) Order Number LM333T See NS Package Number T03B

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Power Dissipation	Internally Limited
Input-Output Voltage Differential	35V
Operating Junction Temperature Range	T _{MIN} to T _{MAX}
LM133	-55°C to +150°C
LM333	-40°C to +125°C

Storage Temperature	-65°C to +150°C
Lead Temperature (Soldering, 10 sec.)	
TO-3 Package	300°C
TO-220 Package	260°C
ESD Susceptibility	TBD

Electrical Characteristics LM133 Specifications with standard typeface are for $T_J = 25^{\circ}$ C, and those with **boldface type** apply over the full operating temperature range. (Note 3)

Parameter	Conditions	Typical	Min (Note 2)	Max (Note 2)	Units
Reference Voltage	ار = 10 mA	-1.250	-1.238	- 1.262	v
	$3V \le V_{IN} - V_{OUT} \le 35V$ 10 mA $\le I_L \le 3A, P \le P_{MAX}$	- 1.250	- 1.225	- 1.275	v
Line Regulation	3V ≤ V _{IN} − V _{OUT} ≤ 35V I _{OUT} = 50 mA (Note 4)	0.01 0.02		0.02 0.05	% /V
Load Regulation	10 mA \leq I _{OUT} \leq 3A, P \leq P _{MAX} (Notes 4, 5)	0.2 0.4		0.5 1.0	%
Thermal Regulation	10 ms Pulse	0.002		0.01	% /W
Temperature Stability	$T_{MIN} \le T_{J} \le T_{MAX}$	0.4			%
Long Term Stability	T _J = 125°C, 1000 Hours	0.15			%
Adjust Pin Current		65 70		90 100	μA
Adjust Pin Current Change	10 mA \le I _L \le 3A 3.0V \le V _{IN} - V _{OUT} \le 35V	2		6	μΑ
Minimum Load Current	$ V_{IN} - V_{OUT} \le 35V$	2.5		5.0	- mA
	$ V_{IN} - V_{OUT} \le 10V$	1.2		2.5	
Current Limit	$3V \le V_{IN} - V_{OUT} \le 10V$	3.9	3.0		
(Note 5)	$ V_{IN} - V_{OUT} = 20V$	2.4	1.25	8	A
	$ V_{IN} - V_{OUT} = 30V$	0.4	0.3		
Output Noise (% of V _{OUT})	10 Hz to 10 kHz	0.003			% (rms)
Ripple Rejection	$V_{OUT} = 10V, f = 120 \text{ Hz}$ $C_{ADJ} = 0 \mu F$ $C_{ADJ} = 10 \mu F$	60 77			dB
Thermal Resistance Junction-to-Case	TO-3 Package (K STEEL)	1.2	1	1.8	°C/W
Thermal Shutdown Temperature		163	150	190	•C

LM133/LM333

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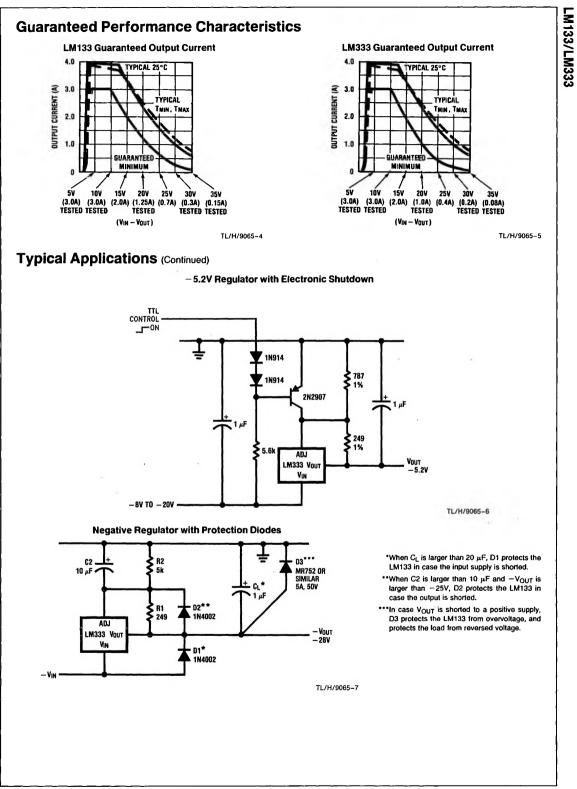
Parameter	Conditions	Typical	Min (Note 2)	Max (Note 2)	Units
Reference Voltage	IL = 10 mA	- 1.250	-1.225	- 1.275	v
	$3V \le V_{IN} - V_{OUT} \le 35V$ 10 mA $\le I_L \le 3A, P \le P_{MAX}$	- 1.250	- 1.213	- 1.287	
Line Regulation	$3V \le V_{IN} - V_{OUT} \le 35V$ $I_{OUT} = 50 \text{ mA (Note 4)}$	0.01 0.02	4 M T	0.04 0.07	% /V
Load Regulation	10 mA \leq I _L \leq 3A, P \leq P _{MAX} (Notes 4 and 5)	0.2 0.4	·	1.0 1.5	%
Thermal Regulation	10 ms Pulse	0.002	1. s.	0.02	% /W
Temperature Stability	$T_{MIN} \le T_J \le T_{MAX}$	0.5	-		%
Long Term Stability	T _J = 125°C, 1000 Hours	0.2			%
Adjust Pin Current		65 70		95 100	μA
Adjust Pin Current Change	10 mA \leq I _L \leq 3A 3.0V \leq V _{IN} $-$ V _{OUT} \leq 35V	2.5	12 3 4 1 4	8	μA
Minimum Load	V _{IN} – V _{OUT} ≤ 35V	2.5		10	mA
Current	$ V_{IN} - V_{OUT} \le 10V$	1.5		5.0	- mA
Current Limit	$3V \le V_{IN} - V_{OUT} \le 10V$	3.9	3.0		रु ा असे
(Note 5)	$ V_{IN} - V_{OUT} = 20V$	2.4	1.0	•	Α
	$ V_{\rm IN} - V_{\rm OUT} = 30V$	0.4	0.20		3 - A
Output Noise (% of V _{OUT})	10 Hz to 10 kHz	0.003	4 A 2		% (rms)
Ripple Rejection	V _{OUT} = 10V, f = 120 Hz C _{ADJ} = ⁺ 0 μF C _{ADJ} = 10 μF.	60 77			dB
Thermal Resistance Junction to Case	TO-3 Package (K STEEL)	1.2	- 1	1.8	•C/W
	TO-220 Package (T)	3	12	4	
Thermal Shutdown Temperature	an a	163			°C
Thermal Resistance Junction to Ambient (No Heatsink)	K Package	35			
	T Package	50	2		•C/W

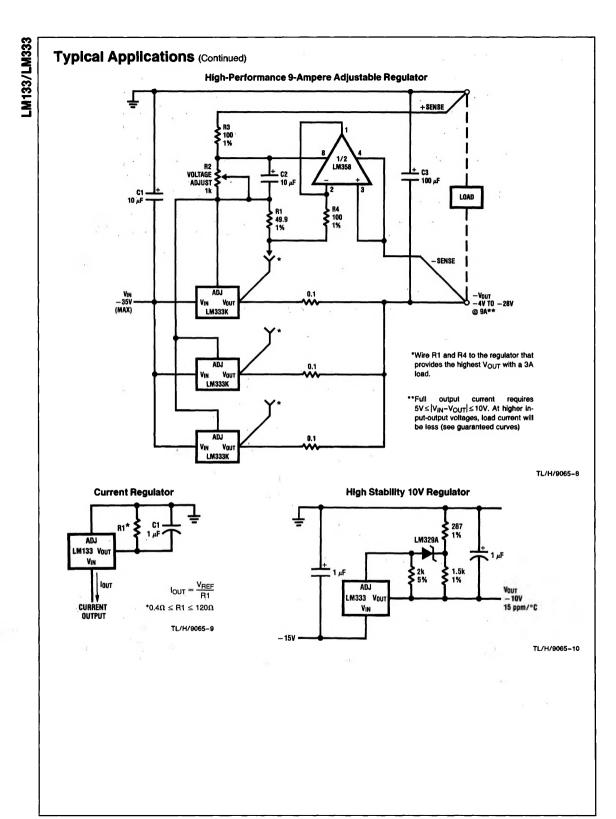
Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Electrical specifications do not apply when operating the device outside of its stated operating conditions.

Note 2: All limits are guaranteed at either room temperature (standard type face) or at temperature extremes (bold typeface) by production testing or correlation techniques using standard Statistical Quality Control (SQC) methods.

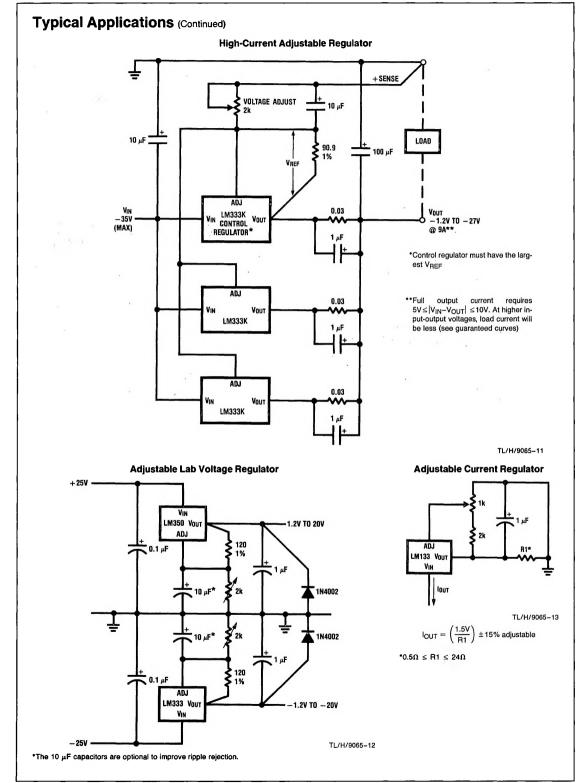
Note 3: Unless otherwise specified: $|V_{IN} - V_{OUT}| = 5V$, $I_{OUT} = 0.5A$, $P_{DISS} \le 30W$.

Note 4: Load and line regulation are measured at constant junction temperature, using low duty cycle pulse testing (output voltage changes due to heating effects are covered by the Thermal Regulation specification). For the TO-3 package, load regulation is measured on the output pin, $\frac{1}{N}$ below the base of the package. Note 5: The output current of the LM333 is guaranteed to be \geq 3A in the range $3V \leq |V_{IN} - V_{OUT}| \leq 10V$. For the range $10V \leq |V_{IN} - V_{OUT}| \leq 15V$, the guaranteed minimum output current is equal to: $30/(V_{IN} - V_{OUT})$. Refer to graphs for guaranteed output currents at other voltages.





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Typical Applications (Continued)

THERMAL REGULATION

When power is dissipated in an IC, a temperature gradient occurs across the IC chip affecting the individual IC circuit components. With an IC regulator, this gradient can be especially severe since the power dissipation is large. Thermal regulation is the effect of these temperature gradients on output voltage (in percentage output change) per watt of power change in a specified time. Thermal regulation error is independent of electrical regulation or temperature coefficient, and occurs within 5 ms to 50 ms after a change in power dissipation. Thermal regulation depends on IC layout as well as electrical design. The thermal regulation of a voltage equilator is defined as the percentage of voltage.

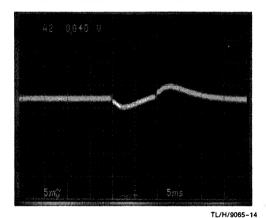


FIGURE 1

per watt, within the first 10 ms after a step of power is applied. The LM133's specification is 0.01%/W, max.

In Figure 1, a typical LM133's output drifts only 2 mV (or 0.02% of $V_{OUT} = -10V$) when a 20W pulse is applied for 10 ms. This performance is thus well inside the specification limit of 0.01%/W×20W = 0.2% max. When the 20W pulse is ended, the thermal regulation again shows a 2 mV step as the LM133 chip cools off. Note that the load regulation error of about 1 mV (0.01%) is additional to the thermal regulation error. In *Figure 2*, when the 20W pulse is applied for 100 ms, the output drifts only slightly beyond the drift in the first 10 ms, and the thermal error stays well within 0.1% (10 mV).

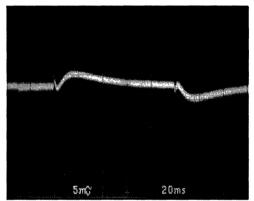


FIGURE 2

