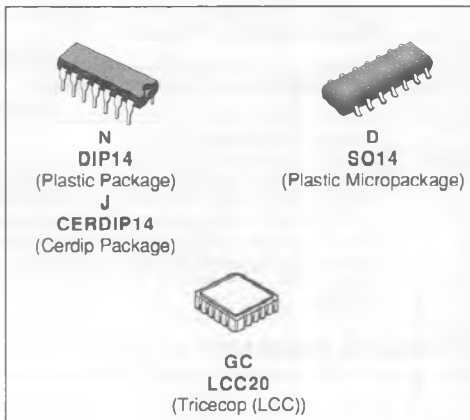


LOW POWER LOW OFFSET VOLTAGE QUAD COMPARATORS

- WIDE SINGLE SUPPLY VOLTAGE RANGE OR DUAL SUPPLIES FOR ALL DEVICES : +2 V TO +36 V OR ± 1 V TO ± 18 V
- VERY LOW SUPPLY CURRENT DRAIN (0.8 mA) INDEPENDENT OF SUPPLY VOLTAGE (1 mW/comparator at +5 V)
- LOW INPUT BIAS CURRENT : 25 nA TYP
- LOW INPUT OFFSET CURRENT : ± 5 nA TYP
- LOW INPUT OFFSET VOLTAGE : ± 1 mV TYP
- INPUT COMMON-MODE VOLTAGE RANGE INCLUDES GROUND
- LOW OUTPUT SATURATION VOLTAGE : 250 mV TYP. ($I_o = 4$ mA)
- DIFFERENTIAL INPUT VOLTAGE RANGE TO THE SUPPLY VOLTAGE
- TTL COMPATIBLE OUTPUTS



DESCRIPTION

These devices consist of four independent precision voltage comparators with an offset voltage specifications as low as 2 mV max for LM339A, LM239A and LM139A. All these comparators were designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible.

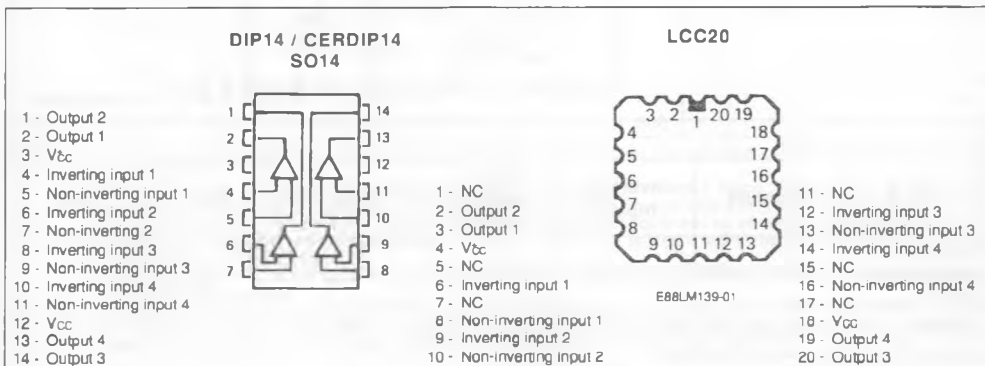
These comparators also have a unique characteristic in that the input common-mode voltage range includes ground even through operated from a single power supply voltage.

ORDER CODES

Part Number	Temperature Range	Package			
		N	J	GC	D
LM139,A	- 55 to + 125 °C	•	•		•
LM239,A	- 40 to + 105 °C	•	•		•
LM339,A	0 to + 70 °C	•	•	•	•
LM2901	- 40 to + 105 °C	•	•	•	•
MC3302	- 40 to + 105 °C	•	•		•

Note : Hi-Rel Versions Available
Examples : LM139GC, LM139AN, LM2901D

PIN CONNECTIONS (top views)

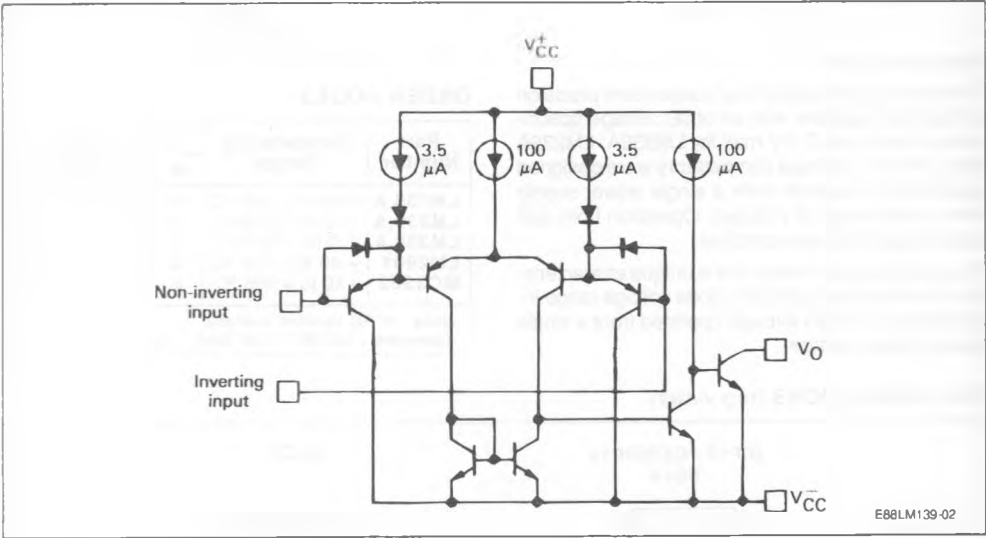


ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	LM139, A LM239, A	LM339, A	LM2901 MC3302	Unit
V _{CC}	Supply Voltage	± 18 to 36	± 18 to 36	± 18 to 36	V
V _{ID}	Differential Input Voltage	36	36	36	V
V _I	Input Voltage	− 0.3 to + 36	− 0.3 to + 36	− 0.3 to + 36	V
	Output Short-circuit to Ground – (note 2)	Continuous	Continuous	Continuous	
P _{tot}	Power Dissipation – (note 1)	570 665	570	570	mW
T _{oper}	Operating Free-air Temperature Range	− 55 to + 125 LM239, A − 40 to + 105	0 to + 70	− 40 to + 105	°C
T _{sig}	Storage Temperature Range	− 65 to + 150	− 65 to + 150	− 65 to + 150	°C

Notes : 1. Short-circuit from the output to V_{CC}* can cause excessive heating and eventual destruction. The maximum output current is approximately 20 mA, independent of the magnitude of V_{CC}.
2. For operating at high temperatures, the LM139, LM139A, LM2901 and MC3302 must be derated based on a + 125 °C max junction temperature and a thermal resistance of 175 °C/W which applies for the device soldered on a printed circuit board, operating in a still air ambient. The LM139 and LM139A must be derated based on a + 150 °C max junction temperature.
R_{th(j-a)} = 250 °C/W. Devices bonded on a 6 x 3 x 0.15 cm glass-epoxy substrate with 30 mm² of 35 µm thick copper.

SCHEMATIC DIAGRAM (1/4 LM139)



Case	Outputs	Inverting Inputs	Non-inverting Inputs	V _{CC}	V _{CC}	N.C.
DIP14 CERDIP14 SO14	1, 2, 13, 14	4, 6, 8, 10	5, 7, 9, 11	12	3	
LCC20	2, 3, 19, 20	6, 9, 12, 14	8, 10, 13, 16	18	4	*

* LCC20 : Other pins are not connected.

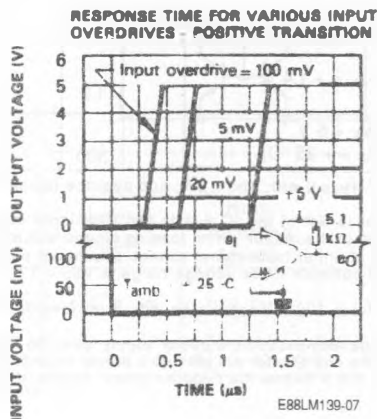
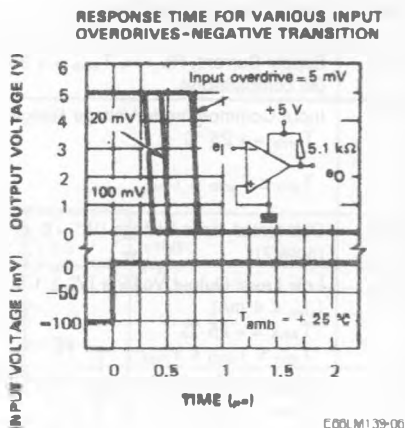
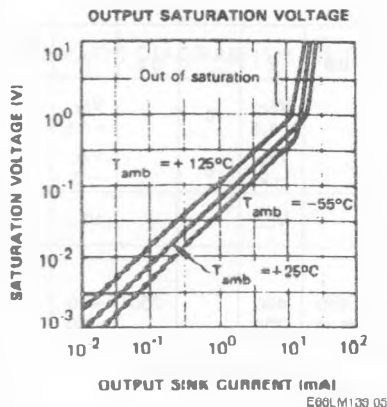
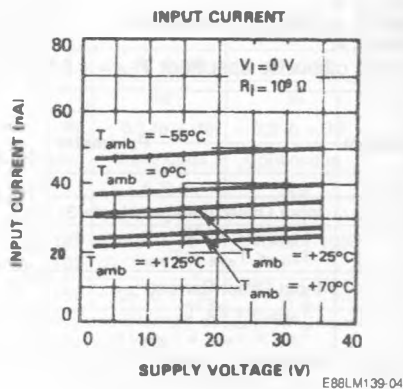
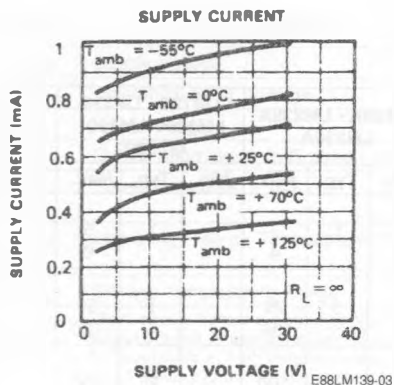
ELECTRICAL CHARACTERISTICS

LM139, A : $-55^{\circ}\text{C} < T_{\text{amb}} < +125^{\circ}\text{C}$
 LM239, A, LM2901, MC3302 : $-40^{\circ}\text{C} < T_{\text{amb}} < +105^{\circ}\text{C}$
 LM339, A : $0^{\circ}\text{C} < T_{\text{amb}} < +70^{\circ}\text{C}$
 (unless otherwise specified) $V_{\text{CC}} = +5\text{ V}$; $V_{\text{CC}} = \text{GND}$

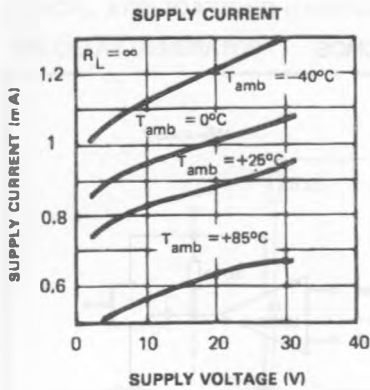
Symbol	Parameter	LM139A - LM239A LM339A			LM139 - LM239 LM339 - LM2901 MC3302			Unit
		Min.	Typ.	Max.	Min.	Typ.	Max.	
V_{IO}	Input Offset Voltage – (note 3) $T_{\text{amb}} = +25^{\circ}\text{C}$ $T_{\text{min}} \leq T_{\text{amb}} \leq T_{\text{max}}$		1	2 4		1	5 9	mV
I_{IO}	Input Offset Current $T_{\text{amb}} = +25^{\circ}\text{C}$ $T_{\text{min}} \leq T_{\text{amb}} \leq T_{\text{max}}$		3	25 100		5	50 150	nA
I_{IB}	Input Bias Current (I_{I^+} or I_{I^-}) $T_{\text{amb}} = +25^{\circ}\text{C}$ – (note 4) $T_{\text{min}} \leq T_{\text{amb}} \leq T_{\text{max}}$		25	100 300		25	250 400	nA
A_{VD}	Large Signal Voltage Gain $V_{\text{CC}} = +15\text{ V}$, $T_{\text{amb}} = +25^{\circ}\text{C}$, $R_{\text{L}} \geq 15\text{ k}\Omega$	50	200		25	200		V/mV
I_{CC}	Supply Current, $R_{\text{L}} = \infty$, $T_{\text{amb}} = +25^{\circ}\text{C}$ (all comparators)		0.8	2		0.8	2	mA
V_{I}	Input Common-mode Voltage Range (note 5) $T_{\text{amb}} = +25^{\circ}\text{C}$ $T_{\text{min}} \leq T_{\text{amb}} \leq T_{\text{max}}$	0 0		$V_{\text{CC}} - 1.5$ $V_{\text{CC}} - 2$	0 0		$V_{\text{CC}} - 1.5$ $V_{\text{CC}} - 2$	V
V_{ID}	Differential Input Voltage ($V_{\text{I}^+} = 0$, or $V_{\text{I}^-} = 0$, if used) (note 7)			V_{CC}			V_{CC}	V
V_{OL}	Low Level Output Voltage ($V_{\text{I}} \geq 1\text{ V}$, $V_{\text{I}}^+ = 0\text{ V}$, $I_{\text{sink}} \leq 4\text{ mA}$) $T_{\text{amb}} = +25^{\circ}\text{C}$ $T_{\text{min}} \leq T_{\text{amb}} \leq T_{\text{max}}$		250	400 700		250	400 700	mV
I_{OH}	High Level Output Current ($V_{\text{I}} \geq 1\text{ V}$, $V_{\text{I}}^- = 0\text{ V}$) $T_{\text{amb}} = +25^{\circ}\text{C}$, $V_{\text{O}} = +5\text{ V}$ $T_{\text{min}} \leq T_{\text{amb}} \leq T_{\text{max}}$, $V_{\text{O}} = +30\text{ V}$		0.1	1		0.1	1	nA μA
$I_{\text{O(sink)}}$	Output Sink Current ($V_{\text{I}} \geq 1\text{ V}$, $T_{\text{amb}} = +25^{\circ}\text{C}$, $V_{\text{I}}^+ = 0\text{ V}$, $V_{\text{O}} \leq +1.5\text{ V}$)	6	16		6	16		mA
t_{re}	Response Time – (note 6) ($V_{\text{L}} = +5\text{ V}$, $R_{\text{L}} = 5100\Omega$, $V_{\text{(ref)}} = +1.4\text{ V}$, $T_{\text{amb}} = +25^{\circ}\text{C}$)		1.3			1.3		μs
t_{rel}	Large Signal Response Time ($V_{\text{L}} = 5\text{ V}$, $\theta_{\text{I}} = \text{TTL}$, $V_{\text{(ref)}} = +1.4\text{ V}$, $T_{\text{amb}} = +25^{\circ}\text{C}$)		300			300		ns

Notes : 3. At output switch point, $V_{\text{O}} = 1.4\text{ V}$, $R_{\text{S}} = 0$ with $V_{\text{CC}} = 5\text{ V}$, and over the full input common-mode range (0 V to $V_{\text{CC}} - 1.5\text{ V}$).

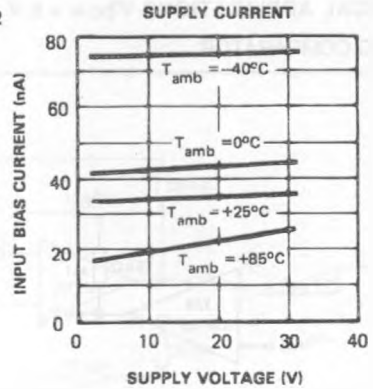
- The direction of the input current is out of the IC due to the PNP input stage. This current is essentially constant, independent of the state of the output, so no loading charge exists on the reference input lines.
- The input common-mode voltage of either input signal voltage should not be allowed to go negative by more than 0.3 V. The upper end of the common-mode voltage range is $V_{\text{CC}} - 1.5\text{ V}$, but either or both inputs can go to +30 V without damage.
- The response time specified is for a 100 mV input step with 5 mV overdrive. For larger overdrive signals 300 ns can be obtained.
- Positive excursions of input voltage may exceed the power supply level. As long as the other voltage remains within the common-mode range, the comparator will provide a proper output state. The low input voltage state must not be less than -0.3 V (or 0.3 V below the negative power supply, if used).



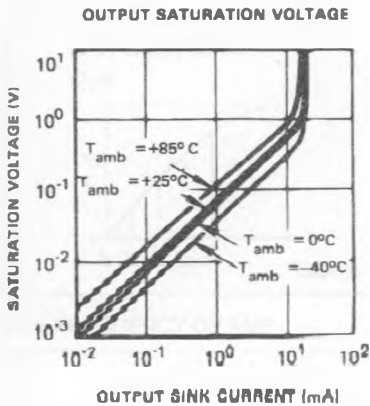
LM239,A
LM2901-MC3302



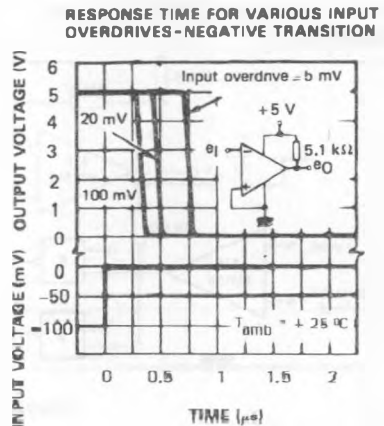
E88LM139-08



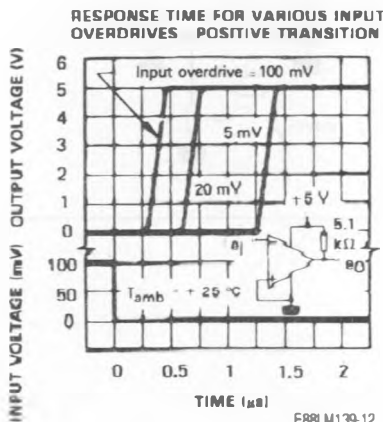
E88LM139-09



E88LM139-10



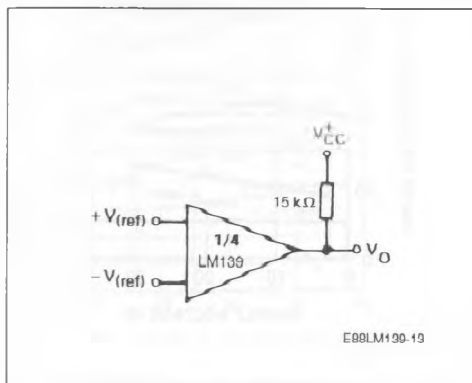
E88LM139-11



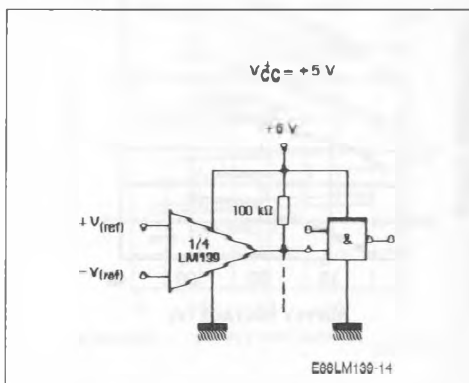
E88LM139-12

TYPICAL APPLICATIONS $V_{CC} = +5\text{ V}$

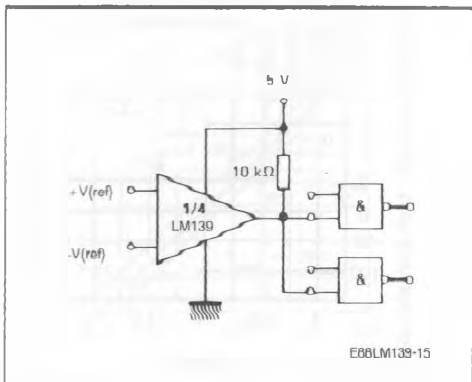
BASIC COMPARATOR



DRIVING CMOS

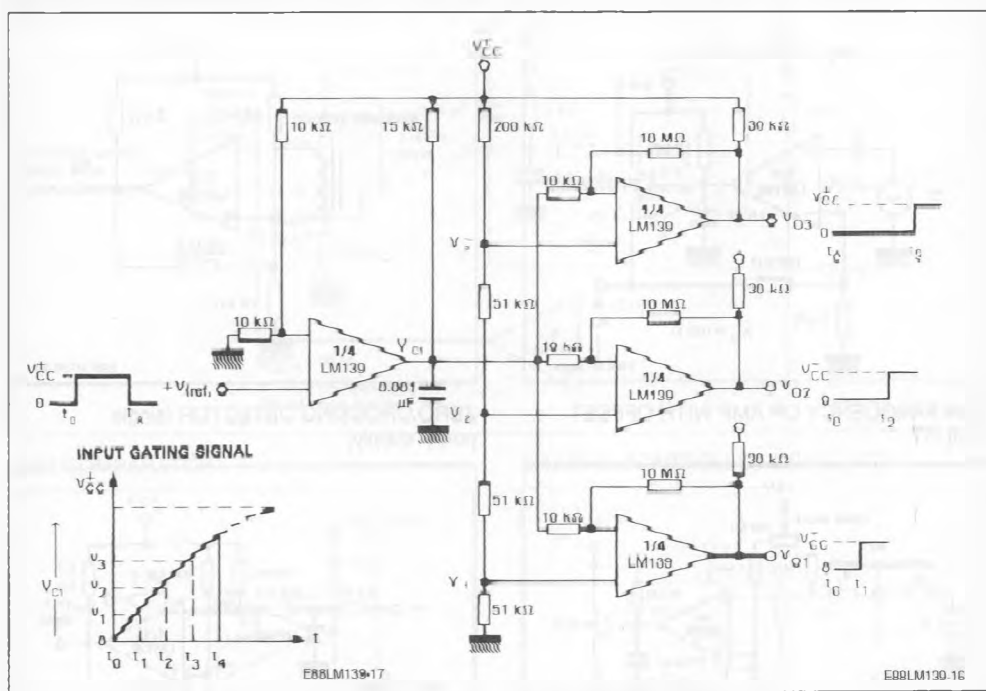


DRIVING TTL

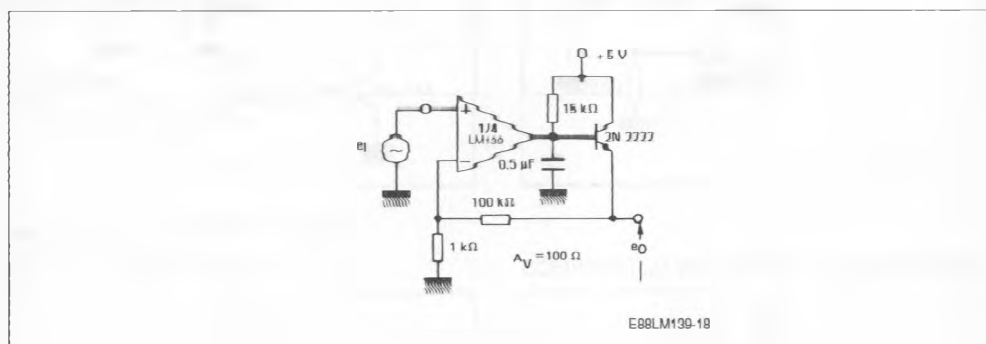


TYPICAL APPLICATIONS (continued)

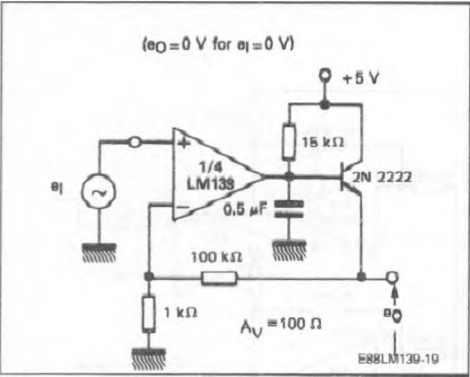
TIME DELAY GENERATOR



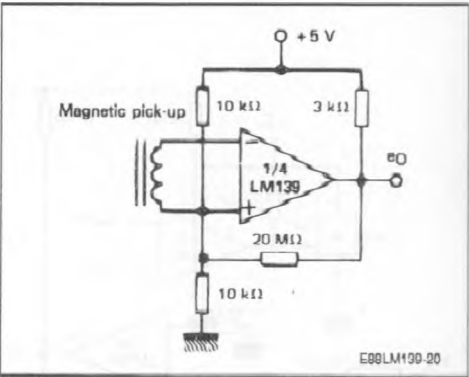
LOW FREQUENCY OP AMP



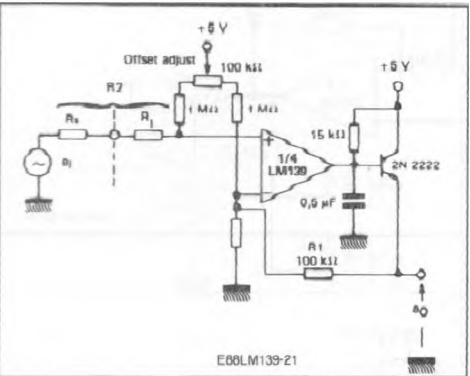
TYPICAL APPLICATIONS (continued)
LOW FREQUENCY OP AMP



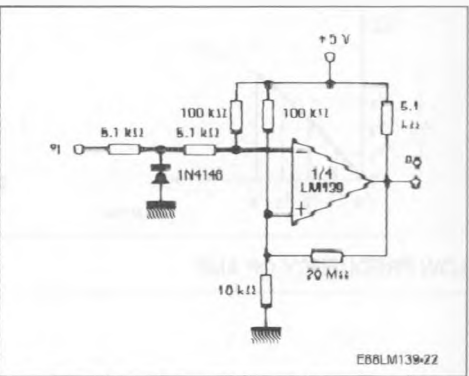
TRANSDUCER AMPLIFIER



LOW FREQUENCY OP AMP WITH OFFSET
ADJUST

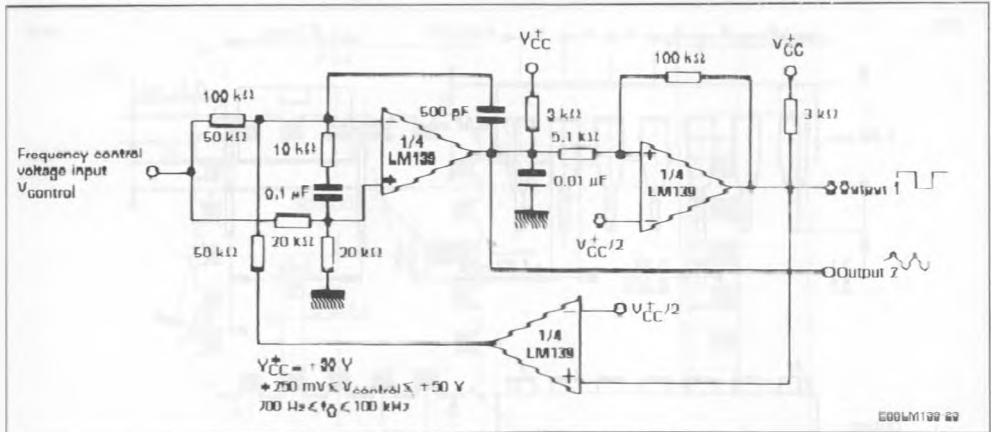


ZERO CROSSING DETECTOR (single
power supply)

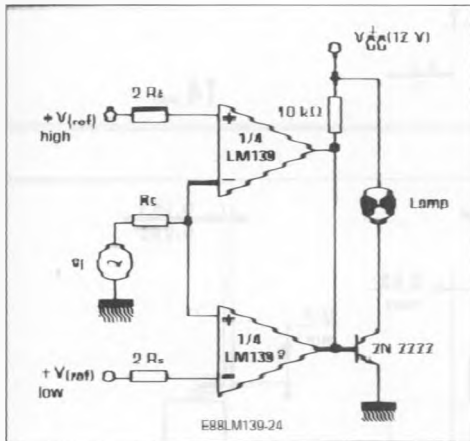


TYPICAL APPLICATIONS (continued)

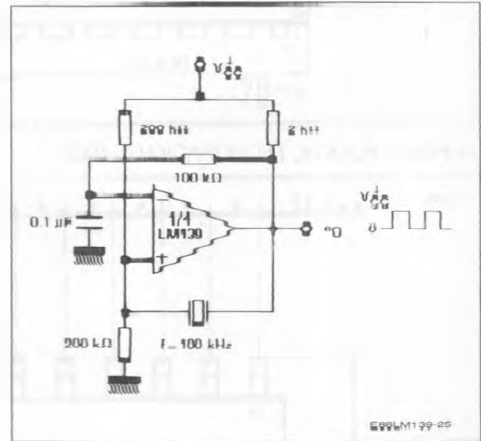
TWO-DECADE HIGH-FREQUENCY VCO



LIMIT COMPARATOR

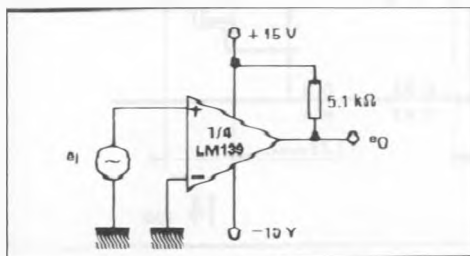


CRYSTAL CONTROLLED OSCILLATOR

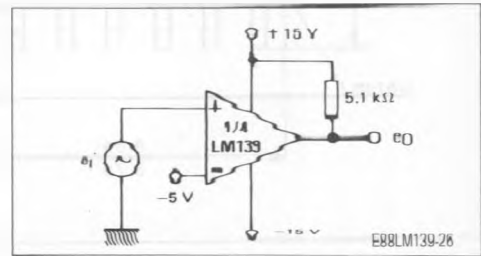


SPLIT-SUPPLY APPLICATIONS

ZERO CROSSING DETECTOR

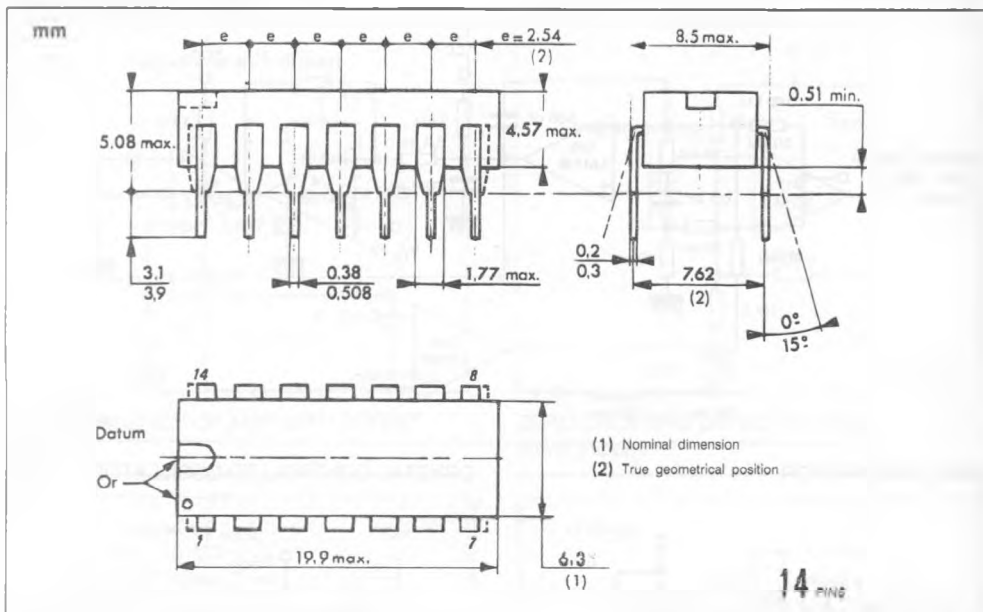


COMPARATOR WITH A NEGATIVE REFERENCE

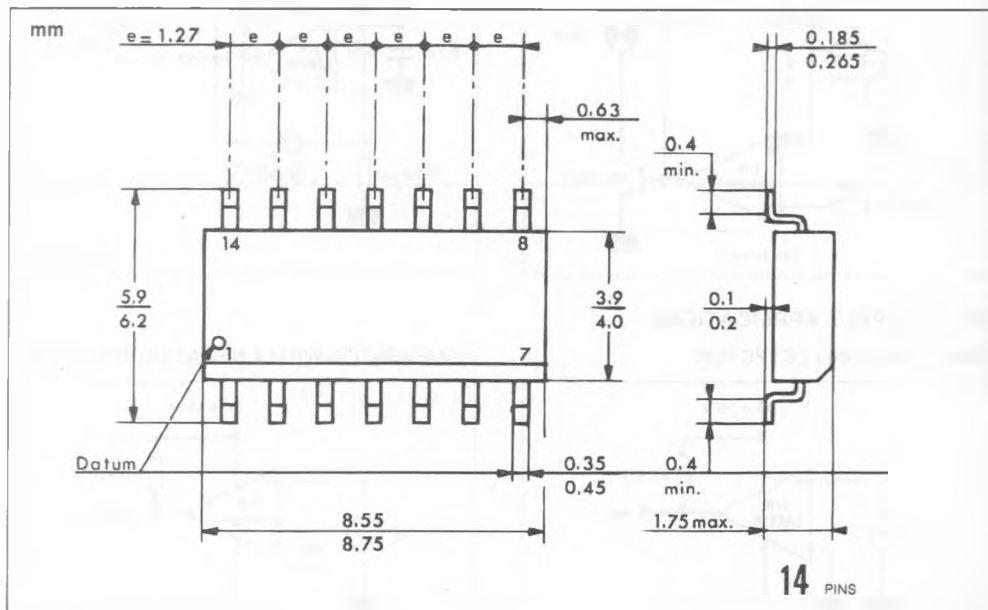


PACKAGE MECHANICAL DATA

14 PINS – PLASTIC DIP OR CerdIP



14 PINS – PLASTIC MICROPACKAGE (SO)



PACKAGE MECHANICAL DATA (continued)

20 PINS – TRICECOP (LCC)

