QUAD VOLTAGE COMPARATOR

DESCRIPTION

The LM139 series consists of four independent precision voltage comparators with an offset voltage specification as low as 2.0mV max for each comparator which were designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage. These comparators also have a unique characteristic in that the input common mode voltage range includes ground, even though operated from a single power supply voltage.

The LM139 series was designed to directly interface with TTL and CMOS. When operated from both plus and minus power supplies, the LM139 series will directly interface with MOS logic where their low power drain is a distinct advantage over standard comparators.

FEATURES

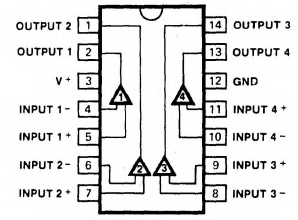
- **Wide single supply voltage range 2.0Vdc to 36Vdc or dual supplies ±1.0Vdc to ±18Vdc**
- **Very low supply current drain (0.8mA) independent of supply voltage (1.0mW/-comparator at 5.0Vdc)**
- **Low input biasing current 25nA**
- **Low input offset currrent ±5nA and offset voltage**
- **Input common-mode voltage range includes ground**
- **Differential input voltage range equal to the power supply voltage.**
- **Low output 250mV at 4mA saturation voltage**
- **Output voltage compatible with TTL, DTL, ECL, MOS and CMOS logic systems.**

APPLICATIONS

- **A/D converters**
- **Wide range VCO**
- **MOS clock generator**
- **High voltage logic gate**
- **Multivibrators**

PIN CONFIGURATION

D,F,N PACKAGE



TOP VIEW

ORDER NUMBERS

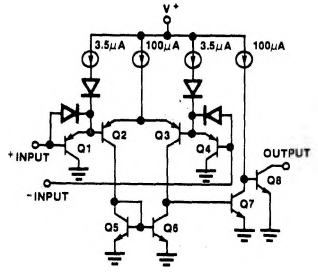
LM139/239/339F,N LM2901F,N
MC3302D,F,N LM339D

ABSOLUTE MAXIMUM RATINGS

PARAMETER	RATING	UNIT
V_{CC} supply voltage	36 or ±18	
Differential input voltage	36	
Input voltage	-0.3 to +36	
Power dissipation[1]		
N package	570	mW
F package	900	mW
Output short circuit to ground[2]	Continuous	
Input current (V_{IN} < -0.3Vdc)[3]	50	mA
Operating temperature range		
LM139/A	-55 to +125	°C
LM239/A	-25 to +85	°C
LM339/A	0 to +70	°C
LM2901/MC3302	-40 to +85	°C
Storage temperature range	-65 to +150	°C
Lead temperature (soldering 10 sec.)	300	°C

EQUIVALENT CIRCUIT

(1 Comparator Only)



DC ELECTRICAL CHARACTERISTICS

V+ = 5Vdc, LM139A/LM139: $-55°C \le T_A \le 125°C$ unless otherwise specified
LM239: $-25°C \le T_A \le 85°C$ unless otherwise specified
LM339: $0°C \le T_A \le 70°C$ unless otherwise specified
V+ = 5Vdc, LM339A: °C $\le T_A \le 70°C$ unless otherwise specified
LM239A: $-25°C \le T_A \le 85°C$ unless otherwise specified
LM2901/LM3302: $-40°C \le T_A \le 85°C$ unless otherwise specified

PARAMETER		TEST CONDITIONS	LM139A			LM239A/339A			LM139			LM239/339			LM2901			MC3302			UNIT
			Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
V_{OS}	Input offset voltage[5]	$T_A = 25°C$ Over temp.		± 1.0	± 2.0 4.0		± 1.0	± 2.0 ± 4.0		± 2.0	± 5.0 9.0		± 2.0	± 5.0 9.0		± 2.0 ± 9	± 7.0 ± 15		± 3.0	± 20 ± 40	mV
V_{CM}	Input common mode voltage range[6]	$T_A = 25°C$ Over temp.	0 0		V+ − 1.5 V+ − 2.0	0 0		V+ − 1.5 V+ − 2.0	0 0		V+ − 1.5 V+ − 2.0	0 0		V+ − 1.5 V+ − 2.0	0 0		V+ − 1.5 V+ − 2.0			V+ − 1.5 V+ − 2.0	V
V_{IDR}	Differential input[4] voltage[4]	Keep all $V_{INS} \ge$ 0Vdc (or V− if need)			V+			V+			V+			V+			V+			V+	V
I_B	Input bias current[7]	$I_{IN(+)}$ or $I_{IN(-)}$ with output in linear range $T_A = 25°C$ Over temp.		25	100 300		25	250 400		25	100 300		25	250 400		25 200	250 500		25	500 1000	nA
I_{OS}	Input offset current	$I_{IN(+)} - I_{IN(-)}$ $T_A = 25°C$ Over temp.		± 3.0	± 25 ± 100		± 5.0	± 50 ± 150		± 3.0	± 25 ± 100		± 5.0	± 50 ± 150		± 5 ± 50	± 50 ± 200		± 5	± 100 ± 300	nA nA
I_{OL}	Output sink current	$V_{IN(-)} \ge$ 1Vdc, $V_{IN(+)} = 0$, $V_O \le$ 1.5Vdc, $T_A = 25°C$ V_O = 800mV, over temp.	6.0	16		6.0	16		6.0	16		6.0	16		6.0	16		2.0	6		mA
I_{OH}	Output leakage current	$V_{IN(+)} \ge$ 1Vdc, $V_{IN(-)} = 0$ V_O = 5Vdc, $T_A = 25°C$ V_O = 30Vdc, over temp.		0.1	 1.0		0.1	 1.0		0.1	 1.0		0.1	 1.0		0.1	 1.0		0.1	 1.0	nA μA
I_{CC}	Supply current	V+ = 28V $R_L = \infty$ on comparators, $T_A = 25°C$ V+ = 30V		0.8	2.0		0.8	2.0		0.8	2.0		0.8	2.0		0.8 1.0	2.0 2.5		.8	1.8	mA
A_V	Voltage gain	$R_L \ge$ 15kΩ, V+ = 15Vdc	50	200		50	200		50	200		50	200		25	100		2	100		V/mV
V_{OL}	Saturation voltage	$V_{IN(-)} \ge$ 1Vdc, $V_{IN(+)} = 0$, $I_{SINK} \le$ 4mA $T_A = 25°C$ Over temp.		250	400 700		250	400 700		250	400 700		250	400 700		400	400 700		150	400 700	mV
T_{LSR}	Large signal response time	V_{IN} = TTL logic swing, V_{REF} = 1.4Vdc, V_{RL} = 5Vdc, R_L = 5.1kΩ, $T_A = 25°C$		300			300			300			300			300			300		ns
T_R	Response time[8]	V_{RL} = 5Vdc, R_L = 5.1kΩ, $T_A = 25°C$		1.3			1.3			1.3			1.3			1.3			1.3		μs

See notes on following page.

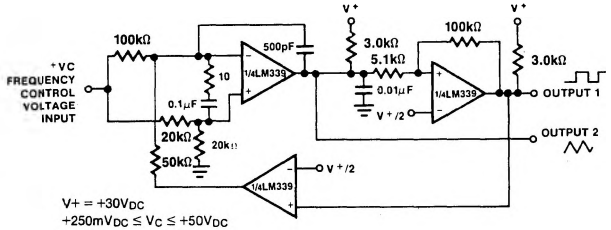
NOTES

1. For operating at high temperatures, the LM339/339A, LM2901 and MC3302 must be derated based on a 125°C maximum junction temperature and a thermal resistance of 175°C/W which applies for the device soldered in a printed circuit board, operating in a still air embient. The LM139/139A/239/239A must be derated on a 150°C maximum junction temperature. The low power dissipation and the "On-Off" characteristics of the outputs keep the chip dissipation very small ($P_D \leq 100mW$), provided the output transistors are allowed to saturate.
2. Short circuits from the output to V+ can cause excessive heating and eventual destruction. The maximum output current is approximately 20mA independent of the magnitude of V+.
3. This input current will only exist when the voltage at any of the input leads is driven negative. It is due to the collector-base junction of the input PNP transistors becoming forward biased and thereby acting as input diode clamps. In addition to this diode action, there is also lateral NPN parasitic transistor action on the IC chip. This transistor action can cause the output voltages of the comparators to go to the V+ voltage level (or to ground for a large overdrive) for the time duration that an input is driven negative. This is not destructive and normal output states will re-establish when the input voltage, which was negative, again returns to a value greater than −0.3Vdc.
4. Positive excursions of input voltage may exceed the power supply level by 17 volts. As long as the other voltage remains within the common-mode range, the comparator will provide a proper output state. The low input voltage state must not be less than −0.3Vdc (or 0.3Vdc below the magnitude of the negative power supply, if used).
5. At output switch point, $V_O \cong 1.4Vdc$, $R_S = 0\Omega$ with V+ from 5Vdc to 30Vdc; and over the full input common-mode range (0Vdc to V+ − 1.5Vdc).
6. The input common-mode voltage or either input signal voltage should not be allowed to go negative by more than 0.3V. The upper end of the common-mode voltage range is V+ − 1.5V, but either or both inputs can go to 30Vdc without damage.
7. The direction of the input current is out of the IC due to the PNP input stage. This current is essentially constant, independent of the state of the output so no loading change exists on the reference or input lines.
8. The response time specified is for a 100mV input step with a 5mV overdrive. For larger overdrive signals, 300ns can be obtained, see typical performance characteristics section.

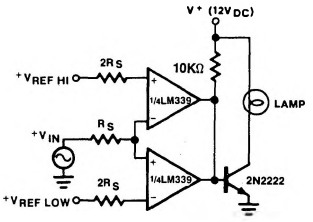
QUAD VOLTAGE COMPARATOR

TYPICAL APPLICATIONS

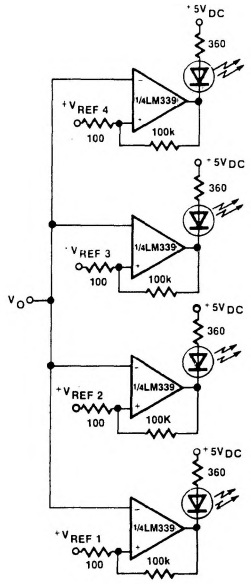
TWO-DECADE HIGH-FREQUENCY VCO



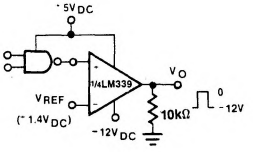
LIMIT COMPARATOR



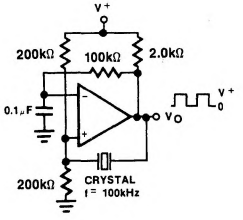
VISIBLE VOLTAGE INDICATOR



TTL TO MOS LOGIC CONVERTER



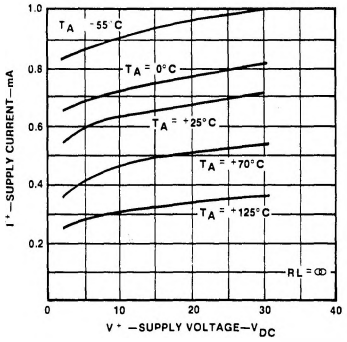
CRYSTAL CONTROLLED OSCILLATOR



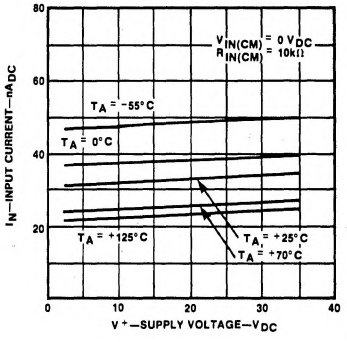
NOTE:
Inputs of unused comparators should be grounded.

TYPICAL PERFORMANCE CHARACTERISTICS

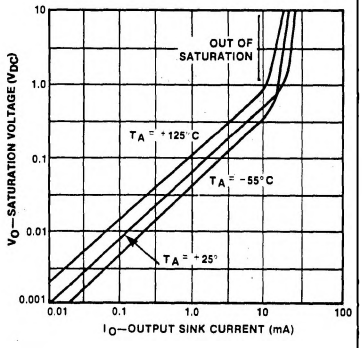
SUPPLY CURRENT



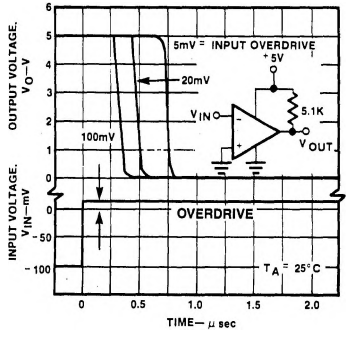
INPUT CURRENT



OUTPUT SATURATION VOLTAGE



RESPONSE TIME FOR VARIOUS INPUT OVERDRIVES— NEGATIVE TRANSITION



RESPONSE TIME FOR VARIOUS INPUT OVERDRIVES— POSITIVE TRANSITION