National Semiconductor

LM79MXX Series 3-Terminal Negative Regulators

General Description

The LM79MXX series of 3-terminal regulators is available with fixed output voltages of -5V, -12V, and -15V. These devices need only one external component—a compensation capacitor at the output. The LM79MXX series is packaged in the TO-202 power package and TO-39 metal can and is capable of supplying 0.5A of output current.

These regulators employ internal current limiting, safe area protection, and thermal shotdown for protection against virtually all overload conditions.

Low ground pin current of the LM79MXX series allows output voltage to be easily boosted above the preset value with a resistor divider. The low quiescent current of these devices with a specified maximum change with line and load ensures good regulation in the voltage boosted mode. For output voltage other than -5V, -12V, and -15V the LM137 series provides an output voltage range from 1.2V to 57V.

Features

- Thermal, short circuit and safe area protection
- High ripple rejection
- 0.5A output current
- 4% preset output voltage

Connection Diagram



Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Input Voltage	
$V_0 = 5V$	-25V
$V_{0} = 12V$ and 15V	-35V
Input/Output Differential	
$V_0 = 5V$	25V
$V_0 = 12V$ and 15V	30V

Power Dissipation	Internally Limited
Operating Junction Temperature Range	0°C to +125°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature (Soldering, 10 sec.)	230°C
ESD Susceptability	TBD

Electrical Characteristics

Conditions unless otherwise noted: I_{OUT} = 350 mA, C_{IN} = 2.2 μ F, C_{OUT} = 1 μ F, 0°C \leq T_J \leq +125°C

Part Number Output Voltage Input Voltage (Unless Otherwise Specified)		LM79M05C		LM79M12C - 12V			L	Units				
		-5V										
		- 10V		- 19V		-23V						
Symbol	Parameter	Conditions	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	
vo	Output Voltage	T _J = 25°C	-4.8	-5.0	-5.2	-11.5	- 12.0	- 12.5	-14.4	-15.0	- 15.6	v
		$5 \text{ mA} \leq I_{OUT} \leq 350 \text{ mA}$	-4.75 (-25	i≤ V _{IN} ≤	-5.25 ; -7)	-11.4 (-27 :	≤ V _{IN} ≤	- 12.6 14.5)	- 14.25 (30 :	≤ V _{IN} ≤	15.75 17.5)	v
ΔV _O	Line Regulation	T _J = 25°C (Note 2)	(-25	8 ≤ V _{IN} ≤		(-30			(-30 :	5 ≤ V _{IN} ≤ 3	80 17.5) 50	mV
			(18	2 ≤ V _{IN} ≤	30 - 8)	(-25	3 ≤ V _{IN} ≤	30 	(-28	3 ≤ V _{IN} ≤		mV
ΔV _O	Load Regulation	T _J = 25°C, (Note 2) 5 mA ≤ I _{OUT} ≤ 0.5A		30	100		30	240		30	240	mV
la	Quiescent Current	T _J = 25°C		1	2		1.5	3		1.5	3	mA
ΔIQ	Quiescent Current Change	With Input Voltage With Load.	(-25	i ≤ V _{IN} ≤	0.4 5 - 8)	(-30 :	≤ V _{IN} ≤	0.4 14.5)	(-30	≤ V _{IN} ≤	0.4 -27)	mA
		$5 \text{ mA} \leq I_{OUT} \leq 350 \text{ mA}$			0.4			0.4			0.4	mA
Vn	Output Noise Voltage	T _A = 25°C, 10 Hz ≤ f ≤ 100 Hz		750			400			400		μ٧
	Ripple Rejection	f = 120 Hz	54 (18	66 s ≤ V _{IN} ≤	. −8)	54 (25	70 ≤ V _{IN} ≤	. – 15)	54 (-30 :	70 ≤ V _{IN} ≤	- 17.5)	dB
	Dropout Voltage	T _J = 25°C, I _{OUT} = 0.5A		1.1			1.1			1.1		v
IOMAX	Peak Output Current	T _J = 25°C		800			800			800		mA
	Average Temperature Coefficient of Output Voltage	I _{OUT} = 5 mA, 0°C ≤ T _J ≤ 100°C		-0.4			-0.8			-1.0		mV/⁰C
	Thermal Resistance Junction to Case	P Package		12			12			12		°C/W
	Thermal Resistance Junction to Ambient	P Package		70			70			70		°C/W

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics. Note 2: Regulation is measured at a constant junction temperature by pulse testing with a low duty cycle. Changes in output voltage due to heating effects must be taken into account.

Typical Applications



M79M05CP

٤ 5k

2.2 µF :

INPUT O

D2

1N4001

O -5.0V

TL/H/10483-4