

# LM79MXX Series 3-Terminal Negative Regulators

### **General Description**

The LM79MXX series of 3-terminal regulators is available with fixed output voltages of -5V, -12V, and -15V. These devices need only one external component—a compensation capacitor at the output. The LM79MXX series is packaged in the TO-220 power package, and is capable of supplying 0.5A of output current.

These regulators employ internal current limiting, safe area protection, and thermal shotdown for protection against virtually all overload conditions.

Low ground pin current of the LM79MXX series allows output voltage to be easily boosted above the preset value with a resistor divider. The low quiescent current of these devices with a specified maximum change with line and load ensures good regulation in the voltage boosted mode. For output voltage other than -5V, -12V, and -15V the LM137 series provides an output voltage range from -1.2V to -57V.

LM79MXX Series

#### **Features**

- Thermal, short circuit and safe area protection
- High ripple rejection
- 0.5A output current
- 4% tolerance on preset output voltage



# Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Input Voltage	
$V_0 = -5V$	25V
$V_0 = -12V, -15V$	-35V
Input/Output Differential	
$V_{O} = -5V$	25V
$V_{O} = -12V, -15V$	30V

Power Dissipation (Note 2)	Internally Limited
Operating Junction Temperature Range	0°C to + 125°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature (Soldering, 10 sec.)	230°C
ESD Susceptability	TBD

# **Electrical Characteristics LM79M05C**

Conditions unless otherwise noted:  $I_{OUT} = 350 \text{ mA}$ ,  $C_{IN} = 2.2 \ \mu\text{F}$ ,  $C_{OUT} = 1 \ \mu\text{F}$ ,  $0^{\circ}\text{C} \le T_{J} \le +125^{\circ}\text{C}$ 

Part Number			LM79M05C			
Output Volt	age	-5V.			Units	
Input Voltage (Unless Otherwise Specified)			- 10V			]
Symbol	Parameter	Conditions	Min	Тур	Max	1 a
vo	Output Voltage	$T_J = 25^{\circ}C$	-4.8	-5.0	-5.2	v
		5 mA $\leq I_{OUT} \leq$ 350 mA	-4.75 (-	25 ≤ V <sub>IN</sub> ≤ −	-5.25 7)	v
ΔV <sub>O</sub>	Line Regulation	T <sub>J</sub> = 25°C (Note 3)		8	50	mV
				25 ≤ V <sub>IN</sub> ≤ − 2 18 ≤ V <sub>IN</sub> ≤ −	30	mV
ΔVo	Load Regulation	T <sub>J</sub> = 25°C, (Note 3) 5 mA ≤ l <sub>OUT</sub> ≤ 0.5A		30	100	mV
la	Quiescent Current	T <sub>J</sub> = 25°C		1	2	mA
∆lQ	Quiescent Current Change	With Input Voltage With Load,	(-	25 ≤ V <sub>IN</sub> ≤ −	0.4 8)	mA
		$5 \text{ mA} \le I_{OUT} \le 350 \text{ mA}$			0.4	mA
Vn	Output Noise Voltage	$T_A = 25^{\circ}C,$ 10 Hz $\le f \le 100$ Hz		150		μV
	Ripple Rejection	f = 120 Hz	54 (-	66 18 ≤ V <sub>IN</sub> ≤ −	8)	dB
	Dropout Voltage	$T_{\rm J} = 25^{\circ}{\rm C}, I_{\rm OUT} = 0.5{\rm A}$		1.1		v
IOMAX	Peak Output Current	T <sub>J</sub> = 25°C		800		mA
	Average Temperature Coefficient of Output Voltage	$I_{OUT} = 5 \text{ mA},$ $0^{\circ}\text{C} \le \text{T}_{\text{J}} \le 100^{\circ}\text{C}$		-0.4		mV/°C

Part Number Output Voltage Input Voltage (Unless Otherwise Specified)			LM79M12C - 12V - 19V			LM79M150			
					- 15V			Units	
					-23V				
Symbol	Parameter	Conditions	Min	Тур	Max	Min	Тур	Max	ĺ
Vo	Output Voltage	T <sub>J</sub> = 25°C	-11.5	-12.0	- 12.5	-14.4	- 15.0	- 15.6	1 V
		$5 \text{ mA} \le I_{OUT} \le 350 \text{ mA}$	-11.4 (-27	≤ V <sub>IN</sub> ≤ ·	- 12.6 - 14.5)		≤ V <sub>IN</sub> ≤ -	- 15.75 - 10.5)	v
ΔV <sub>O</sub>	Line Regulation	T <sub>J</sub> = 25°C (Note 3)		5 ≤ V <sub>IN</sub> ≤ - 3 ≤ V <sub>IN</sub> ≤	50		5, ≤ V <sub>IN</sub> ≤ - 3 3 ≤ V <sub>IN</sub> ≤	50	mV mV
ΔV <sub>O</sub>	Load Regulation	T <sub>J</sub> = 25°C, (Note 3) 5 mA ≤ I <sub>OUT</sub> ≤ 0.5A		30	240		30	240	mV
10	Quiescent Current	T <sub>J</sub> = 25°C		1.5	3	2	1.5	3	mA
ΔIQ	Quiescent Current Change	With Input Voltage With Load, 5 mA $\leq I_{OUT} \leq 350$ mA	(-30	≤ V <sub>IN</sub> ≤ -	0.4 - 14.5) 0.4	(-30	) ≤ V <sub>IN</sub> ≤ 1	0.4 27) 0.4	mA mA
Vn	Output Noise Voltage	$T_A = 25^{\circ}C,$ 10 Hz $\leq f \leq 100$ Hz		400		-	400	0.4	μV
	Ripple Rejection	f = 120 Hz	54 (-25	70 5 ≤ V <sub>IN</sub> ≤	- 15)	54 (-30	70 ≤ V <sub>IN</sub> ≤ -	- 17.5)	dB
	Dropout Voltage	$T_{J} = 25^{\circ}C, I_{OUT} = 0.5A$		1.1			1.1		v
IOMAX	Peak Output Current	T <sub>J</sub> = 25°C		800			800		mA
	Average Temperature Coefficient of Output Voltage	$I_{OUT} = 5 \text{ mA},$ 0°C $\leq T_J \leq 100$ °C		-0.8		2 A.	- 1.0		mV/°C

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings Indicate conditions for which the device is intended to be functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics.

Note 2: Refer to Typical Performance Characteristics and Design Considerations for details.

Note 3: Regulation is measued at a constant junction temperature by pulse testing with a low duty cycle. Changes in output voltage due to heating effects must be taken into account.

LM79MXX Series

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**Maximum Average Power** Dissipation (TO-220)

**Output Impedance** 

Cour = 5 µ

TANTALUN

FREQUENCY (Hz)

Quiescent Current vs

Load Current

LM79M05

UMINUM

COUT = 10 μF SOLID TANTALUM

= 0°C

= 250

1250 T,

OUTPUT CURRENT (A)

= 100 m/

= 5

COUT = 1.0 µF=

101

100

10

10-2

1.4

1.3

1.2

1.1

1.0

0.9

0.1

0 0.1 0.2 0.3 0.4 0.5

QUIESCENT CURRENT (mA)

0.01k 0.1k 1k 10k 100k 1.11

DUTPUT IMPEDANCE (D)

lour

Vout

Τ,

= 25%

= 1 118

100k

35 40



TL/H/10483-10

1-174



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# **Design Considerations**

The LM79MXX fixed voltage regulator series have thermaloverload protection from excessive power, internal short-circuit protection which limits the circuit's maximum current, and output transistor safe-area compensation for reducing the output current as the voltage across the pass transistor is increased.

Although the internal power dissipation is limited, the junction temperature must be kept below the maximum specified temperature in order to meet data sheet specifications. To calculate the maximum junction temperature or heat sink required, the following thermal resistance values should be used:

		~		
Package		θ <sub>JC</sub> (°C/W)	θ <sub>JA</sub> (°C/W)	]
	TO-220	3	40	
PC	$\theta_{MAX} = \frac{T_{JMax} - T}{\theta_{JC} + \theta_{C}}$	A or	-	(1)
	$=\frac{T_{JMax}-1}{\theta_{JA}}$	T <sub>A</sub> (Without a H	eat Sink)	
$\theta_{CA}$	$= \theta_{CS} + \theta_{SA}$			
Solvi	ing for T <sub>J</sub> :			
	$T_{A} + P_{D} (\theta_{JC} + T_{A} = + P_{D} \theta_{JA}$		Sink)	
Whe	re	-		
ТJ	= Junction Tem	perature		
TA	= Ambient Tem	perature		
PD	= Power Dissipa	ation		
$\theta_{\rm JC}$	= Junction-to-Ca	ase Thermal Re	sistance	
θCA	= Case-to-Ambi	ent Thermal Re	sistance	
$\theta_{CS}$	= Case-to-Heat	Sink Thermal R	esistance	
$\theta_{SA}$	= Heat Sink-to-/	Ambient Therma	al Resistance	
$\theta_{JA}$	= Junction-to-A	mbient Thermal	Resistance	

# **Typical Applications**

Bypass capacitors are necessary for stable operation of the LM79MXX series of regulators over the input voltage and output current ranges. Output bypass capacitors will improve the transient response of the regulator.

The bypass capacitors (2.2  $\mu$ F on the input, 1.0  $\mu$ F on the output), should be ceramic or solid tantalum which have good high frequency characteristics. If aluminum electrolytics are used, their values should be 10  $\mu$ F or larger. The bypass capacitors should be mounted with the shortest leads, and if possible, directly across the regulator terminals.



TL/H/10483-2

\*Required if regulator is separated from filter capacitor by more than 3". For value given, capacitor must be solid tantalum. 25 µF aluminum electrolytic may be substituted.

 $\dagger$  Required for stability. For value given, capacitor must be solid tantalum. 25  $\mu F$  aluminum electrolytic may be substituted. Values given may be increased without limit.

For output capacitance in excess of 100  $\mu$ F, a high current diode from input to output (1N4001, etc.) will protect the regulator from momentary input shorts.



\*Improves transient response and ripple rejection. Do not increase beyond 50 μF.

$$V_{OUT} = V_{SET} \left( \frac{R1 + R2}{R2} \right)$$

Select R2 as follows: LM79M05C 300Ω LM79M12C 750Ω LM79M15C 1k



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