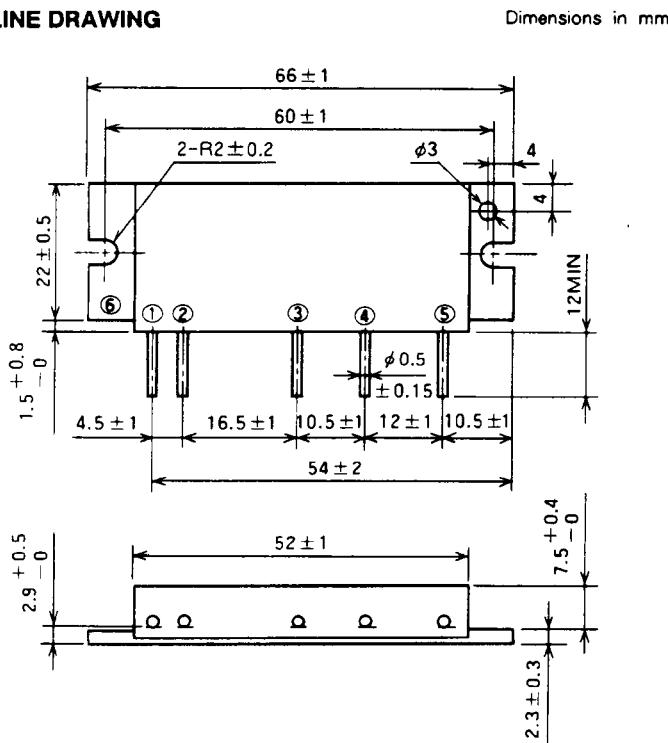
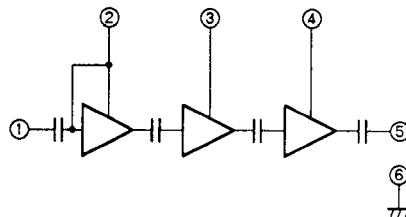


OUTLINE DRAWING

H3

BLOCK DIAGRAM

PIN :

- ① Pin : RF INPUT
- ② VCC1 : 1st. DC SUPPLY
- ③ VCC2 : 2nd. DC SUPPLY
- ④ VCC3 : 3rd. DC SUPPLY
- ⑤ Po : RF OUTPUT
- ⑥ GND : FIN

ABSOLUTE MAXIMUM RATINGS ($T_c = 25^\circ\text{C}$ unless otherwise noted)

| Symbol | Parameter | Conditions | Ratings | Unit |
|----------|----------------------------|-------------------------|-------------|------|
| Vcc | Supply voltage | | 17 | V |
| Icc | Total current | | 4 | A |
| Pin(max) | Input power | $Z_G = Z_L = 50 \Omega$ | 0.2 | W |
| Po(max) | Output power | $Z_G = Z_L = 50 \Omega$ | 12 | W |
| Tc(OP) | Operation case temperature | | - 30 to 110 | °C |
| Tstg | Storage temperature | | - 40 to 110 | °C |

Note. Above parameters are guaranteed independently.

ELECTRICAL CHARACTERISTICS ($T_c = 25^\circ\text{C}$ unless otherwise noted)

| Symbol | Parameter | Test conditions | Limits | | Unit |
|-------------|---------------------|--|---------------------------|------|------|
| | | | Min | Max | |
| f | Frequency range | $\text{Pin} = 0.1\text{W}$ $\text{Vcc} = 12.5\text{V}$ $Z_G = Z_L = 50 \Omega$ | 430 | 450 | MHz |
| Po | Output power | | 7 | | W |
| η_T | Total efficiency | | 38 | | % |
| 2fo | 2nd. harmonic | | | - 30 | dBc |
| ρ_{in} | Input VSWR | | | 2 | - |
| - | Load VSWR tolerance | $\text{Vcc} = 15.2\text{V}$, $\text{Po} = 7\text{W}$ (Pin : controlled) Load VSWR=20:1(All phase), 2sec. $Z_G = 50 \Omega$ | No degradation or destroy | | - |

Note. Above parameters, ratings, limits and conditions are subject to change.