



General Description

The MAX4147 differential line driver offers high-speed performance while consuming only 100mW of power. Its amplifier has fully symmetrical inputs and outputs and uses laser-trimmed, matched, thin-film resistors to deliver 70dB CMR at 10MHz. Using current-feedback techniques, the MAX4147 achieves a 300MHz bandwidth and a 2000V/µs slew rate.

Optimized for differential, high-output-current applications such as transformer drivers, the MAX4147 drives ± 2.6 V into a 26.5 Ω load (single-ended) or ± 5.6 V into a 53Ω load (differential). This device is preset for a closed-loop gain of 2V/V. Its ultra-low 0.008%/0.03° differential gain/phase allow for a variety of video and RF signal-processing applications.

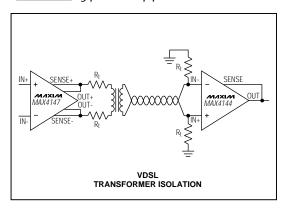
For power-sensitive applications, the MAX4147 has a shutdown function that reduces supply current to less than 1mA. In addition, superior SFDR (-82dBc at 10kHz, $R_L = 33\Omega$) makes it ideal as a transformer driver for HDSL applications.

For a complete differential transmission link, use the MAX4147 with the MAX4144 line receiver (see the MAX4144 data sheet for more information).

Applications

VDSL, ADSL, HDSL Video Twisted-Pair Driver Differential Pulse Amplifier Differential ADC Driver

Typical Application Circuit



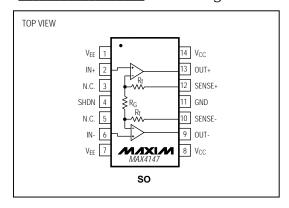
Features

- ♦ 2V/V Fixed Gain
- ♦ 300MHz -3dB Bandwidth
- ♦ 2000V/µs Slew Rate
- ♦ 82dBc SFDR at 10kHz
- ♦ 70dB CMR at 10MHz
- ♦ Low Differential Gain/Phase: 0.008%/0.03°
- ♦ High Output Drive: ±5.6V into 53Ω
- ♦ Low Power: 100mW

Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX4147ESD	-40°C to +85°C	14 SO

Pin Configuration



/VIXI/VI

Maxim Integrated Products 1

ABSOLUTE MAXIMUM RATINGS

Supply Voltage (V_{CC} to V_{EE})	Operating Temperature Range MAX4147ESD40°C to +85°C Storage Temperature Range65°C to +160°C Lead Temperature (soldering, 10sec)+300°C
Short-Circuit Duration	, , ,

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{CC} = +5V, V_{EE} = -5V, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } T_A = +25^{\circ}C.)$

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
DC SPECIFICATIONS	•		1			
Input Offset Voltage	Vos	Vout = 0V, R _L = ∞		0.5	6	mV
Input Offset Voltage Drift	TCVos	V _{OUT} = 0V, R _L = ∞		30		μV/°C
Input Bias Current	ΙΒ	V _{OUT} = 0V, R _L = ∞, V _{IN} = -V _{OS}		9	20	μΑ
Input Offset Current	los	Vout = 0V, RL = ∞, VIN = -Vos		0.03	2	μΑ
Input Voltage Noice	en	f = 10kHz		8		nV/√Hz
Input Voltage Noise		f = 1MHz to 100MHz		80		μVRMS
Input Current Noise	In	f = 10kHz		1.7		pA/√Hz
Input Current Noise		f = 1MHz to 100MHz		17		nA _{RMS}
Input Capacitance	CIN			1		pF
Differential Input Resistance				1		MΩ
Differential Input Voltage Range		R _L = ∞	-3.6		3.6	V
Common-Mode Input Voltage Range	Vсм	RL = ∞	-2.8		2.8	V
Gain	A _V	$-1V \le V_{OUT} \le +1V$, $R_L = 53\Omega$		2		V/V
Gain Error		$-1V \le V_{OUT} \le +1V$, $R_L = 53\Omega$		0.3	1	%
Common-Mode Rejection	CMR	V _{CM} = ±2.8V	70	100		dB
Power-Supply Rejection	PSR	$V_S = \pm 4.5 V \text{ to } \pm 5.5 V$	70	100		dB
Quiescent Supply Current	Isy	V _{IN} = 0, R _L = ∞		10	13	mA
Shutdown Supply Current	ISHDN	V _{IN} = 0, R _L = ∞		0.6	1	mA
	Vout	Single-ended, R _L = ∞	3.2	3.8		- V
Output Voltage Swing		Differential, R _L = ∞	7.2	7.8		
Output voltage Swing		Single-ended, $R_L = 26.5\Omega$	2.2	2.6		
		Differential, $R_L = 53\Omega$	5.0	5.6		
Output Current Drive	I _{OUT}	$V_{OUT} = \pm 2.2V$	110	160		mA
SHDN High Threshold	VIH				2.0	V
SHDN Low Threshold	V _{IL}		0.8			V
SHDN Input Current	ISHDN	VSHDN = 0V		75	150	μΑ

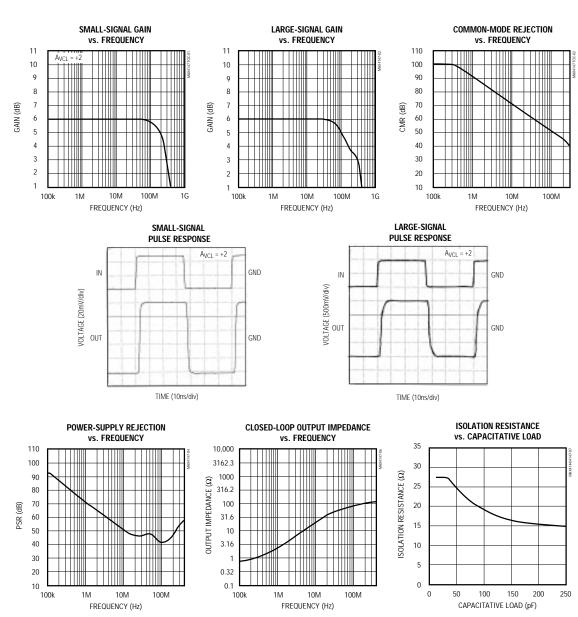
ELECTRICAL CHARACTERISTICS (continued)

(VCC = +5V, VEE = -5V, TA = T_{MIN} to T_{MAX} , unless otherwise noted. Typical values are at TA = +25°C.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS	
AC SPECIFICATIONS								
-3dB Bandwidth	BW(-3dB)	Vout ≤ 0.1V _{RMS}			300		MHz	
Full-Power Bandwidth	FPBW	V _{OUT} = 2Vp-p			250		MHz	
0.1dB Bandwidth	BW(0.1dB)	Vout ≤ 0.1V _{RMS}			70		MHz	
Common-Mode Rejection	CMR	f = 10MHz		-lz 70			dB	
Slew Rate	SR	Differential, -2V ≤ V _{OUT} ≤ +2V		$-2V \le V_{OUT} \le +2V $ 2000			V/µs	
Cattling Time	t _S	$1V \le V_{OUT} \le +1V$, $R_L = 150\Omega$, $A_{VCL} = +2$	to 0.1%		10		ns	
Settling Time			to 0.01%		30			
Differential Gain	DG	$f = 3.58MHz, R_L = 150\Omega$			0.008		%	
Differential Phase	DP	$f = 3.58MHz$, $R_L = 150\Omega$			0.03		degrees	
Spurious Froe Dynamic Dange	SFDR	f_C = 10kHz, V_{OUT} = 4.0Vp-p, R_L = 33 Ω single-ended, R_S = 50 Ω , Figure 1			-82		dBc	
Spurious-Free Dynamic Range		$f_C = 5MHz$, $V_{OUT} = 2Vp-p$, $R_L = 150\Omega$ differential, Figure 2			-75		dBC	

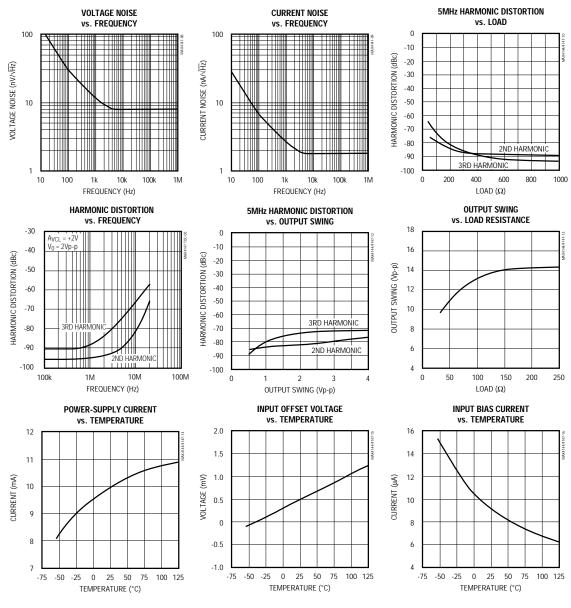
_Typical Operating Characteristics

 $(V_{CC} = +5V, V_{EE} = -5V, R_L = 150\Omega, T_A = +25^{\circ}C, unless otherwise noted.)$



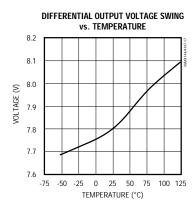
_Typical Operating Characteristics (continued)

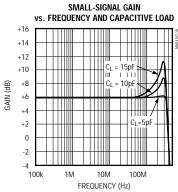
 $(V_{CC} = +5V, V_{EE} = -5V, R_L = 150\Omega, T_A = +25^{\circ}C, unless otherwise noted.)$

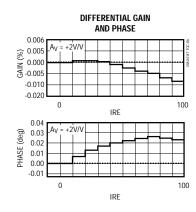


_Typical Operating Characteristics (continued)

 $(V_{CC} = +5V, V_{EE} = -5V, R_L = 150\Omega, T_A = +25^{\circ}C, unless otherwise noted.)$







_Pin Description

PIN	NAME	FUNCTION
1, 7	VEE	Negative Power Supply. Connect to -5V.
2	IN+	Noninverting Input
3, 5	N.C.	No Connect. Not internally connected.
4 SHDN Logic Input for Shutdown Circuitry. A logic low enables the amplifier. A log amplifier.		Logic Input for Shutdown Circuitry. A logic low enables the amplifier. A logic high disables the amplifier.
6	IN-	Inverting Input
8, 14	Vcc	Positive Power Supply
9	OUT-	Inverting Output
10	SENSE-	Sense Line for the Inverting Output. Connect to OUT-, close to the pin.
11	GND	Ground
12	SENSE+	Sense Line for the Noninverting Output. Connect to OUT+, close to the pin.
13	OUT+	Noninverting Output

6 ______M/XI/M

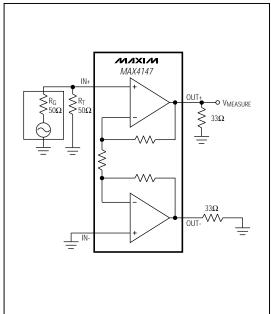


Figure 1. Single-Ended Distortion Setup

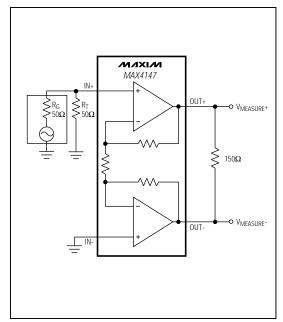
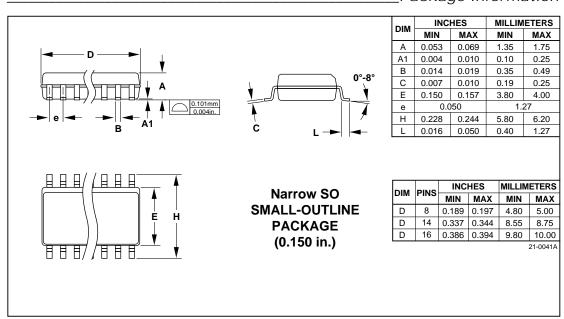


Figure 2. Differential Distortion Setup

Package Information



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