

MAXIM

High-Side Power Supplies

General Description

The MAX622/MAX623 high-side power supplies, using a regulated charge-pump, generate a regulated output voltage 11V greater than the input supply voltage to power high-side switching and control circuits. The MAX622/MAX623 allow low-resistance N-Channel MOSFETs (FETs) to be used in circuits that normally require costly, less efficient P-Channel FETs and PNP transistors. The high-side output also eliminates the need for logic FETs in +5V and other low-voltage switching circuits.

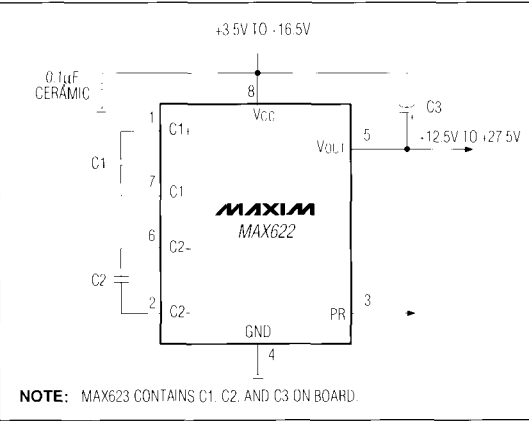
A +3.5V to +16.5V input supply range and a typical quiescent current of only 70µA make the MAX622/MAX623 ideal for a wide range of line- and battery-powered switching and control applications where efficiency is crucial. Also provided is a logic-level Power-Ready Output (PR) to indicate when the high-side voltage reaches the proper level.

The MAX622 comes in 8-pin DIP and SO packages and requires three inexpensive external capacitors. The MAX623 is supplied in 16-pin DIPs only, but contains internal capacitors and requires no external components.

Applications

- High-Side Power Control with N-Channel FETs
- Low-Dropout Voltage Regulators
- Power Switching from Low Supply Voltages
- H-Switches
- Stepper Motor Drivers
- Battery-Load Management
- Portable Computers

Typical Operating Circuit



Features

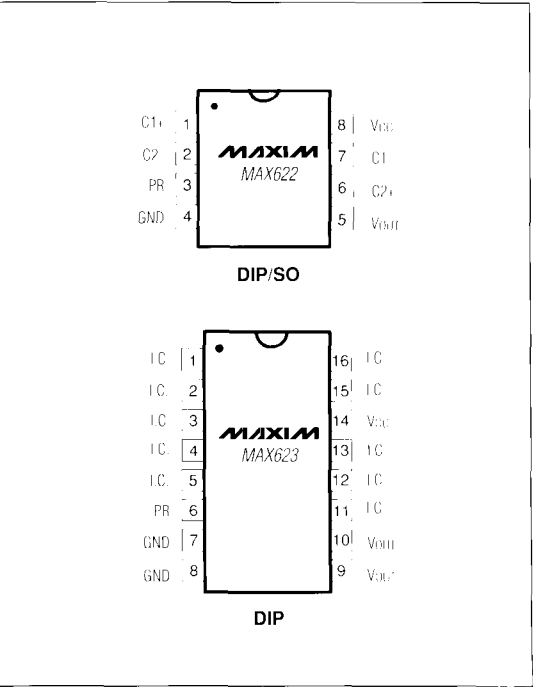
- ◆ +3.5V to +16.5V Operating Supply Voltage Range
- ◆ Output Voltage Regulated to V_{CC} + 11V (Typ)
- ◆ 70µA Typ Quiescent Current
- ◆ Power-Ready Output

Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX622CPA	0 C to +70 C	8 Plastic DIP
MAX622CSA	0 C to +70 C	8 SO
MAX622C/D	0 C to +70 C	Dice*
MAX622EPA	-40 C to +85 C	8 Plastic DIP
MAX622ESA	-40 C to +85 C	8 SO
MAX623CPE	0 C to +70 C	16 Plastic DIP
MAX623EPE	-40 C to +85 C	16 Plastic DIP

*Contact factory for dice specifications.

Pin Configurations



High-Side Power Supplies

ELECTRICAL CHARACTERISTICS (MAX623)

(VCC = +5V, TA = TMIN to TMAX, unless otherwise noted)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Supply Voltage	VCC		3.5		16.5	V
High-Side Voltage (Note 1)	VOUT	IOUT = 0, VCC = 3.5V	11.5	12.5	16.5	V
		IOUT = 0, VCC = 4.5V	14.5	15.5	17.5	
		IOUT = 0, VCC = 16.5V	26.5	27.5	29.5	
		IOUT = 50µA, VCC = 3.5V	8.5	10.5	16.5	
		IOUT = 250µA, VCC = 5V	15		18	
		IOUT = 500µA, VCC = 16.5V	26.5		29.5	
Power-Ready Threshold	PRT	IOUT = 0 (Note 3)	12	13.5	14.5	V
Power-Ready Output High	PRQH	ISOURCE = 100µA	3.8	4.3	5	V
Power-Ready Output Low	PRQL	ISINK = 1mA			0.4	V
Output Voltage Ripple	VR	IOUT = 500µA (Note 4)		100		mV
Switching Frequency	FO			90		kHz
Quiescent Supply Current	IQ	IOUT = 0, VCC = 5V, TA = +25 C		70	500	µA
		IOUT = 0, VCC = 16.5V, TA = +25 C		70	350	

- Note 1:** High-Side Voltage measured with respect to ground.
Note 2: For VCC > +13V on the MAX622, use C1 = C2 = 0.01µF.
Note 3: Power-Ready Threshold is the voltage with respect to ground at VOUT when PR switches high (PR = VCC).
Note 4: Output Voltage Ripple on the MAX623 may be reduced by adding an external 10µF reservoir capacitor.

MAX622/MAX623

Pin Description

MAX622 8-PIN	MAX623 16-PIN	NAME	FUNCTION
1	1-5, 11-13, 15, 16	C1+	Positive terminal to primary charge-pump capacitor.
		I.C.	Internal Connection. Make no connection to this pin.
2		C2-	Negative terminal to secondary charge-pump capacitor.
3	6	PR	Power-Ready Output. High when VOUT is ≥ VCC + 8.5V with respect to GND.
4	7, 8	GND	Ground
5	9, 10	VOUT	High-Side Voltage Out
6		C2+	Positive terminal to secondary charge-pump capacitor.
7		C1-	Negative terminal to primary charge-pump capacitor.
8	14	VCC	Input Supply

High-Side Power Supplies

ELECTRICAL CHARACTERISTICS (MAX623)

(VCC = +5V, TA = TMIN to TMAX, unless otherwise noted)

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		IOUT = 0, VCC = 4.5V	14.5	15.5	17.5	
		IOUT = 0, VCC = 16.5V	26.5	27.5	29.5	
		IOUT = 50μA, VCC = 3.5V	8.5	10.5	16.5	
		IOUT = 250μA, VCC = 5V	15		18	
		IOUT = 500μA, VCC = 16.5V	26.5		29.5	
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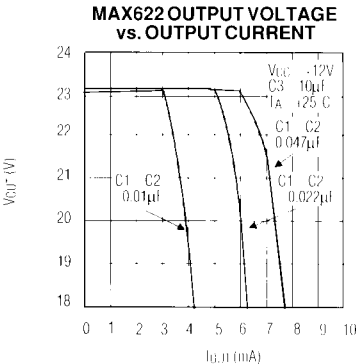
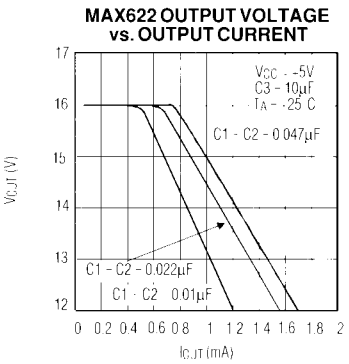
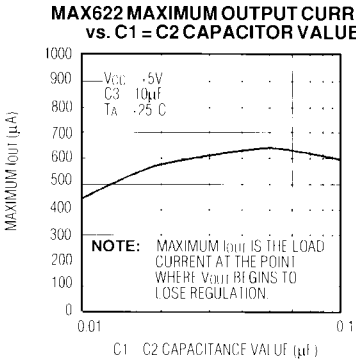
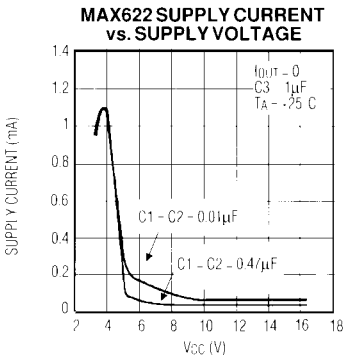
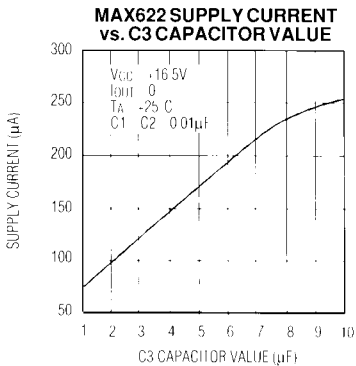
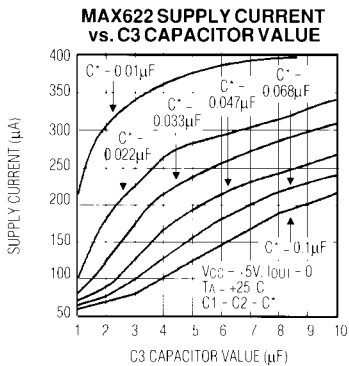
MAX622/MAX623

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2		C2-	Negative terminal to secondary charge-pump capacitor.
3	6	PR	Power-Ready Output. High when VOUT is ≥ VCC + 8.5V with respect to GND.
4	7, 8	GND	Ground
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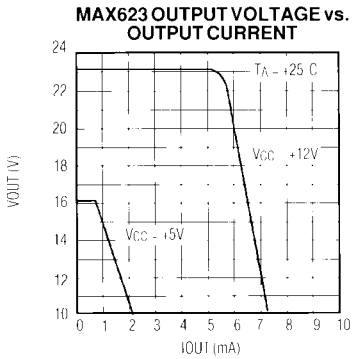
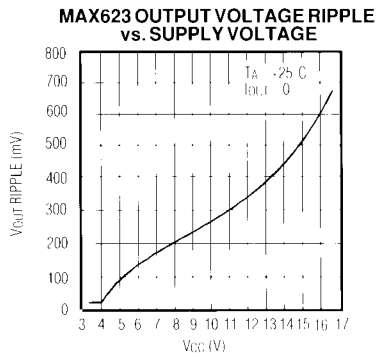
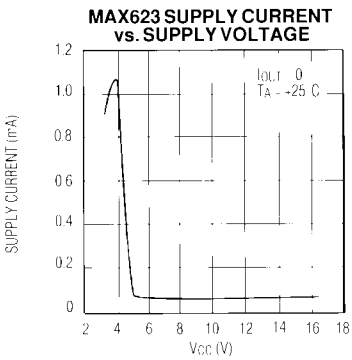
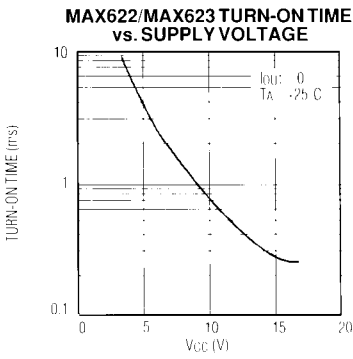
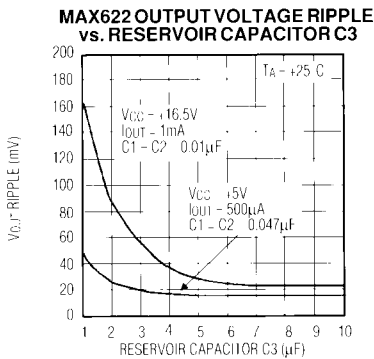
Typical Operating Characteristics



High-Side Power Supplies

Typical Operating Characteristics (continued)

MAX622/MAX623



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V_{OUT} ripple is typically 50mV peak-to-peak with V_{CC} = +5V, C1 and C2 = 0.047μF, and C3 = 1μF (*Typical Operating Characteristics*). Ripple can be reduced by increasing the ratio between the output storage capacitor C3 and C1 and C2. This is usually accomplished by increasing C3 and keeping C1 and C2 in the 0.01μF to 0.047μF range. For example, if C1 and C2 are 0.047μF (V_{CC} must not exceed 13V) and C3 is 10μF, output ripple typically falls to 15mV (*Typical Operating Characteristics*). Similarly, MAX623 output ripple is reduced by adding an external storage capacitor from V_{OUT} to V_{CC}.

Capacitor type is unimportant when selecting capacitors for the MAX622. However, when V_{CC} exceeds 13V, C1 and C2 must be no greater than 0.01 μ F. Using larger value capacitors with input voltages above 13V causes excessive amounts of energy to pass through internal

Output Protection

$$R_{CL} \geq \frac{V_{CC}}{25\text{mA}}$$

Simple Single-Load Switch

A single switch can be made with the MAX622/MAX623 and a MAX480 op amp configured as a comparator

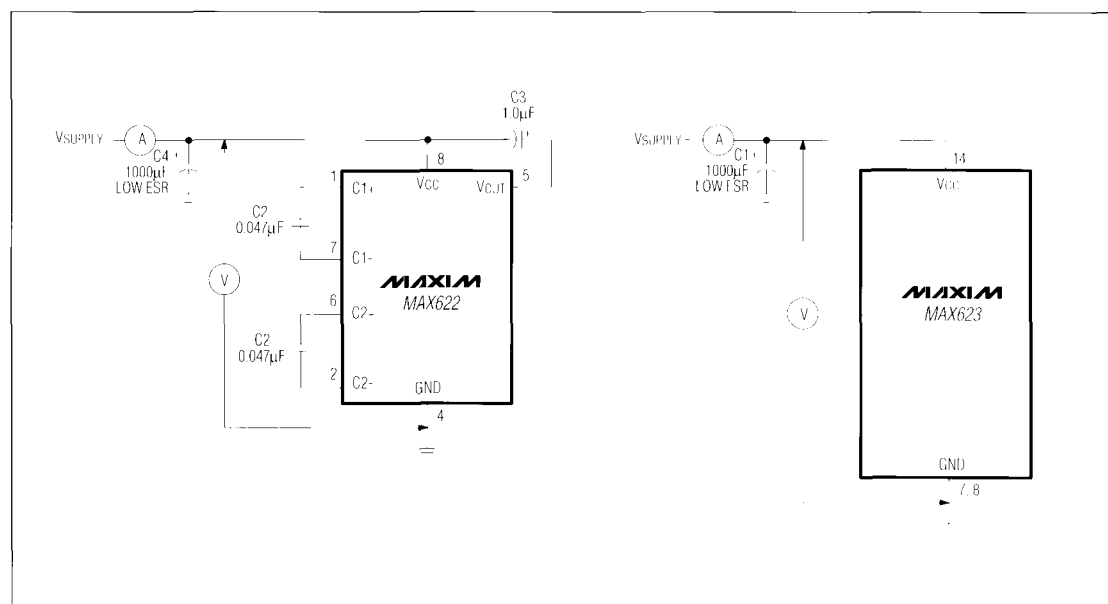


Figure 2. MAX622/MAX623 Quiescent Supply-Current Test Circuits

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(Figure 3). The switch is turned on by applying VBATT to the ON/OFF input and turned off by pulling it to GND.

One MAX622 Drives Six High-Side Switches

Multiple subsystems or modules can be turned on and off using a single MAX622 and an open-drain hex buffer such as the 74C906 (Figure 4). The drains of all buffer outputs are pulled up through resistors to the MAX622's VOUT. The pull-up resistance depends on the number of channels being used with the MAX622/MAX623 and power-dissipation limitations. The minimum pull-up resistor value is determined by the number of channels paralleled on each high-side power supply and the high-side output current from the MAX622/MAX623 at a given supply voltage, calculated as follows:

$$R_{MIN} = \frac{V_{OUT} \times (\text{number of channels})}{I_{OUT}}$$

where VOUT is the high-side output voltage and IOUT is the output current of the MAX622.

For example, assuming an output current of 1mA and six channels, as in Figure 4, the minimum pull-up resistor value that will not excessively load the MAX622 is about 100kΩ, assuming all six channels are pulled low at the same time. The value of the pull-up resistor also affects the turn-on time of each FET, and hence the amount of

energy dissipated in the FET during turn on. The rate of rise of VGS is limited by the RC time constant of the pull-up resistor and FET gate capacitance; waste power will be dissipated in the FET equal to (ILOAD)² x rDS during the RC time period.

H-Bridge Motor Driver

An H-bridge motor driver is shown in Figure 5. The motor direction can be controlled by toggling between IN1 and IN2 of the DG303 analog switch. Each switch section turns on the appropriate FET pair which passes current through the motor in the desired direction.

Battery-Load Controller

In Figure 6, a MAX8211 undervoltage detector detects the battery's end-of-life, and a MAX622 high-side power supply turns the power FET switch on. During normal operation, the MAX8211 Hysteresis pin powers the MAX622, providing gate-drive to keep the FET off. When the battery reaches its discharge threshold (end-of-life), the MAX8211 output pulls the FET gate low, cutting off current to the load. At the same time, the Hysteresis pin goes low, turning off the MAX622. As a result, supply current is approximately 10μA in the load-disconnected condition.

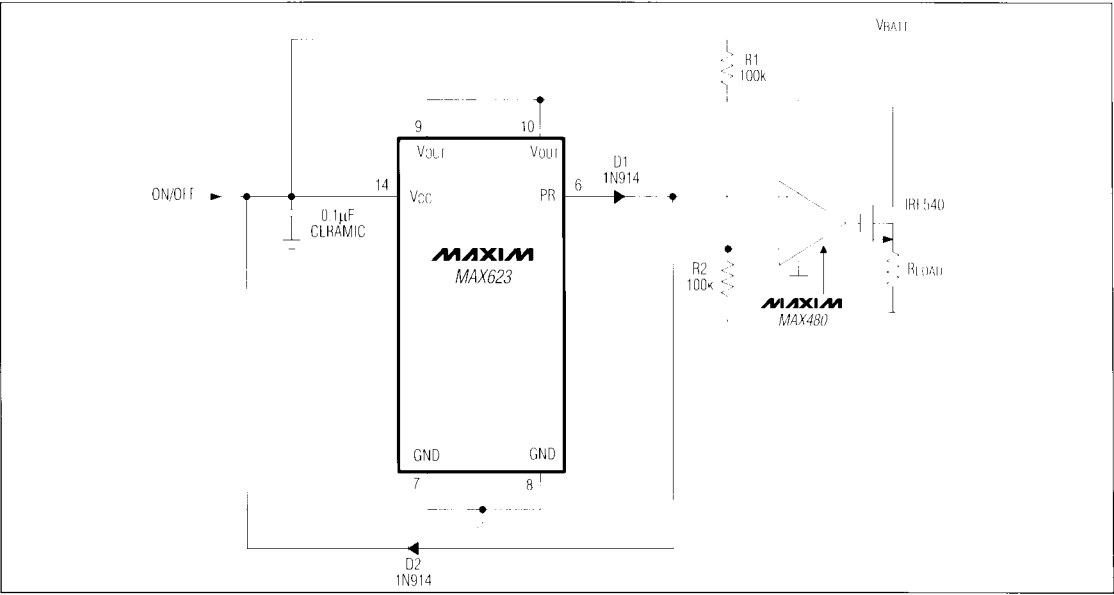


Figure 3. Single-Load Switch

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(Figure 3). The switch is turned on by applying VBATT to the ON/OFF input and turned off by pulling it to GND.

One MAX622 Drives Six High-Side Switches

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energy dissipated in the FET during turn on. The rate of rise of VGS is limited by the RC time constant of the pull-up resistor and FET gate capacitance; waste power will be dissipated in the FET equal to $(I_{LOAD})^2 \times r_{DS}$ during the RC time period.

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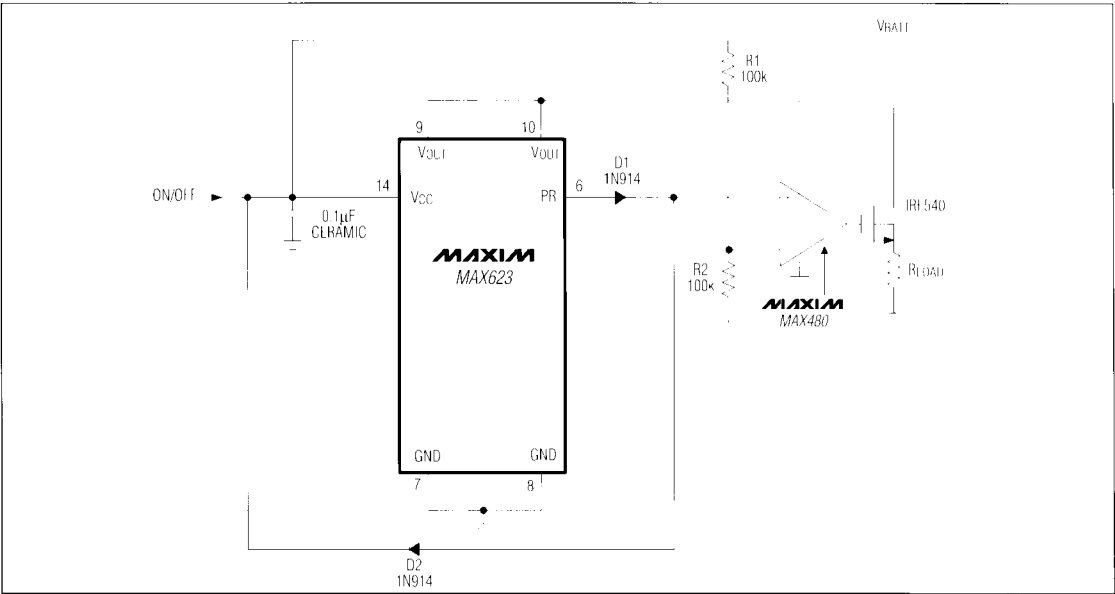
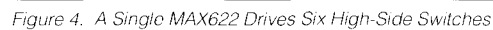


Figure 3. Single-Load Switch

MAX622/MAX623



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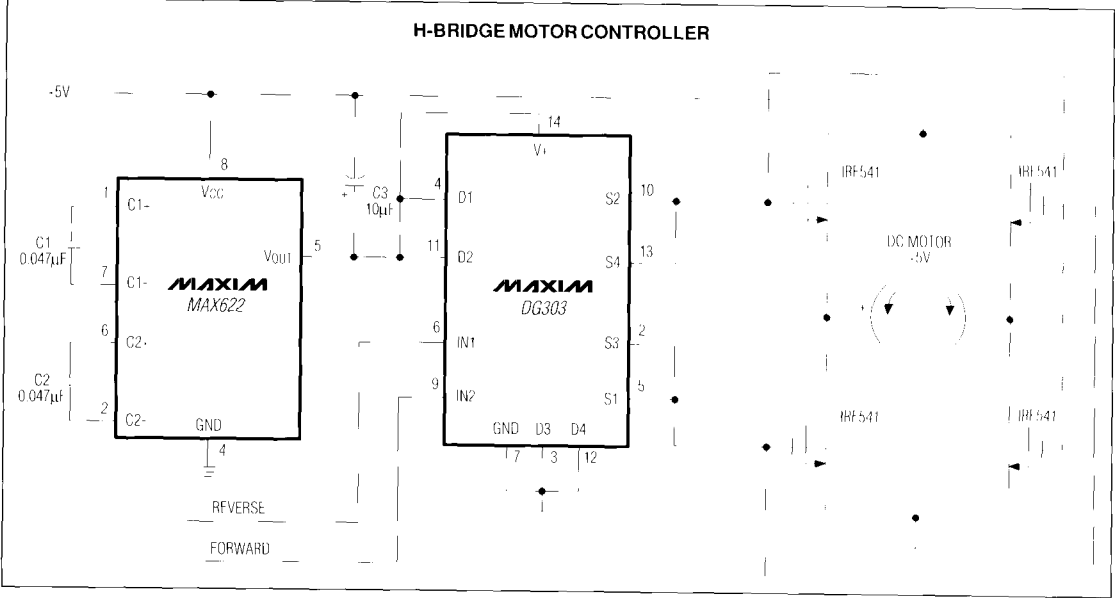


Figure 5. H-Bridge Motor Controller

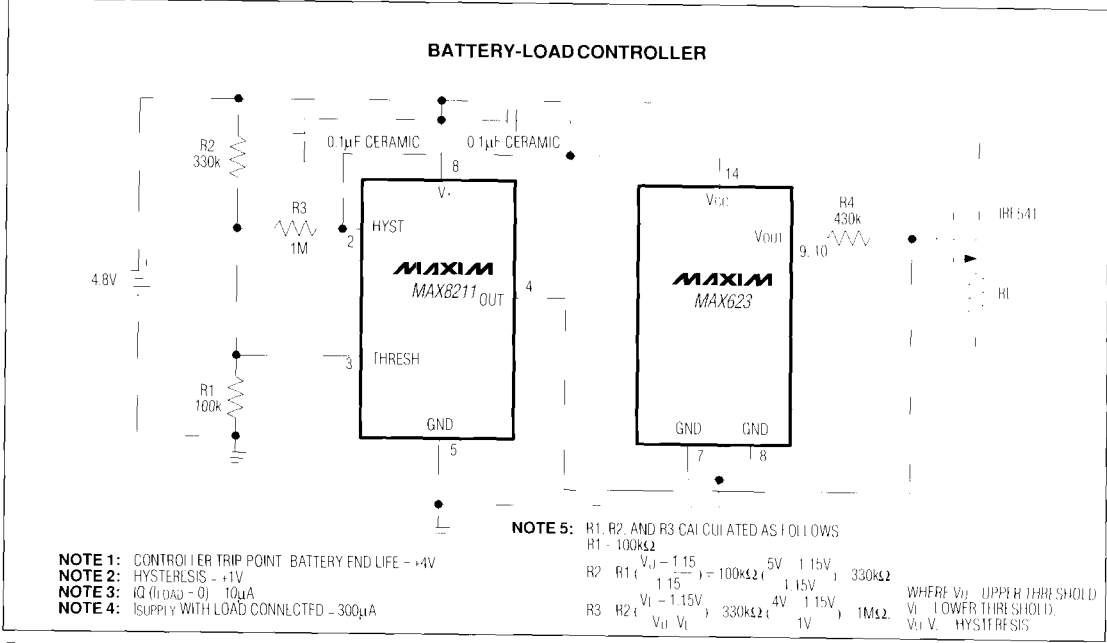
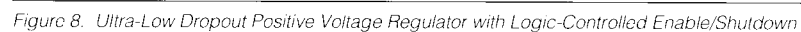
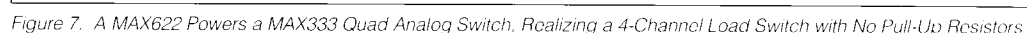


Figure 6. Battery-Load Controller Prevents Excessive Load at Battery End-of-Life

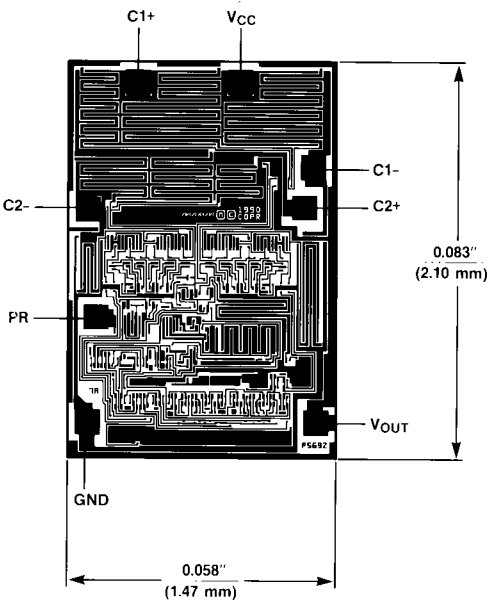
MAX622/MAX623



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Chip Topography



NOTE: Connect substrate to VOUT.

MAX622/MAX623 Transistor Count: 158

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