General Description

The MAX809/MAX810 are microprocessor (µP) supervisory circuits used to monitor the power supplies in µP and digital systems. They provide excellent circuit reliability and low cost by eliminating external components and adjustments when used with 5V-powered or 3Vpowered circuits.

These circuits perform a single function: They assert a reset signal whenever the V_{CC} supply voltage declines below a preset threshold, keeping it asserted for at least 140ms after V_{CC} has risen above the reset threshold. The only difference between the two devices is that the MAX809 has an active-low RESET output (which is guaranteed to be in the correct state for V_{CC} down to 1V), while the MAX810 has an active-high RESET output. The reset comparator is designed to ignore fast transients on V_{CC} . Reset thresholds suitable for operation with a variety of supply voltages are available.

Low supply current makes the MAX809/MAX810 ideal for use in portable equipment. The MAX809/MAX810 come in a 3-pin SOT23 package.

Applications

Computers

Controllers

Intelligent Instruments

Critical µP and µC Power Monitoring

Portable/Battery-Powered Equipment

Automotive



Typical Operating Circuit

Precision Monitoring of 3V, 3.3V, and 5V

Features

- Fully Specified Over Temperature
- 140ms Min Power-On Reset Pulse Width: **RESET** Output (MAX809) **RESET Output (MAX810)**
- ♦ 17µA Supply Current

Power-Supply Voltages

- Guaranteed RESET Valid to V_{CC} = 1V (MAX809)
- Power Supply Transient Immunity
- No External Components
- ♦ 3-Pin SOT23 Package

Ordering Information

	_	
PART†	TEMP. RANGE	PIN-PACKAGE
MAX809_EUR-T	-40°C to +105°C	3 SOT23-3
MAX810_EUR-T	-40°C to +105°C	3 SOT23-3

Ordering Information continued at end of data sheet.

†Insert the desired suffix letter (from the table below) into the blank to complete the part number:

SUFFIX	RESET THRESHOLD (V)
L	4.63
М	4.38
J*	4.00
Т	3.08
S	2.93
R	2.63

J version is available for MAX809 only.

Pin Configuration



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ABSOLUTE MAXIMUM RATINGS

Terminal Voltage (with respect to GND)	Continuous Power Dissipation ($T_A = +70^{\circ}C$)
V _{CC} 0.3V to 6.0V	SOT23 (derate above +70°C by 4mW/°C)
RESET, RESET0.3V to (V _{CC} + 0.3V)	Operating Temperature Range40°C to +105°C
Input Current, V _{CC} 20mA	Storage Temperature Range65°C to +160°C
Output Current, RESET, RESET	Lead Temperature (soldering, 10sec)+300°C
Rate of Rise, V _{CC} 100V/µs	

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V_{CC} = full range, T_A = -40°C to +105°C, unless otherwise noted. Typical values are at T_A = +25°C, V_{CC} = 5V for L/M/J versions, V_{CC} = 3.3V for T/S versions, and V_{CC} = 3V for R version.) (Note 1)

PARAMETER	SYMBOL		CONDITIONS	MIN	ТҮР	MAX	UNITS
Maa Dongo		$T_{A} = 0^{\circ}C \text{ to } + 70^{\circ}$	°C	1.0		5.5	
V _{CC} Range		T _A = -40°C to +105°C		1.2		5.5	- V
		T _A = -40°C to +85°C	Vcc < 5.5V, MAX8L/M		24	60	μΑ
Current Current	Icc		V _{CC} < 3.6V, MAX8 R/S/T		17	50	
Supply Current		$T_A = +85^{\circ}C$ to	Vcc < 5.5V, MAX8 L/M			100	
		+105°C	V _{CC} < 3.6V, MAX8 R/S/T			100	
			$T_A = +25^{\circ}C$	4.56	4.63	4.70	-
		MAX8L	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	4.50		4.75	
			$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	4.40		4.86	
			$T_A = +25^{\circ}C$	4.31	4.38	4.45	
		MAX8M	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	4.25		4.50	
		$T_{A} = +85^{\circ}C \text{ to } +105^{\circ}$		4.16		4.56	-
		$T_{A} = +25^{\circ}C$		3.93	4.00	4.06	
	VTH	MAX809J	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	3.89		4.10	- V
Reset Threshold (Note 2)			$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	3.80		4.20	
Reset mileshold (Note Z)			$T_A = +25^{\circ}C$	3.00	3.08	3.15	
		MAX8T	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	3.04		3.11	
			$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	2.92		3.23	
			$T_A = +25^{\circ}C$	2.89	2.93	2.96	
		MAX8S	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	2.85		3.00	
			$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	2.78		3.08	
			$T_A = +25^{\circ}C$	2.59	2.63	2.66	
		MAX8R	$T_{A} = -40^{\circ}C \text{ to } +85^{\circ}C$	2.55		2.70	
			$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	2.50		2.76	
Reset Threshold Tempco					30		ppm/°C
V _{CC} to Reset Delay (Note 2)		$V_{CC} = V_{TH}$ to (V-	гн - 100mV)		20		μs
Reset Active Timeout Period		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$		140	240	560	ms
		$T_{A} = +85^{\circ}C \text{ to } +105^{\circ}C$		100		840	
		V _{CC} = V _{TH} min, I _{SINK} = 1.2mA, MAX809R/S/T				0.3	
RESET Output Voltage Low (MAX809)	Vol	V _{CC} = V _{TH} min, I _{SINK} = 3.2mA, MAX809L/M				0.4	
(V _{CC} > 1.0V, I _{SINK} = 50µA				0.3	

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ELECTRICAL CHARACTERISTICS (continued)

 $(V_{CC} = full range, T_A = -40^{\circ}C \text{ to } +105^{\circ}C, \text{ unless otherwise noted.}$ Typical values are at $T_A = +25^{\circ}C$, $V_{CC} = 5V$ for L/M/J versions, $V_{CC} = 3.3V$ for T/S versions, and $V_{CC} = 3V$ for R version.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
RESET Output Voltage High	Vон	V _{CC} > V _{TH} max, I _{SOURCE} = 500µA, MAX809R/S/T	0.8V _{CC}			V
(MAX809)		V _{CC} > V _{TH} max, I _{SOURCE} = 800µA, MAX809L/M	Vcc - 1.5			V
RESET Output Voltage Low	Vol	V _{CC} = V _{TH} max, I _{SINK} = 1.2mA, MAX810R/S/T			0.3	v
(MAX810)		V _{CC} = V _{TH} max, I _{SINK} = 3.2mA, MAX810L/M			0.4	7 V
RESET Output Voltage High (MAX810)	Voh	1.8V < V _{CC} < V _{TH} min, I _{SOURCE} = 150µA	0.8VCC			V

Note 1: Production testing done at $T_A = +25^{\circ}$ C, over temperature limits guaranteed by design only Note 2: RESET Output for MAX809, RESET Output for MAX810.

Typical Operating Characteristics



PIN	NAME	FUNCTION			
1	GND	Ground			
2	RESET (MAX809)	RESET Output remains low while V_{CC} is below the reset threshold, and for 240ms after V_{CC} rises above the reset threshold.			
۷	RESET (MAX810)	RESET Output remains high while V_{CC} is below the reset threshold, and for 240ms after V_{CC} rises above the reset threshold.			
3	Vcc	Supply Voltage (+5V, +3.3V, or +3.0V)			

Pin Description

Applications Information

Negative-Going Vcc Transients

In addition to issuing a reset to the microprocessor (μ P) during power-up, power-down, and brownout conditions, the MAX809/MAX810 are relatively immune to short duration negative-going V_{CC} transients (glitches).

Figure 1 shows typical transient duration vs. reset comparator overdrive, for which the MAX809/MAX810 do not generate a reset pulse. The graph was generated using a negative-going pulse applied to V_{CC}, starting 0.5V above the actual reset threshold and ending below it by the magnitude indicated (reset comparator overdrive). The graph indicates the maximum pulse width a negative-going V_{CC} transient can have without causing a reset pulse. As the magnitude of the transient increases (goes farther below the reset threshold), the maximum allowable pulse width decreases. Typically, for the MAX809L and MAX810M, a V_{CC} transient that goes 100mV below the reset threshold and lasts 20µs or less will not cause a reset pulse. A 0.1µF bypass capacitor mounted as close as possible to the V_{CC} pin provides additional transient immunity.



Figure 1. Maximum Transient Duration without Causing a Reset Pulse vs. Reset Comparator Overdrive

Ensuring a Valid Reset Output Down to V_{CC} = 0V

When V_{CC} falls below 1V, the MAX809 RESET output no longer sinks current—it becomes an open circuit. Therefore, <u>high-impedance</u> CMOS logic inputs connected to RESET can drift to undetermined voltages. This presents no problem in most applications, since most μ P and other circuitry is inoperative with V_{CC} below 1V. However, in applications where RESET must be valid down to 0V, adding a pull-down resistor to RESET causes any stray leakage currents to flow to ground, holding RESET low (Figure 2). R1's value is not critical; 100k Ω is large enough not to load RESET and small enough to pull RESET to ground.

A 100k Ω pull-up resistor to V_{CC} is also recommended for the MAX810, if RESET is required to remain valid for V_{CC} < 1V.



Figure 2. RESET Valid to V_{CC} = Ground Circuit

Interfacing to µPs with Bidirectional Reset Pins

 μPs with bidirectional reset pins (such as the Motorola 68HC11 series) can connect to the MAX809 reset output. If, for example, the MAX809 RESET output is asserted high and the μP wants to pull it low, indeterminate logic levels may result. To correct this, connect a 4.7k Ω resistor between the MAX809 RESET output and the μP reset I/O (Figure 3). Buffer the MAX809 RESET output to other system components.

Benefits of Highly Accurate Reset Threshold

Most μ P supervisor ICs have reset threshold voltages between 5% and 10% below the value of nominal supply voltages. This ensures a reset will **not** occur within 5% of the nominal supply, but **will** occur when the supply is 10% below nominal.



Figure 3. Interfacing to µPs with Bidirectional Reset I/O

When using ICs rated at only the nominal supply $\pm 5\%$, this leaves a zone of uncertainty where the supply is between 5% and 10% low, and where the reset may or may not be asserted.

The MAX809L/T and MAX810L/T use highly accurate circuitry to ensure that reset is asserted close to the 5% limit, and long before the supply has declined to 10% below nominal.

Chip Information

TRANSISTOR COUNT: 275



MAX809J/L/M/R/S/T, MAX810L/M/R/S/T

Package Information



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8

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