

### **General Description**

The MAX9614/MAX9616 are low-power precision op amps that feature precision MOS inputs.

These devices are ideal for a large number of signal processing applications such as photodiode transimpedance amplifiers and filtering/amplification of a wide variety of signals in industrial equipment. The devices also feature excellent RF immunity, making them ideal for portable applications.

The MAX9614/MAX9616 are capable of operating from a 2.5V to 5.5V supply voltage over the -40°C to +125°C automotive temperature range.

Both singles and duals are available in tiny SC70 packages. The MAX9614 features an active-low shutdown pin.

#### Applications

Notebooks, Portable Media Players Industrial and Medical Sensors General Purpose Signal Processing

#### Features

- ◆ Vcc = 2.5V to 5.5V (-40°C to +125°C)
- Low 100µV (max) Vos
- ♦ 1µA Supply Current in Shutdown, 175µA Operating
- Small SC70 Package
- 2.8MHz Bandwidth
- Excellent RF Immunity

#### **Ordering Information**

PART	TEMP RANGE	PIN- PACKAGE	TOP MARK	
MAX9614AXT+T	-40°C to +125°C	6 SC70	+ADL	
MAX9616AXA+T	-40°C to +125°C	8 SC70	+AAE	

+Denotes lead(Pb)-free/RoHS-compliant package. T = Tape and reel.



### **Typical Application Circuit**

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For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

### **ABSOLUTE MAXIMUM RATINGS**

IN+, IN-, SHDN, VCC to GND	0.3V to +6V
OUT to GND	0.3V to V <sub>CC</sub> + 0.3V
Short-Circuit (GND) Duration, OUT, OU	ITA, OUTB5s
Continuous Input Current (any pin)	±20mA
Thermal Limits (Note 1) Multilayer PCB	
Continuous Power Dissipation ( $T_A = +7$	70°C)
6-Pin SC70 (derate 3.1mW/°C above	+70°C)245mW
θJA	326.5°C/W
θJC	115°C/W

8-Pin SC70 (derate 3.1mW/°C above +70°C)	)245mW
θυα	326°C/W
θJC	115°C/W
Operating Temperature Range	-40°C to +125°C
Junction Temperature	+150°C
Lead Temperature (soldering, 10s)	+300°C
Soldering Temperature (reflow)	+240°C

Note 1: Package thermal resistances were obtained using the method described in JEDEC specification JESD51-7, using a 4-layer board. For detailed information on package thermal considerations, refer to <u>www.maxim-ic.com/thermal-tutorial</u>.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### **ELECTRICAL CHARACTERISTICS**

 $(V_{CC} = V_{\overline{SHDN}} = 3.3V, V_{IN+} = V_{IN-} = V_{CM} = GND, R_L = 10k\Omega$  to  $V_{CC}/2, T_A = -40^{\circ}C$  to  $+125^{\circ}C$ . Typical values are at  $T_A = +25^{\circ}C$ , unless otherwise noted.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
DC CHARACTERISTICS						
Input Voltage Range	V <sub>IN+</sub> , V <sub>IN-</sub>	Guaranteed by CMRR test	-0.1		VCC - 1.4	V
		$T_A = +25^{\circ}C$		17	100	
Input Offset Voltage	Vos	$T_A = -40^{\circ}C$ to $+125^{\circ}C$ , after power-up autocalibration			165	μV
		$T_{A} = -40^{\circ}C \text{ to } +125^{\circ}C$			750	1
Input Offset Voltage Drift	Vos - TC			1	7.5	µV/°C
		$T_A = -40^{\circ}C \text{ to } +25^{\circ}C$		1	1.55	рА
Input Bias Current (Note 3)		$T_A = +70^{\circ}C$			45	
Input bias Current (Note 3)	ΙB	$T_A = +85^{\circ}C$			135	
		$T_{A} = +125^{\circ}C$			1.55	nA
	los	$T_A = -40^{\circ}C$ to $+25^{\circ}C$			0.5	рА
Input Offect Current (Note 2)		$TA = +70^{\circ}C$			7	
Input Offset Current (Note 3)		$T_A = +85^{\circ}C$			25	
		$T_{A} = +125^{\circ}C$			4000	
		$V_{CM} = -0.1V$ to $V_{CC} - 1.4V$ , $T_A = +25^{\circ}C$	80	95		
Common-Mode Rejection Ratio	CMRR	$V_{CM} = -0.1V$ to $V_{CC} - 1.4V$ , $T_A = -40^{\circ}C$ to $+125^{\circ}C$	78			dB
	A 01	$+0.4V \le V_{OUT} \le V_{CC} - 0.4V, R_L = 10k\Omega$	99	115		dD
Open-Loop Gain	AOL	$+0.4V \le V_{OUT} \le V_{CC} - 0.4V, R_L = 600\Omega$	93	110		- dB
Output Short-Circuit Current		To VCC		275		
(Note 4)	ISC	To GND		75		mA
		$R_L = 10k\Omega$		1	11	
Output Voltage Low	VOL	$R_L = 600\Omega$		11	100	mV
		$R_L = 32\Omega$		170		]

### ELECTRICAL CHARACTERISTICS (continued)

 $(V_{CC} = V_{SHDN} = 3.3V, V_{IN+} = V_{IN-} = V_{CM} = G_{ND}, R_L = 10k\Omega$  to  $V_{CC}/2, T_A = -40^{\circ}C$  to  $+125^{\circ}C$ . Typical values are at  $T_A = +25^{\circ}C$ , unless otherwise noted.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS
		$R_L = 10k\Omega$	V <sub>CC</sub> - 11	V <sub>CC</sub> - 2		
Output Voltage High	Voh	$R_L = 600\Omega$	V <sub>CC</sub> - 100	V <sub>CC</sub> - 30		mV
		$R_L = 32\Omega$		V <sub>CC</sub> - 560		
AC CHARACTERISTICS	·					
Input Voltage Noise Density	en	f = 10 kHz		28		nV/√Hz
Input Voltage Noise	Total noise	$0.1Hz \le f \le 10Hz$		5		μVp-p
Input Current Noise Density	In	f = 10 kHz		0.1		fA/√Hz
Gain Bandwidth	GBW			2.8		MHz
Slew Rate	SR			1.3		V/µs
Capacitive Loading	CLOAD	No sustained oscillation		200		pF
Total Harmonic Distortion	THD	$f = 10kHz$ , $V_{OUT} = 2V_{P-P}$ , $A_V = 1V/V$		-85		dB
POWER-SUPPLY CHARACTERI	STICS					
Power-Supply Range	Vcc	Guaranteed by PSRR	2.5		5.5	V
Power-Supply Rejection Ratio	PSRR	$T_A = +25^{\circ}C$	85	106		- dB
rower-supply hejection hallo	Fonn	$T_A = -40^{\circ}C \text{ to } + 125^{\circ}C$	83			
Quiescent Current	laa	$T_A = +25^{\circ}C$ , per amplifier	mplifier 170 255	255	- μΑ	
	Icc	$T_A = -40^{\circ}C$ to $+125^{\circ}C$ , per amplifier		350		
Shutdown Supply Current	ISHDN	MAX9614 only			1	μA
Shutdown Input Low	VIL	MAX9614 only			0.5	V
Shutdown Input High	VIH	MAX9614 only	1.4			V
Output Impedance in Shutdown	ROUT_SHDN	MAX9614 only		10		MΩ
Turn-On Time from SHDN	ton	MAX9614 only		20		μs
Power-Up Time	tup			10		ms

**Note 2:** All devices are 100% production tested at  $T_A = +25$ °C. Temperature limits are guaranteed by design. **Note 3:** Guaranteed by design, not production tested.

Note 4: Do not exceeed package thermal dissipation in the Absolute Maximum Ratings section.



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### Typical Operating Characteristics (continued)

 $(V_{CC} = 3.3V, V_{IN+} = V_{IN-} = 0V, V_{CM} = V_{CC}/2, R_L = 10k\Omega$  to  $V_{CC}/2$ , values are at  $T_A = +25^{\circ}C$ , unless otherwise noted.)



MAX9614/MAX9616

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MAX9614/MAX9616





#### **Pin Description**

PIN			FUNCTION	
MAX9614	MAX9616	NAME	FUNCTION	
1	_	IN+	Positive Input	
_	3	INA+	Positive Input A	
_	5	INB+	Positive Input B	
2	4	GND	Ground	
3	_	IN-	Negative Input	
_	2	INA-	Negative Input A	
_	6	INB-	Negative Input B	
4	_	OUT	Output	
_	1	OUTA	Output A	
_	7	OUTB	Output B	
5	_	SHDN	Active-Low Shutdown	
6	8	Vcc	Positive Power Supply. Bypass with a 0.1µF capacitor to ground.	

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### \_Detailed Description

The MAX9614/MAX9616 are low-power op amps ideal for signal processing applications due to the devices' high precision and CMOS inputs.

The MAX9614 also features a low-power shutdown mode that greatly reduces quiescent current while the device is not operational.

The MAX9614/MAX9616 self-calibrate on power-up to eliminate effects of temperature and power-supply variation.

#### **RF Immunity**

The MAX9614/MAX9616 feature robust internal EMI filters that reduce the devices' susceptibility to high-frequency RF signals such as from wireless and mobile devices. This, combined with excellent DC and AC specifications, makes these devices ideal for a wide variety of portable audio and sensitive signal-conditioning applications.

#### **Applications Information**

#### **Power-Up Autotrim**

The MAX9614/MAX9616 feature an automatic power-up autotrim that self-calibrates the VOS of these devices to less than  $100\mu$ V of input offset voltage. The autotrim sequence takes approximately 10ms to complete, and is triggered by an internal power-on reset (POR) circuitry. During this time, the inputs and outputs are put into high impedance and left unconnected. This self-calibration feature allows the device to eliminate input offset voltage effects due to power supply and operating temperature variation simply by cycling its power.

Take care to ensure that the power supply settles within 0.4ms of power-up after it crosses a POR threshold of 0.5V to ensure that a stable power supply is present when it steps through its autotrim sequence. If the power supply glitches below the 0.5V threshold, the POR circuitry reactivates during next power-up.

#### **Shutdown Operation**

The MAX9614 features an active-low shutdown mode that puts both inputs and outputs into a high-impedance state. In this mode, the quiescent current is less than 1 $\mu$ A. Putting the output in high-impedance allows multiple signal outputs to be multiplexed onto a single output line without the additional external buffers. The device

does not self-calibrate when exiting shutdown mode, and retains its power-up trim settings. The device also instantly recovers from shutdown.

The shutdown logic levels of the device are independent of supply allowing the shutdown to operate by either a 1.8V or 3.3V microcontroller.

#### Interfacing with the MAX11613

The MAX9616 dual amplifier's low power and tiny size is ideal for driving multichannel analog-to-digital converters (ADCs) such as the MAX11613 (see the *Typical Application Circuit*). The MAX11613 is a low-power, 12-bit I<sup>2</sup>C ADC that measures either four single-ended or two differential channels in an 8-pin µMAX<sup>®</sup> package. Operating from a single 3V or 3.3V supply, the MAX11613 draws a low 380µA supply current when sampling at 10ksps.The MAX11613 family also offers pincompatible 5V ADCs (MAX11612) and 8-bit (MAX11601) and 10-bit (MAX11607) options.

The MAX9614/MAX9616's output voltage low is designed to be especially close to ground—it is only 11mV above ground, allowing maximum dynamic range in single-supply applications. High output current and capacitance drive capability of the part help it to be useful in ADC driver and line-driver.



Figure 1. Autotrim Timing Diagram

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#### **Active Filters**

### Input Bias Current

The MAX9614/MAX9616 feature a high-impedance CMOS input stage and a specialized ESD structure that allows low input bias current operation at low input common-mode voltages. Low input bias current is useful when interfacing with high-ohmic sensors. It is also beneficial for designing transimpedance amplifiers for photodiode sensors. This makes the MAX9614/MAX9616 ideal for ground referenced medical and industrial sensor applications.

The MAX9614/MAX9616 are ideal for a wide variety of active filter circuits that make use of their rail-to-rail output stages and high impedance CMOS inputs. The *Typical Application Circuit* shows an example multiple feedback active filter circuit with a corner frequency of 1.3kHz. At low frequencies, the amplifier behaves like a simple low-distortion inverting amplifier of gain = -1, while its high bandwidth gives excellent stopband attenuation above its corner frequency. See the *Typical Application Circuit*.

**Chip Information** 

PROCESS: BICMOS

### **Package Information**

For the latest package outline information and land patterns, go to <u>www.maxim-ic.com/packages</u>. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
6 SC70	X6SN-1	<u>21-0077</u>	<u>90-0189</u>
8 SC70	X8SN-1	<u>21-0460</u>	—



### Package Information (continued)

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REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	8/10	Initial release	_

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**Revision History**