Triple 2-input exclusive OR/Exclusive NOR

This three gate array is designed to provide the positive logic Exclusive OR and Exclusive NOR functions in high speed applications. Input pulldown resistors eliminate the need to tie unused inputs to V_{EE}.

$$3 = (4 \cdot \overline{5}) + (\overline{4} \cdot 5)$$
$$2 = (\overline{4} \cdot \overline{5}) + (4 \cdot 5)$$

$V_{CC1} = $ Pin 1
$V_{CC2} = $ Pin 16
$V_{EE} = $ Pin 8

$P_D = 40$ mW typ/gate (No Load)
$t_{pd} = 2.5$ ns typ