



V_{CC} = Gnd = Pins 1,16
 V_{EE} = Pin 8 = $-5.2 \text{ Vdc} \pm 5\%$
 V_{SS} = Pin 9 ($+5.0 \text{ Vdc}$ or $+6.0 \text{ Vdc} \pm 10\%$)

Max Load: 350 pF
 $P_D = 1.0 \text{ W typ/pkg}$ ($@ 5.0 \text{ MHz}$)
 Operating Rate: 5.0 MHz typ.
 (all 3 translators in use simultaneously)

Triple MECL to NMOS Translator

The MC10177 consists of three MECL to MOS translators which convert MECL 10,000 logic levels to NMOS levels. It is designed for use in N-channel memory systems as a Read/Write, Data/Address driver. It may also be used as a high fanout (30) MECL to TTL translator, or in other applications requiring the capability to drive high capacitive loads. A separate lead from each of the three translators is brought out of the package. These leads may be connected to V_{SS} or to an external capacitor (0.01 to 0.05 μF to ground), for waveform improvement, and short circuit protection. When connection is made to an external capacitor, V_{SS} line fluctuations due to transient currents are also reduced.