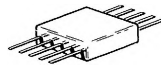


DIFFERENTIAL COMPARATOR

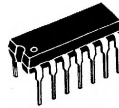
SENSE AMPLIFIERS

MC1710C

... designed for use in level detection, low-level sensing, and memory applications.



CASE 72
(TO-91)
"F" SUFFIX



CASE 93
(TO-116)
"P" SUFFIX

Lead 4 connected to case



CASE 96
(TO-99)
"G" SUFFIX

Typical Amplifier Features:

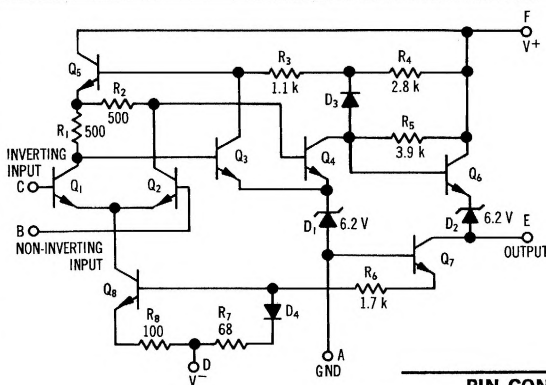
- Differential Input Characteristics:
Input Offset Voltage = 1.5 mV
Offset Voltage Drift = 5.0 $\mu\text{V}/^\circ\text{C}$
- Fast Response Time – 40 ns
- Output Compatible with All Saturating Logic Forms
 $V_{\text{out}} = +3.2 \text{ V to } -0.5 \text{ V typical}$
- Low Output Impedance – 200 ohms

MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

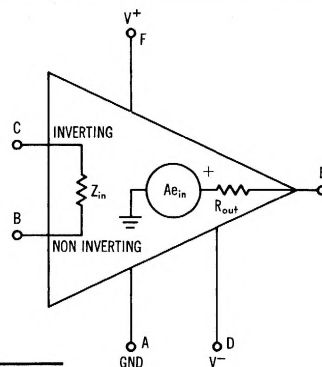
Rating	Symbol	Value	Unit
Power Supply Voltage	V^+ V^-	+14 -7.0	Vdc Vdc
Differential Input Signal	V_{in}	± 5.0	Volts
Common Mode Input Swing	CMV_{in}	± 7.0	Volts
Peak Load Current	I_L	10	mA
Power Dissipation (package limitation)	P_D		
Metal Can		680	mW
Derate above 25°C		4.6	mW/°C
Flat Package		500	mW
Derate above 25°C		3.3	mW/°C
Plastic Package		400	mW
Derate above 25°C		3.3	mW/°C
Operating Temperature Range*	T_A	0 to +75	°C
Storage Temperature Range	T_{stg}		°C
Metal Can and Flat Package		-65 to +150	
Plastic Package		-65 to +125	

*For full temperature range (-55°C to +125°C) and characteristic curves, see MC1710 data sheet.

CIRCUIT SCHEMATIC



EQUIVALENT CIRCUIT

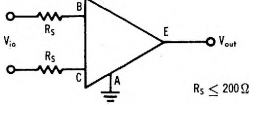
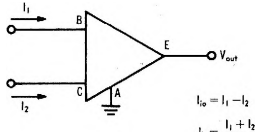
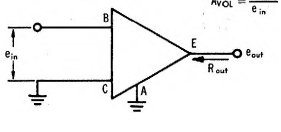
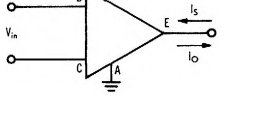
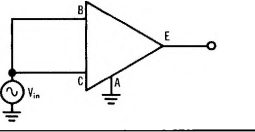
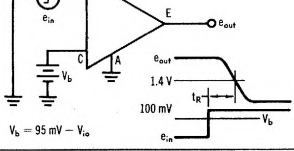
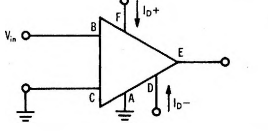


PIN CONNECTIONS

Schematic	A	B	C	D	E	F
"G" Package	1	2	3	4	7	8
"F" Package	1	2	3	5	6	8
"P" Package	2	3	4	6	9	11

MC1710C (continued)

ELECTRICAL CHARACTERISTICS (V⁺ = +12 Vdc, V⁻ = -6 Vdc, T_A = 25°C unless otherwise noted)

Characteristic Definitions	Characteristic	Symbol	Min	Typ	Max	Unit
 <p>$R_S \leq 200 \Omega$</p>	Input Offset Voltage V _{out} = 1.4 Vdc, T _A = 25°C V _{out} = 1.5 Vdc, T _A = 0°C V _{out} = 1.2 Vdc, T _A = -70°C	V _{io}	-	1.5	5.0	mVdc
	Temperature Coefficient of Input Offset Voltage	TC _{V_{io}}	-	5.0	-	μV/°C
 <p>$I_{io} = I_1 - I_2$ $I_b = \frac{I_1 + I_2}{2}$</p>	Input Offset Current V _{out} = 1.4 Vdc, T _A = 25°C V _{out} = 1.5 Vdc, T _A = 0°C V _{out} = 1.2 Vdc, T _A = -70°C	I _{io}	-	1.0	5.0	μAdc
	Input Bias Current V _{out} = 1.4 Vdc, T _A = 25°C V _{out} = 1.5 Vdc, T _A = 0°C V _{out} = 1.2 Vdc, T _A = -70°C	I _b	-	15	25	μAdc
 <p>$A_{VOL} = \frac{e_{out}}{e_{in}}$</p>	Voltage Gain T _A = 25°C T _A = 0 to +70°C	A _{VOL}	1000	1500	-	V/V
	Output Resistance	R _{out}	-	200	-	ohms
	Differential Voltage Range	V _{in}	±5.0	-	-	Vdc
	Positive Output Voltage V _{in} ≥ 5.0 mV, 0 ≤ I _o ≤ 0.5 mA	V _{OH}	2.5	3.2	4.0	Vdc
	Negative Output Voltage V _{in} ≥ -5.0 mV	V _{OL}	-1.0	-0.5	0	Vdc
	Output Sink Current V _{in} ≥ -5.0 mV, V _{out} ≥ 0 T _A = 25°C T _A = 0°C	I _s	1.6	2.5	-	mAdc
			0.5	-	-	
	Input Common Mode Range V ⁻ = -7.0 Vdc	CMV _{in}	±5.0	-	-	Volts
	Common Mode Rejection Ratio R _o ≤ 200 Ω	CM _{rej}	70	100	-	dB
 <p>V_b = 95 mV - V_{io}</p>	Propagation Delay Time For Positive and Negative Going Input Pulse	t _{pd}	-	40	-	ns
	Power Supply Current V _{out} ≤ 0 Vdc	I _{D+} I _{D-}	-	6.4	9.0	mAdc
	Power Consumption		-	110	150	mW