

MC3410, MC3510, MC3410C

10-Bit High-Speed Multiplying D/A Converter

Linear Products

Product Specification

DESCRIPTION

The MC3410 series are 10-bit Multiplying Digital-to-Analog Converters. They are capable of high-speed performance, and are used as general-purpose building blocks in cost-effective D/A systems.

The Signetics' design provides complete 10-bit accuracy without laser trimming, and guaranteed monotonicity over temperature. Segmented current sources, in conjunction with an R-2R DAC provides the binary weighted currents. The output buffer amplifier and voltage reference have been omitted to allow greater speed, lower cost, and maximum user flexibility.

FEATURES

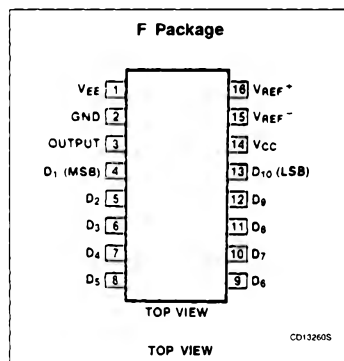
- 10-bit resolution and accuracy ($\pm 0.05\%$)
- Guaranteed monotonicity over temperature
- Fast settling time — 250ns typical

- Digital inputs are TTL and CMOS compatible
- Wide output voltage compliance range
- High-speed multiplying input slew rate — 20mA/ μ s
- Reference amplifier internally-compensated
- Standard supply voltages +5V and -15V

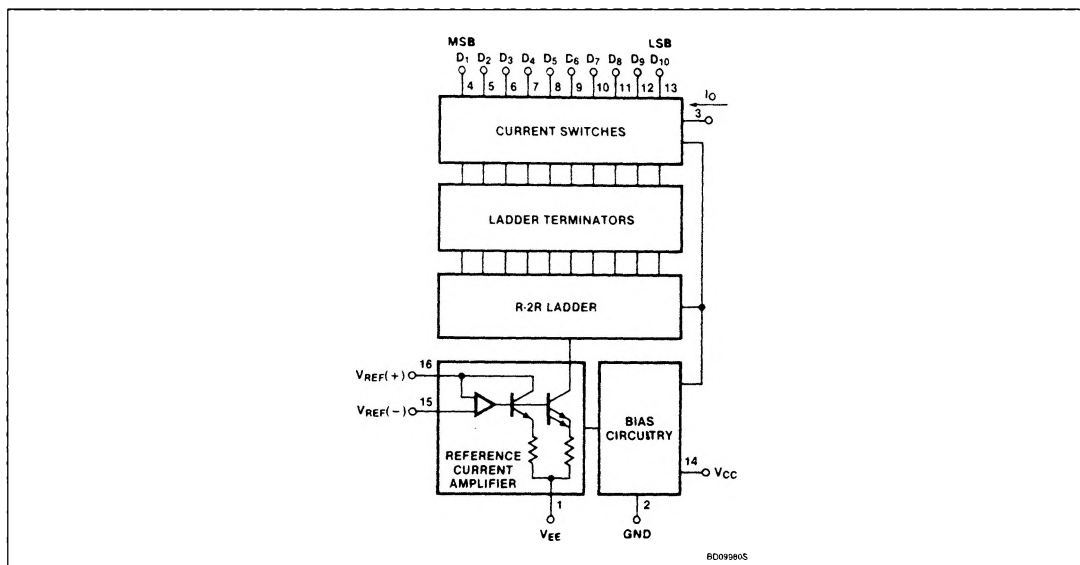
APPLICATIONS

- Successive approximation A/D converters
- High-speed, automatic test equipment
- High-speed modems
- Waveform generators
- CRT displays
- Strip CHART and X-Y plotters
- Programmable power supplies
- Programmable gain and attenuation

PIN CONFIGURATION



BLOCK DIAGRAM



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ORDERING INFORMATION

DESCRIPTION	TEMPERATURE RANGE	ORDER CODE
16-Pin Cerdip	0 to +70°C	MC3410F
16-Pin Cerdip	0 to +70°C	MC3410CF
16-Pin Cerdip	-55°C to +125°C	MC3510F

ABSOLUTE MAXIMUM RATINGS $T_A = +25^\circ\text{C}$ unless otherwise noted

SYMBOL	PARAMETER	RATING	UNIT
V_{CC} V_{EE}	Power supply	+7.0 -18	V_{DC} V_{DC}
V_I	Digital input voltage	+15	V_{DC}
V_O	Applied output voltage	0.5, -5.0	V_{DC}
$I_{REF(16)}$	Reference current	2.5	mA
V_{REF}	Reference amplifier inputs	V_{CC}, V_{EE}	V_{DC}
$V_{REF(D)}$	Reference amplifier differential inputs	0.7	V_{DC}
T_A	Operating ambient temperature range MC3510 MC3410, 3410C	-55 to +125 0 to +70	$^\circ\text{C}$ $^\circ\text{C}$
T_J	Junction temperature, ceramic package	+150	$^\circ\text{C}$
P_D	Maximum power dissipation, $T_A = 25^\circ\text{C}$ (still-air) ¹ F package	1190	mW

NOTE:

1. Derate above 25°C , at the following rates:
F package at $9.5\text{mW}/^\circ\text{C}$

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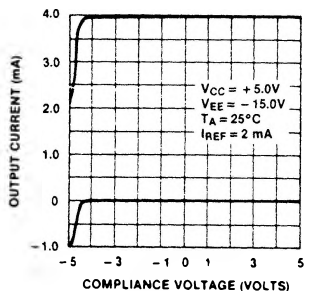
DC AND AC ELECTRICAL CHARACTERISTICS $V_{CC} = +5.0V_{DC}$, $V_{EE} = -15V_{DC}$, $\frac{V_{REF}}{R_{16}} = 2.0mA$, all digital inputs at high logic level.

MC3510: $T_A = -55^\circ C$ to $+125^\circ C$, MC3410 Series: $T_A = 0^\circ C$ to $+70^\circ C$, unless otherwise noted.

SYMBOL	PARAMETER	TEST CONDITIONS	MC3410, MC3510			MC3410C			UNIT
			Min	Typ	Max	Min	Typ	Max	
E_r	Relative accuracy (error relative to full-scale I_O)	$T_A = 25^\circ C$			± 0.05			± 0.1	%
					$\frac{1}{4}$			$\frac{1}{2}$	LSB
TCE_r	Relative accuracy drift (relative to full-scale I_O)			2.5			2.5		ppm/ $^\circ C$
	Monotonicity	Over temperature	10			10			Bits
t_s	Settling time to within $\pm \frac{1}{2}$ LSB (all bits LOW-to-HIGH)	$T_A = 25^\circ C$		250			250		ns
t_{PLH} t_{PHL}	Propagation delay time	$T_A = 25^\circ C$		35 20			35 20		ns
TCI_O	Output full scale current drift				60			70	ppm/ $^\circ C$
V_{IH}	Digital input logic levels (all bits) HIGH-level, Logic "1" LOW-level, Logic "0"		2.0		0.8	2.0		0.8	V_{DC}
I_{IH} I_{IL}	Digital input current (all bits) HIGH-level, $V_{IH} = 5.5V$ LOW-level, $V_{IL} = 0.8V$			-0.05	+0.04 -0.4		-0.05	+0.04 -0.4	mA
$I_{REF(15)}$	Reference input bias current (Pin 15)			-1.0	-5.0		-1.0	-5.0	μA
I_{OR}	Output current range			4.0	5.0		4.0	5.0	mA
I_{OH}	Output current (all bits high)	$V_{REF} = 2.000V$, $R_{16} = 1000\Omega$	3.8	3.996	4.2	3.8	3.996	4.2	mA
I_{OL}	Output current (all bits low)	$T_A = 25^\circ C$		0	2.0		0	4.0	μA
V_O	Output voltage compliance	$T_A = 25^\circ C$			-2.5 +0.2			-2.5 +0.2	V_{DC}
$SR I_{REF}$	Reference amplifier slew rate			20			20		mA/ μs
$ST I_{REF}$	Reference amplifier settling time	0 to 4.0mA, $\pm 0.1\%$		2.0			2.0		μs
$PSRR(-)$	Output current power supply sensitivity			0.003	0.01		0.003	0.02	%/%
C_O	Output capacitance	$V_O = 0$		25			25		pF
C_I	Digital input capacitance (all bits high)			4.0			4.0		pF
I_{CC} I_{EE}	Power supply current (all bits low)			-11.4	+18 -20		-11.4	+18 -20	mA
V_{CC} V_{EE}	Power supply voltage range	$T_A = 25^\circ C$	+4.75 -14.25	+5.0 -15	+5.25 -15.75	+4.75 -14.25	+5.0 -15	+5.25 -15.75	V_{DC}
	Power consumption (all bits low) (all bits high)			220 200	380		220 200	380	mW

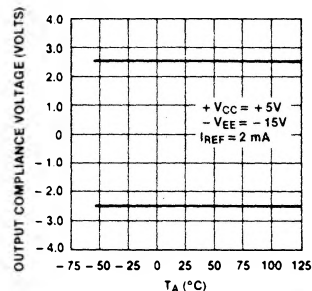
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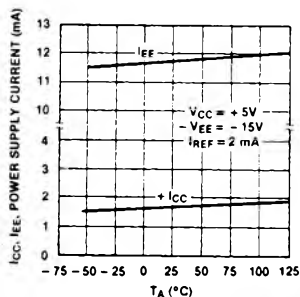
CP196305

Figure 1. Output Current vs Output Compliance Voltage



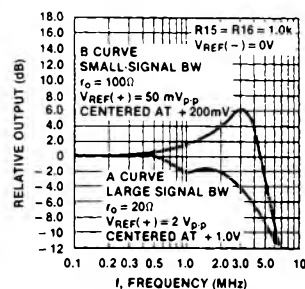
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Figure 2. Maximum Output Compliance Voltage vs Temperature



CP196505

Figure 3. Power Supply Current vs Temperature



CP196605

Figure 4. Reference Amplifier Frequency Response

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CIRCUIT DESCRIPTION

The MC3410 consists of four segment current sources which generate the two most significant bits (MSBs), and an R-2R DAC implemented with ion-implanted resistors for scaling the remaining eight least significant bits (LSBs) (See Figure 5). This approach provides complete 10-bit accuracy without trimming.

The individual bit currents are switched ON or OFF by fully differential current switches. The switches use current steering for speed.

An on-chip high-slew reference current amplifier drives the R-2R ladder and segment decoder. The currents are scaled in such a way that, with all bits on, the maximum output current is two times $1023/1024$ of the reference amplifier current, or nominally 3.996mA for a 2.000mA reference input current. The reference amplifier allows the user to provide a voltage input. Out-board resistor R_{16} (see Figure 6) converts this voltage to a usable current. A current mirror doubles this reference current and feeds it to the segment

decoder and resistor ladder. Thus, for a reference voltage of 2.0V and a $1\text{k}\Omega$ resistor tied to Pin 16, the full-scale current is approximately 4.0mA . This relationship will remain regardless of the reference voltage polarity.

Connections for a positive reference voltage are shown in Figure 6a. For negative reference voltage inputs, or for bipolar reference voltage inputs in the multiplying mode, R_{15} can be tied to a negative voltage corresponding to the minimum input level. For a negative reference input, R_{16} should be grounded (Figure 6b). In addition, the negative voltage reference must be at least 3V above the V_{EE} supply voltage for best operation. Bipolar input signals may be handled by connecting R_{16} to a positive voltage equal to the peak positive input level at Pin 15.

When a DC reference voltage is used, capacitive bypass to ground is recommended. The 5V logic supply is not recommended as a reference voltage. If a well regulated 5.0V supply, which drives logic, is to be used as the reference, R_{16} should be decoupled by

connecting it to the $+5.0\text{V}$ logic supply through another resistor and bypassing the junction of the two resistors with a $0.1\mu\text{F}$ capacitor to ground.

The reference amplifier is internally-compensated with a 10pF feed-forward capacitor, which gives it its high slew rate and fast settling time. Proper phase margin is maintained with all possible values of R_{16} and reference voltages which supply 2.0mA reference current into Pin 16. The reference current can also be supplied by a high impedance current source of 2.0mA . As R_{16} increases, the bandwidth of the amplifier decreases slightly and settling time increases. For a current source with a dynamic output impedance of $1.0\text{M}\Omega$, the bandwidth of the reference amplifier is approximately half what it is in the case of $R_{16} = 1.0\text{k}\Omega$, and settling time is $\approx 10\mu\text{s}$. The reference amplifier phase margin decreases as the current source value decreases in the case of a current source reference, so that the minimum reference current supplied from a current source is 0.5mA for stability.

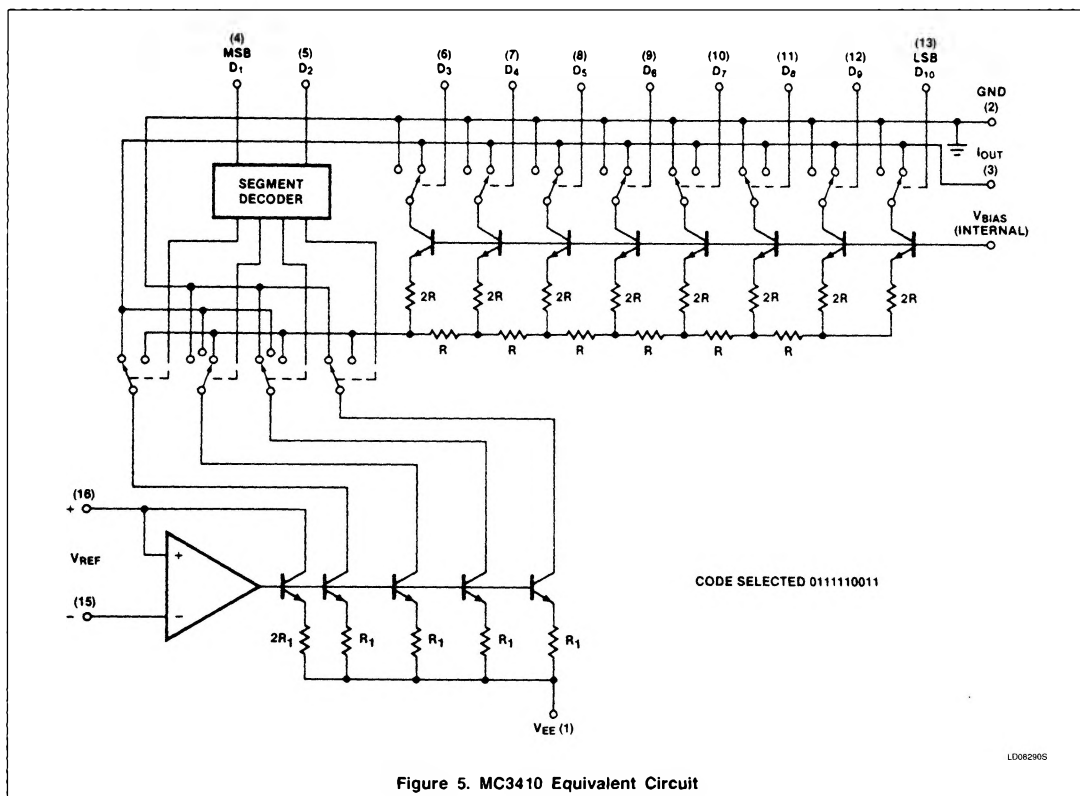
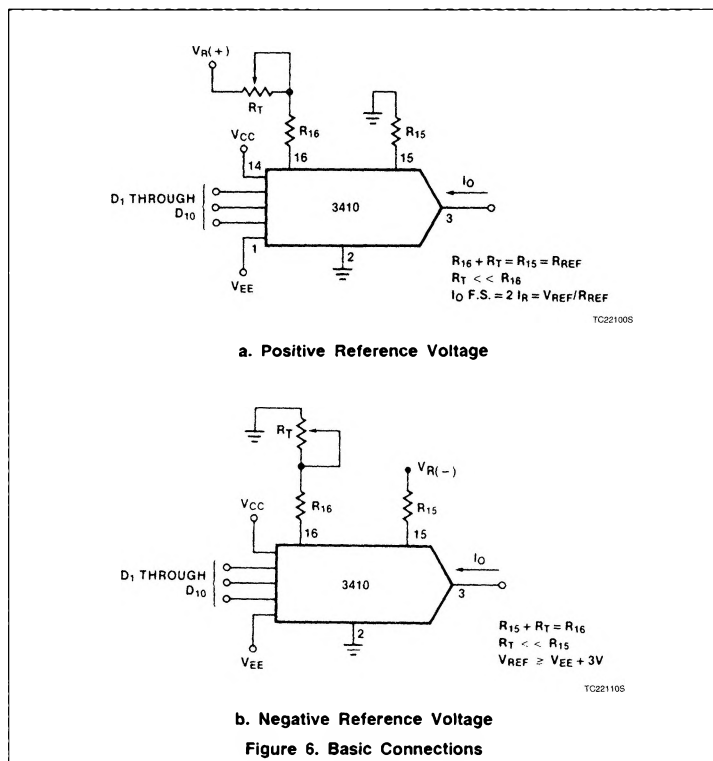


Figure 5. MC3410 Equivalent Circuit

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OUTPUT VOLTAGE COMPLIANCE

The output voltage compliance ranges from -2.5 to $+0.2V$. As shown in Figure 2, this compliance range is nearly constant over temperature. At the temperature extremes, however, the compliance voltage may be reduced if $V_{EE} > -15V$.

ACCURACY

Absolute accuracy is a measure of each output current level with respect to its intended

value. It is dependent upon relative accuracy and full-scale current drift. Relative accuracy, or linearity, is the measure of each output current with respect to its intended fraction of the full-scale current. The relative accuracy of the MC3410 is fairly constant over temperature due to the excellent temperature tracking of the implanted resistors. The full-scale current from the reference amplifier may drift with temperature causing a change in the absolute accuracy. However, the MC3410 has a low full-scale current drift with temperature.

The MC3510 and the MC3410 are accurate to within $\pm 0.05\%$ at $25^\circ C$ with a reference current of $2.0mA$ on Pin 16.

MONOTONICITY

The MC3410, MC3510 and MC3410C are guaranteed monotonic over temperature. This means that for every increase in the input digital code, the output current either remains the same or increases but never decreases. In the multiplying mode, where reference input current will vary, monotonicity can be assured if the reference input current remains above $0.5mA$.

SETTLING TIME

The worst-case switching condition occurs when all bits are switched "on," which corresponds to a low-to-high transition for all bits. This time is typically $250ns$ for the output to settle to within $\pm 1/2$ LSB for 10-bit accuracy, and $200ns$ for 8-bit accuracy. The turn-off time is typically $120ns$. These times apply when the output swing is limited to a small ($< 0.7V$) swing and the external output capacitance is under $25pF$.

The major carry (MSB off-to-on, all others on-to-off) settles in approximately the same time as when all bits are switched off-to-on.

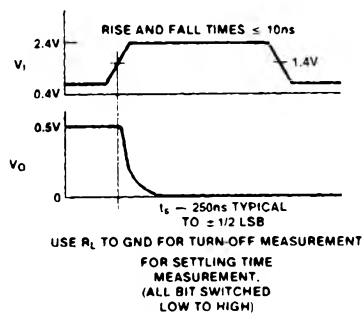
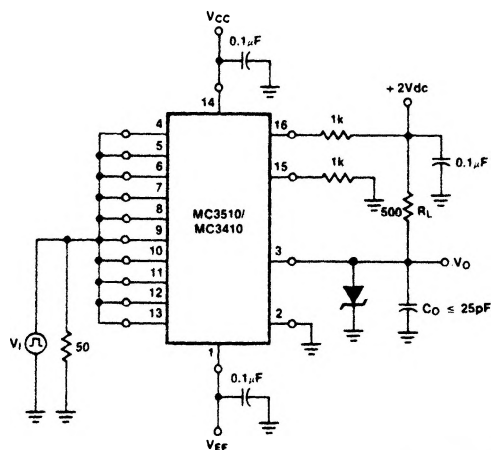
If a load resistor of 625Ω is connected to ground, allowing the output to swing to $-2.5V$, the settling time increases to $1.5\mu s$.

Extra care must be taken in board layout as this is usually the dominant factor in satisfactory test results when measuring settling time. Short leads, $100\mu F$ supply bypassing, and minimum scope lead length are all necessary.

A typical test setup for measuring settling time is shown in Figure 7. The same setup for the most part can be used to measure the slew rate of the reference amplifier (Figure 9) by tying all data bits high, pulsing the voltage reference input between 0 and $2V$, and using a 500Ω load resistor R_L .

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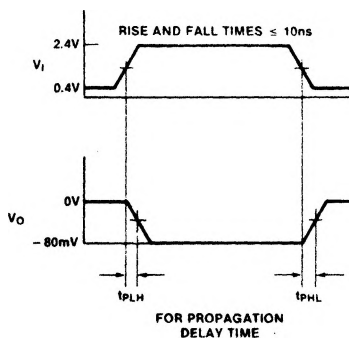
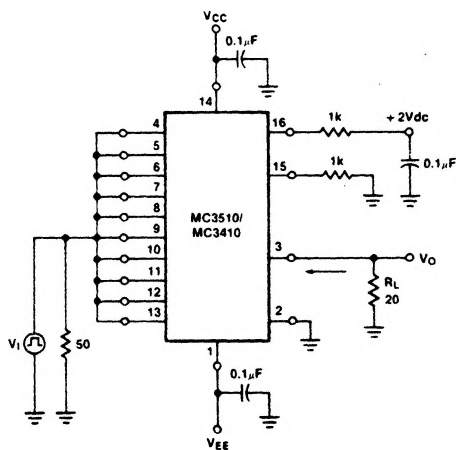
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WF211005

TC221305

Figure 7. Settling Time



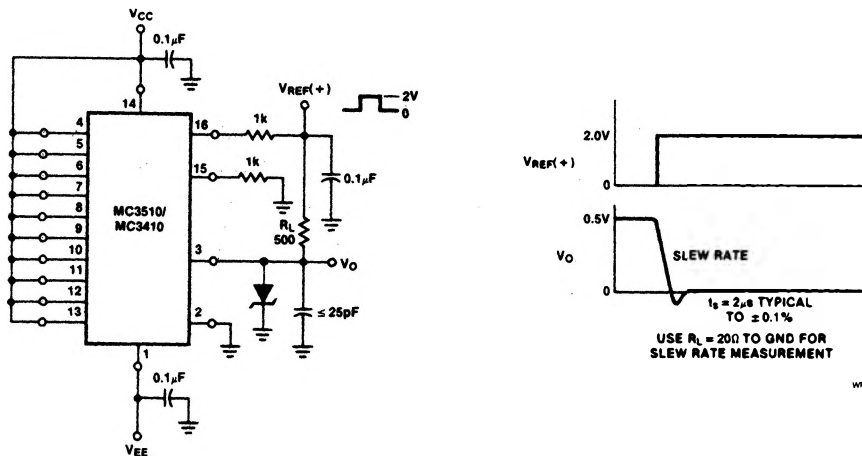
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Figure 8. Propagation Delay Time

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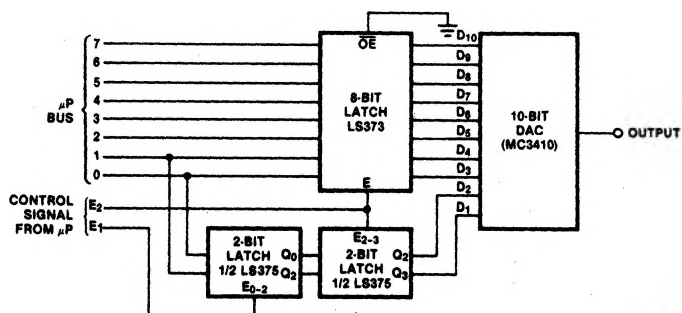


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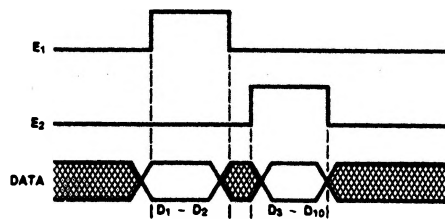
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Figure 9. Reference Amplifier Settling Time and Slew Rate

TYPICAL APPLICATIONS



LD083005



WF211305

THE VALID DATA WILL BE LATCHED TO THE DAC UNTIL UPDATED WITH E_2 PULSE.
TIMING WILL DEPEND ON THE PROCESSOR USED.

Figure 10. Interfacing 10-Bit DAC With 8-Bit Microprocessor