

Add Suffix F for TO-86 ceramic package (Case 607). Suffix L for TO-116 ceramic package (Case 632). Suffix P for TO-116 plastic package (Case 605) MC7460 only.



This device consists of two 4-input OR expanders for use with the AND-OR-INVERT gates. A maximum of four expander gates can be added to the MC5450/7450 or MC5453/7453 expandable gates without seriously affecting their operation.





SWITCHING TIME TEST CIRCUIT AND WAVEFORMS

ELECTRICAL CHARACTERISTICS

the same manner. Further, test procedures are shown for only one input of the ex-pander being tested. To complete testing, sequence through remaining inputs. Test procedures are shown for only one expander. The other expander is tested in

> 20 12 [2] Z1______ Collector

۷ 8 υ 0

[3] 13 2 [2] [9] [2] [8]

-3

FLAT DIL Pkg Pkg Pin Pin

Emitter Emitter

4

Gnd = Pin 7 [11] $V = V_{CC} = Pin 14 [4]$

	6												TES	T VOLTA	VGE VAL	TEST VOLTAGE VALUES (All Temperatures)	Tempt	srature	s)					
	[10]	 0)	Collector	9 13				Ohms		di la			1.2.1	Volts								
	.								R _{EX 1}	REX 2	V _{IL}	HI	Чини	V _{R1}	V _{R2}	V _{th 1}	V _{th 0}	20	V _{o2}	V03	V _{cc} V	V _{ccL} V	V _{ccH}	
								MC5460	1.2 k*	* 1.1 kţ	0.4	2.4	5.5	4.5	5.0	2.0	0.8	4.5	1.0 0.85	. 85	5.0 4	4.50 5	5.50	Pin 7[11] is
	1			and the second			-	MC7460	1.2 k*	* 1.1 kt	0.4	2.4	5.5	4.5	5.0	2.0	0.8	4.5	1.0	0.85	5.0 4	4.75 5	5.25	all tests in
		Pin IInder	MC546 -55	460 Test Limit 5 to +125°C	60 Test Limits to +125°C		MC7460 Test Limits 0 to +70°C	· Limits)°C					TEST V	OLTAGE	APPLIE	FEST VOLTAGE APPLIED TO PINS LISTED BELOW	NS LIS	TED BE	IOW:	Ī				addition to the pins listed below:
Characteristic	Symbol	Test	Min	Max	Unit	Min	Max	Unit	REX 1	REX 2	V _{IL}	< HI	VIHH	V _{R1}	V _{R2}	V _{th 1}	V _{th 0}	Voi	V ₀₂	V ₀₃	V _{cc} \	V _{ccL} V	V _{CCH}	Gnd
Input Forward Current	IF	В	2.00	-1.6	mAdc	1	-1.6	mAdc			В		- 1 ²	A,C,D			1	1	1			1	v	1
Leakage Current	IRI	В		40	μAdc	1	40	μAdc			1	В	1	1		1	î	Û.	1			,	Λ	A,C,D
	I _{R2}	B		1.0	mAdc	1	1.0	mAdc			ιć.	1	В	1	1	. 1	1		1	2.4			>	A,C,D
Output Output Voltage	∧ ^{OT} ↓	Z0,Z1		0.4	Vdc	- 512. 	0.4	Vdc		ZO		1	20 20 1. (. 34)			A,B,C,D		· · ·	Z1		,	Λ	,	
Leakage Current	ICEX	Z0	-	150\$	μAdc	1	270\$	μAdc	Z1		,	1	1 -	A,C,D		2	В	ZO	-	1	-	Λ	. 1	•
Drive Current	IDR	Z1	-0.3\$		mAdc	-0.43\$		mAdc		1	1	1 - 3	1	Ĩ.		A,B,C,D	,		Z1	5	1	A	1.	-
Power Requirements (Total Device) Power Supply Drain	Ipdl	Δ		4.0	mAdc		4.0	mAdc			12			1.		1 			1	IZ	1		>	All Inputs
	ЧДД	۸	•	2.5	mAdc		2.5	mAdc	1	•	•	- 1		i.,	All Inputs			• **	÷.,	DZ .	1	• •	Δ	-
Switching Parameters									Pulse In										inter a la se					
Turn-On Delay	t pd-	#	1	20**	us		20**	su	B		1	A,C,D		1			.1	į,	. T	ı	Λ	,	1	1
Turn-Off Delay	t pd+	*	1777 1777 2777	34**	su	•	34**	su	æ		•	A,C,D	1	1		-	1	1	1	1	Δ	1	1	

\$Tested only at low temperature limit 1: e., at -55°C for MC5460, at 0°C for MC7460. **Tested only at 25°C; times include delay of expandable gate.

 $^{\dagger}\mathrm{V}_{\mathrm{OL}}$ measured between pins 11 and 12.

*Resistor to ground. [‡]Resistor to VCCL. #See test circuit.

MC5460, MC7460 (continued)