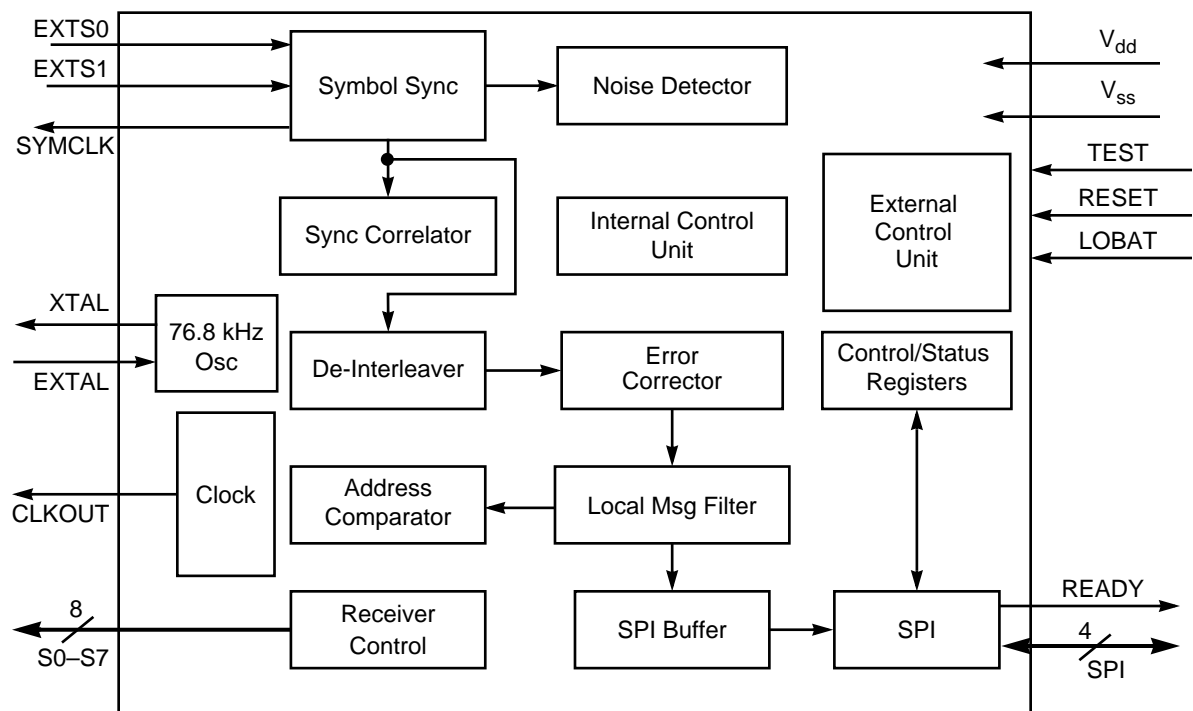


MC68175

Product Preview MC68175 FLEXchip™ IC

FLEX™ protocol is a multi-speed, high-performance protocol adopted by leading service providers worldwide as a de facto paging standard. FLEX protocol gives service providers the increased capacity, added reliability, and enhanced pager battery performance they need today. It also provides an upward migration path to the service provider that is completely transparent to the end user.

The MC68175 FLEXchip IC is part of a total solution available from Motorola for providing FLEX capabilities in a low-power, low-cost system. The FLEXchip simplifies implementation of a FLEX paging device by interfacing with any of several off-the-shelf paging receivers, such as the MC13150 or MC3374, and any of several off-the-shelf host microcontroller / microprocessors. The primary function of the FLEXchip is to process information received and demodulated from a radio paging channel, select messages addressed to the paging device, and communicate the message information to the host. The host interprets the message information in an appropriate manner (numeric, alphanumeric, binary, etc.) and handles all the I/O activity. The FLEXchip IC also operates the paging receiver in an efficient power consumption mode and enables the host to operate in a low power mode when message information for the paging device is not being received.



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Figure 1 FLEXchip IC Functional Block Diagram

This document contains information on a product under development. Motorola reserves the right to change or discontinue this product without notice.

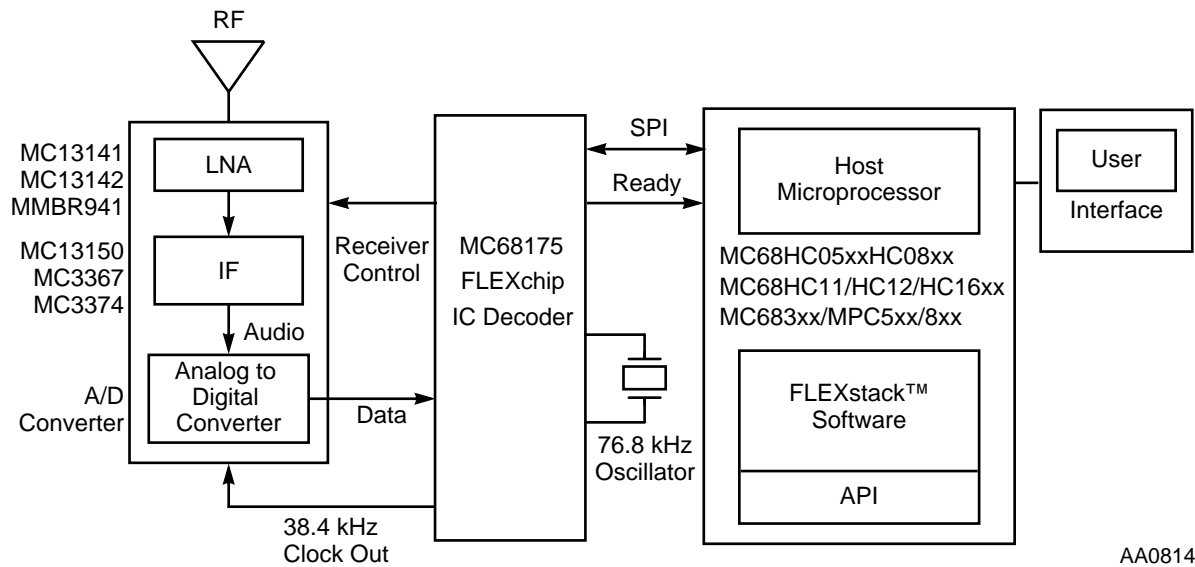
FLEXchip FEATURES

- FLEX paging protocol signal processor
- Sixteen programmable user address words
- Sixteen fixed temporary addresses
- 1600, 3200, and 6400 bits per second (bps) decoding
- Any-phase decoding
- Uses standard Serial Peripheral Interface (SPI) in Slave mode
- Wide operating voltage range from 3.3 V down to 1.8 V
- Allows low current Stop mode operation of host processor
- Highly programmable receiver control
- Real time clock time base
- FLEX fragmentation, and group messaging support
- Real time clock over-the-air update support
- Compatible with synthesized receivers
- Low battery indication (external detector)
- 32-pin Thin Quad Flat Pack (TQFP) package
- Operating temperature range 0° to +70°C (32° to 158°F)

FLEXchip SYSTEM DESIGN

The FLEXchip IC connects to a receiver capable of converting a 4-level audio signal into a 2-bit digital signal (see **Figure 2**). The FLEXchip IC has eight receiver control lines used for warming up and shutting down a receiver in stages. The FLEXchip IC has dual bandwidth control signals for two post-detection filter bandwidths for receiving the two symbol rates of the FLEX signal. The FLEXchip IC has the ability to detect a low battery signal during the receiver control sequences. It interfaces to a back-end host MCU through a standard SPI. It has a 38.4 kHz clock output capable of driving other devices. It has a 1 minute timer that offers low power support for time of day function on the host.

All data communicated between the FLEXchip IC and the host MCU is transmitted on the SPI in 32-bit packets. Each packet consists of an 8-bit ID followed by 24 bits of information. The FLEXchip IC uses the SPI bus in Full Duplex mode. In other words, whenever a packet communication occurs, the data in both directions is valid packet data.



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Figure 2 FLEXchip System Block Diagram

FLEX PROTOCOL

FLEX System Software from Motorola is a family of software components for building world-class products incorporating messaging capabilities. FLEXstack™ Software is specifically designed to support the FLEXchip IC. FLEXstack Software runs on a product's host processor and takes care of communicating with the FLEXchip IC and fully interpreting the code words that are passed to the host from the FLEXchip IC.

FLEX protocol can give the service provider a capacity increase of greater than 10 times over a POCSAG 512 bps system. FLEX protocol can operate at three different speeds, allowing a system to initially run more slowly, and then increase speed as system demands increase, while still using the same pager units. FLEX protocol provides accurate message delivery by offering protection from fading conditions, such as bridges or overpasses. It provides 12 times the fading protection of POCSAG 1200, and 24 times the fading protection of POCSAG 2400. FLEX protocol is a synchronous time slot scheme, designed to increase the battery life of pagers. Instead of sending out messages at random, all paging data intended for a particular pager is scheduled into a pre-defined time slot. This allows a FLEX pager to selectively decode one or more frames over each four minute FLEX cycle, so that the pager does not waste its battery life decoding data intended for other pagers.

PRODUCT DOCUMENTATION


The manual listed in **Table 1** is required for a complete description of the MC68175 and is necessary to properly design with the part. Documentation is available from a local Motorola distributor, a Motorola semiconductor sales office, a Motorola Literature Distribution Center, or through the Motorola DSP home page on the Internet (the source for the latest information).

Table 1 Additional Documentation

Document Name	Description	Order Number
MC68175 Technical Data	MC68175 features list and physical, electrical, timing, and package specifications	MC68175/D

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