



TRUTH TABLE

MODE	INPUT			OUTPUT
	CE	WE	D	
Write "0"	L	L	L	L
Write "1"	L	L	H	L
Read	L	H	ϕ	Q
Disabled	H	ϕ	ϕ	L

ϕ = Don't Care.

$P_D = 625$ mW typ/pkg
Access Time = 10 ns typ

64-Bit Register File (RAM)

The MCM10145 is a 64-Bit RAM organized as a 16 x 4 array. This organization and the high speed make the MC10145 particularly useful in register file or small scratch pad applications. Fully decoded inputs, together with a chip enable, provide expansion of memory capacity. The Write Enable input, when low, allows data to be entered; when high, disables the data inputs. The Chip Enable input when low, allows full functional operation of the device; when high, all outputs go to a low logic state. The Chip Enable, together with open emitter outputs allow full wire-ORing and data bussing capability. On-chip input pulldown resistors allow unused inputs to remain open.